The Versatile Commodore Emulator

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1 About VICE

VICE is the one and only *Versatile Commodore Emulator*. It provides emulation of the Commodore C64, C64DTV, C128, VIC20, PET, PLUS4, SCPU64 and CBM-II computers within a single package. The emulators run as separate programs, but have the same user interface, share the same settings and support the same file formats.

Important notice: If you have no idea what a Commodore 8-bit computer is, or have questions about how these machines are used, how the file formats work or anything else that is not strictly related to VICE, you should read the appropriate FAQs *first*, as that kind of information is not available here. See Chapter 20 [Contacts], page 546. for information about how to retrieve the FAQs.

All the emulators provide an accurate 6502/6510 emulator, with emulation of all the opcodes (both documented and undocumented ones) and accurate timing. Unlike other emulators, VICE aims to be cycle accurate; it tries to emulate chip timings as precisely as possible and does so *efficiently*.

Please do *not* expect the C64DTV, C128, PET, PLUS4, SCPU64 and CBM-II emulators to be as good as the C64 or VIC20 one, as they are still under construction.

Notice: This documentation is written for the Unix release of VICE, but is slowly being made universal.

1.1 Emulator features

1.1.1 C64 emulator features

As of version 2.3, two C64 emulators are provided: '`x64`' (fast) and '`x64sc`' (accurate). As of version 3.4 '`x64`' will no more get built by default and is not contained in the default binary packages.

The fast C64 emulator, called '`x64`', features a fairly complete emulation of the VIC-II video chip: sprites, all registers and all video modes are fully emulated. The emulation has been fully cycle-accurate since version 0.13.0.

The accurate C64 emulator, called '`x64sc`', features a cycle-based and pixel-accurate VIC-II emulation. This requires a much faster machine than the old '`x64`'.

A rather complete emulation of the SID sound chip is also provided. All the basic features are implemented as well as most of the complex ones including synchronisation, ring modulation and filters. There are two emulators of the SID chip available: first is the "standard" VICE emulator, available since VICE 0.12; the second is Dag Lem's reSID engine. The reSID engine is a lot more accurate than the standard engine, but it is also a lot slower, and only suitable for faster machines.

Naturally, also both CIAs (or VIAs, in some cases) are fully emulated and cycle accurate.

1.1.2 C64DTV emulator features

The C64DTV emulator, called '`x64dtv`', features emulation of C64DTV revisions 2 and 3. The emulator is under construction, but most of the DTV specific features are already supported (with varying accuracy).

Video cache is disabled by default as it currently doesn't work with some of C64DTV's new video modes. The new video modes have a simple "fake" video cache implementation that may give incorrect results and decreased performance.

1.1.3 C128 emulator features

The C128 emulator, called '`x128`', features a complete emulation of the internal MMU (*Memory Management Unit*), 80 column VDC screen, fast IEC bus emulation, 2 MHz mode, Z80 emulation plus all the features of the C64 emulation.

1.1.4 VIC20 emulator features

The VIC20 emulates all the internal hardware, including the VIA chips. The VIC-I video chip is fully emulated except NTSC interlace mode, so most graphical effects will work correctly.

The VIC20 emulator allows the use of the VIC1112 IEEE488 interface. You have to enable the hardware (by menu, resource, or commandline option) and then load the IEEE488 ROM (see for example `http://www.funet.fi/pub/cbm/schematics/cartridges/vic20/ieee-488/325329-04.bin`, but you have to double the size to 4KiB for now). The IEEE-488 code is then started by `SYS45065`.

1.1.5 PET emulator features

The PET emulator emulates the 2001, 3032, 4032, 8032, 8096, 8296 and SuperPET (MicroMainFrame 9000) models, covering the whole series. The hardware is pretty much the same in each and that is why one single program is enough to emulate all of them. For more detailed information about PET hardware please refer to the PETDoc (`https://sourceforge.net/p/vice-emu/code/HEAD/tree/techdocs/PET/PETdoc.txt?format=raw`) file.

Both the 40 column and 80 column CRTC video chips are emulated (from the 4032 onward), but a few of the features are not implemented yet (numbers of rasterlines per char and lines per screen). Fortunately, they are not very important for average applications.

The PET 8096 is basically a PET 8032 with a 64KiB extension board which allows remapping the upper 32KiB with RAM. You have to write to a special register at `$fff0` to remap the memory. The PET 8296 is a 8096 but with a completely redesigned motherboard with 128KiB RAM in total. Of the additional 32KiB RAM you can use only some in blocks of 4KiB, but you have to set jumpers on the motherboard for it. VICE uses the command line options '`-petram9`' and '`-petramA`' instead. Also, the video controller can handle a larger address range. The PET 8x96 model emulations run the Commodore LOS-96 operating system - basically an improved BASIC 4 version with up to 32KiB for BASIC text and 32KiB for variables. See PETDoc (`https://sourceforge.net/p/vice-emu/code/HEAD/tree/techdocs/PET/PETdoc.txt?format=raw`) for more information.

The PET 8296D is an 8296 with built-in 8250 low-profile dual disk drive.

The PET 8296GD is an 8296D with additionally a "HiRes Emulator" (HRE). This is a cheaper version of a "HRG" hi-res board which was based on Thomson chips. This version instead uses no additional hardware support apart from some memory mapping tricks. It has supporting software in the hre-*.bin rom files.

The SuperPET also is a PET 8032 with an expansion board. It can map 4KiB at a time out of 64KiB into the `$9***` area. Also it has an ACIA 6551 for RS232 communication.

The 6809 CPU that is built into the SuperPET is now emulated, since release 2.4, including the 6702 dongle chip.

The Super-OS-9 MMU expansion, developed by TPUG (Toronto PET Users Group) is also emulated.

The PET computers came with three major ROM revisions, so-called BASIC 1, 2 and 4, all of which are provided. The PET 2001 uses the version 1, the PET 3032 uses version 2, and the others use version 4. The 2001 ROM is horribly broken with respect to IEEE488 (they shipped it before they tested it with the floppy drive, so only tape and printers worked. Therefore the emulator patches the ROM to fix the IEEE488 routines.

As well as other low-level fixes the 2001 patch obtains the load address for a program file from the first two bytes of the file. This allows the loading of both PET2001-saved files (that have $0400 as their load address) and other PET files (that have $0401). The PET2001 saves from $0400 and not from $0401 as other PETs do.

Moreover, the secondary addresses used are now `0` and `1` for load and save, respectively, and not arbitrary unused secondary addresses.

To select which model to run, specify it on the command line with the `-model MODEL` option, where `MODEL` can be one of a list of PET model numbers, all described in see Section 7.7.1 [PET model], page 208,

1.1.6 CBM-II emulator features

The CBM-II emulator emulates several types of CBM-II models. Those models are known under different names in the USA and Europe. In the States they have been sold as `B128` and `B256`, in Europe as `CBM 610`, `CBM 620` (low-profile case) or `CBM 710` and `CBM 720` (high-profile case with monitor). In addition to that now an experimental C510 emulation is included. The C510 (also known as P500) is the little brother of the C600/700 machines. It runs at roughly 1 MHz and, surprise, it has a VIC-II instead of the CRTC. Otherwise the different line of computers are very similar.

These computers are prepared to take a coprocessor board with an 8088 or Z80 CPU. Indeed there are models `CBM 630` and `CBM 730` that supposedly had those processors. However these models are not emulated.

The basic difference is the amount of RAM these machines have been supplied with. The `B128` and the `CBM *10` models had 128KiB RAM, the others 256KiB. This implies some banking scheme, as the 6502 can only address 64KiB. And indeed those machines use a 6509, that can address 1 MiB of RAM. It has 2 registers at addresses 0 and 1. The indirect bank register at address 1 determines the bank (0-15) where the opcodes `LDA (zp),Y` and `STA (zp),Y` take the data from. The exec bank register at address 0 determines the bank where all other read and write addresses take place.

The business line machines (C6xx/7xx) have the RAM in banks 1-2, resp. 1-4. All available banks are used for BASIC, where program code is separated from all variables, resp. from normal variables, strings and arrays that are distributed over other banks. The C510 instead has RAM in banks 0 and 1, and uses bank 1 for program and all variables. Bank 0, though, can be accessed by the VIC-II to display graphics.

Many models have been expanded to more than the built-in memory. In fact some machines have been expanded to the full 1MiB. Bank 15 is used as system bank, with only little RAM, and lots of expansion cartridge ROM area, the I/O and the kernal/basic ROMs.

Some models have been modified to map RAM into the expansion ROM area. Those modifications can be emulated as well.

The different settings are described in see Section 7.8.1 [CBM-II model], page 220.

1.1.7 SCPU64 emulator features

The XSCPU64 emulator is a simulation of a C64 equipped with a SuperCPU64 V2B. Features:

- 20 MHz asynchronous single cycle 65816 CPU core with proper dummy and invalid cycle handling.
- 128 KiB static RAM, 0-16 MiB SIMM RAM, 64-512 KiB EPROM emulated and their respective timing details.
- All RAM optimization configurations supported with write buffer.
- I/O area access delays, write through to SRAM implemented.
- Memory mappings including cartridge and boot memory map and kernal shadow.
- Hardware registers and switches implemented.
- Replacement SCPU64 ROM compatible with the original to avoid distribution problems
- It's using the single cycle VICII core for accurate simulation

Still to do:

- Measure and verify VICII interrupt phase shift
- Measure and verify BA phase shift
- SIMM RAM extra 7.5 cycle refresh delay every 10us missing.
- CPU NMI support for "reset" button

The emulation is quite accurate but not perfect. If you code something timing intensive using this simulation please always check it on real hardware to avoid bad surprises.

The hardware itself is asynchronous in nature, therefore caution must be taken to not do long timing loops without synchronization in 20 MHz mode. Also don't squeeze out the last remaining cycles without leaving a safety buffer. Synchronization points can be created by doing I/O reads or writes and leaving a few hundred cycles left each frame will not hurt.

Otherwise it can happen that the code is running on this version of VICE or my SCPU64 V2+C128D perfectly but nowhere else due to manufacturing variations and frequency drifts.

1.2 The keyboard emulation

There are two fundamentally different ways of emulating the keyboard in VICE, both of which have their individual shortcomings and strengths:

Symbolic Mapping

The default way (*symbolic mapping*) is to map every key - as far as possible - in a way that inside the emulation the symbol that is printed on the host key is produced. For example, if you press `*`, which is bound to *`Shift-8`* on a U.S. keyboard, in the C64 emulator, the emulated machine will have just the *unshifted* `*` key pressed (as `*` is unshifted on the C64 keyboard). Likewise, pressing `'` on the same U.S. keyboard without any shift key will cause the combination *`Shift-7`* to be pressed in the emulated C64. This way, it becomes quite

obvious what keys should be typed to obtain all the symbols. *The key printed on the host keyboard will be pressed in the emulator.* (A not so obvious exception to this rule are keypads attached to the joystick port - since various different devices with different layouts exist, these are always mapped "positional".)

Note that while these mappings will allow easier typing if you are used to your host keymap, it might cause various problems in the emulation and not all emulated keys are accessible. If you encounter such problems, try the positional mapping.

- Some keys really need to be mapped specially regardless (those that do not exist on a PC keyboard). Some examples are the `Commodore` key, `RUN/STOP`, `Clear/Home`. The exact mapping depends on your host layout, but should follow mostly the layouts shown below. If in doubt, you can read the keyboard mapping files.
- the emulator has to "de-shift" (remove the shift modifier) from some keys. For example, if you want to get a ":" on a german keyboard, you have to press "." and shift simulaneously. Then, the emulator sees the events "shift pressed" and ":" pressed. At the moment you have pressed shift, the emulator does not know what key you are using next (or even if you are actually going to use another key before releasing shift again), so it has to deliver this state to the keyboard matrix. If you press ".", then shift, to achive a ":", the emulator has to take back the shift key although it is still pressed, as the ":" is an unshifted key in the c64/c128 keyboard matrix.
- A similar problem exists when you press a key that is unshifted on the host keyboard, and the emulator must "shift" (add the shift modifier) for the emulated keyboard. For example on a US keyboard the brackets ([,]) are such a case. Unlike what would happen on the real C64 when typing, VICE "presses" the `Shift` key *together* with the key to shift when the `Shift` must be forced. In most cases this should work fine, but some keyboard routines are quite picky (eg x128 in CP/M mode) and tend not to recognize the shift key because of this. For instance, ***F6*** (which on the real C64 is obtained with ***Shift*** + ***F5***) could be recognized as ***F5***. In that case, use the shift key manually (i.e., type ***Shift*** + ***F5*** in the example).

Yes, we know this is a bug. Unfortunately it is less than trivial to fix 100%. If you encounter any problems, please use a positional keymap as a workaround - that should always work as expected.

PROs:

- Mostly intuitive typing if your muscle memory is conditioned strong to your host keyboard.

CONs:

- Does not preserve the position of the keys.
- Due to how the key mapping may add or remove the SHIFT flag from individual keys, strange side effects may occur (see above).
- SHIFT/LOCK is added to the emulated keys - which may give unexpected results depending on your host OS and keymap (It is really emulating the SHIFT-LOCK key and not working as a CAPS-LOCK like in your host OS anymore).

Positional Mapping

For the *positional mapping*s first all keys are mapped at their exact positions, as far as possible, and then the remaining few (usually 2 or 3) will be mapped to other, yet unused,

keys. **This is the recommended mapping for playing games or other programs that require keys+modifiers (shift/control/cbm) to work exactly like on the emulated machine.** This way the keyboard is more comfortable to use in those programs (such as some games) that require the keys to be in the correct positions. On the other hand it can be quite confusing if you are not very familiar with the original emulated keyboards. Also not all keys can be mapped exactly this way either, which means some of them still need to be mapped to other keys (see further down).

One perhaps not so obvious exception to the above is the mapping of the cursor-up (mapped to arrow-up) and cursor-left (mapped to arrow-left) keys. These are still mapped "symbolic" - which means the virtual shift might lead to odd unexpected behaviour. Because of this, if you require the shift keys to work exactly like on the real hardware, please use shift+arrow-down and shift+arrow-right - just like you would do on the real thing.

Like with symbolic mapping, some keys really need to be mapped specially regardless (eg `RUN/STOP`). The exact mapping depends on your host layout, but should follow mostly the layouts shown below. If in doubt, you can read the keyboard mapping files.

PROs:

- Mostly intuitive typing if your muscle memory is conditioned strong to your (eg) C64 keyboard.
- Does preserve the position of (most of) the keys.
- Natural SHIFT and SHIFT/LOCK behaviour

CONs:

- Weird to use if you are not familiar with the emulated (eg) C64 keyboard.

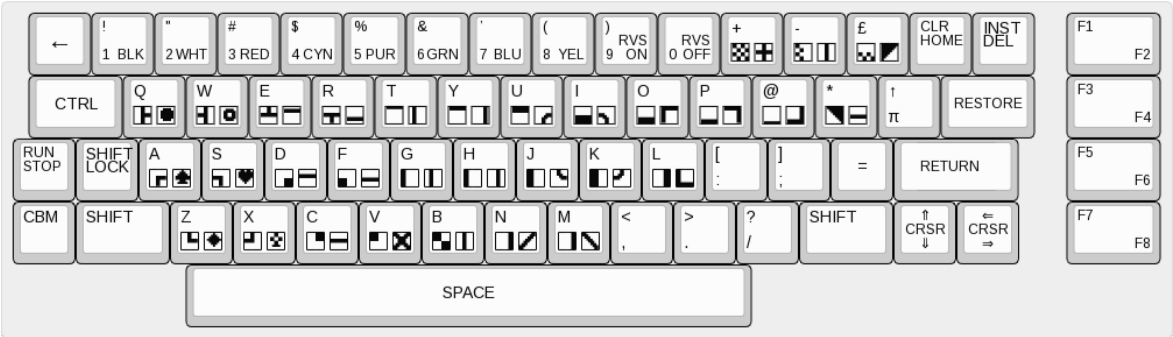
Notes

We depend a lot on your support to improve the keyboard maps, as we can not test all emulators in all possible configurations and using all host keyboard mappings. Please report any problems to us so we can fix them!

If you experience problems with 'accent' keys such as acute, grave, tilde, circumflex, diaresis (and possibly more/other, depending on your host keyboard layout) try switching to a "no deadkeys" layout in your OS. Note that **currently SDL requires using a "no deadkeys" keyboard layout!** In any case, please also report these problems so we can fix them!

For more information on making your own keymaps (or helping to fix/update the existing ones), see see Section 3.2 [Keymap files], page 32.

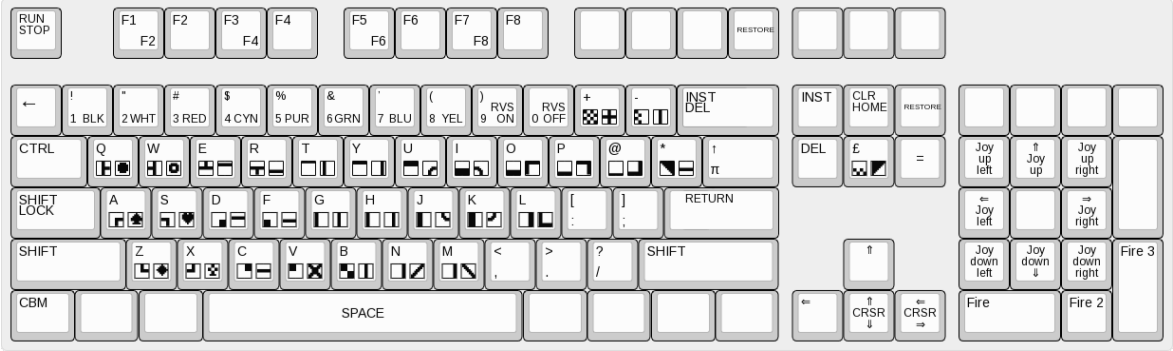
1.2.1 C64 & VIC20 Keyboard Map



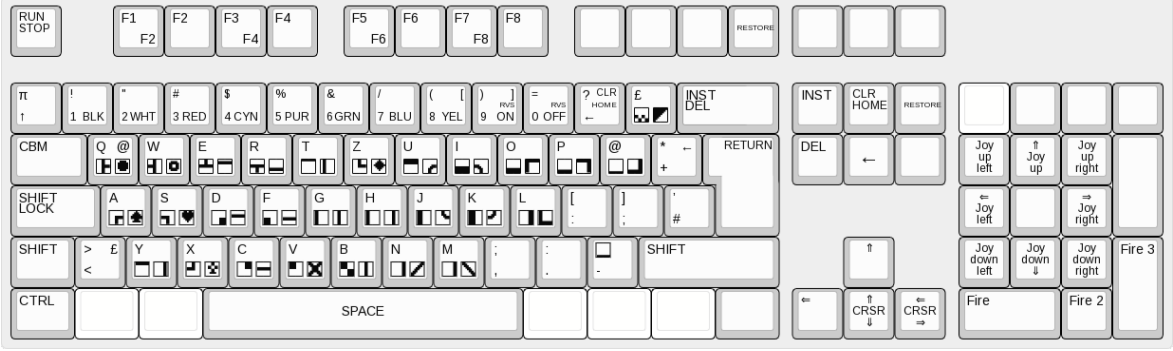
This mapping applies to the C64, SCPU, VIC20 and DTV emulators.

1.2.1.1 Symbolic (US)

1.2.1.2 Positional (US)

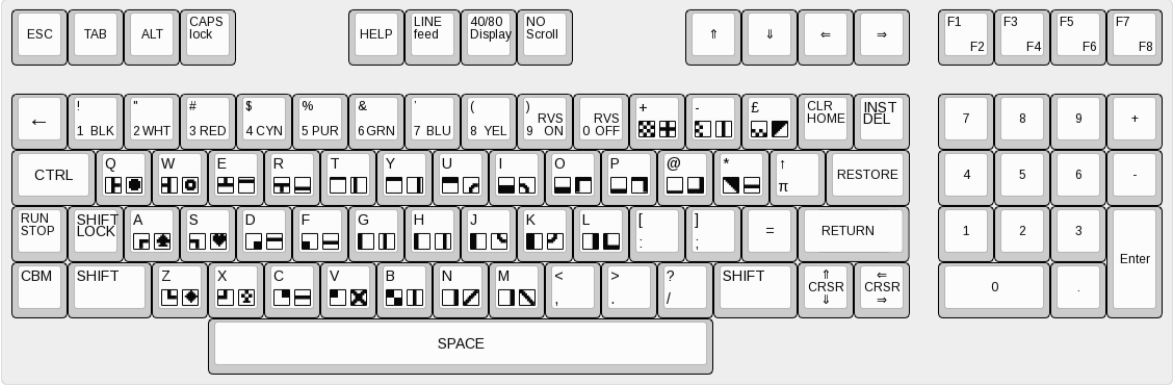


1.2.1.3 Symbolic (DE)



1.2.1.4 Positional (DE)

1.2.2 C128 Keyboard Map

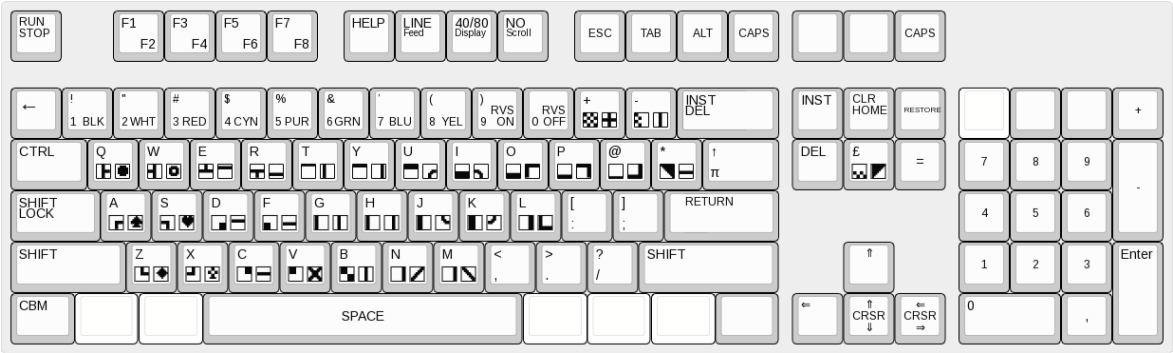


This mapping applies to the C128 emulator. It is for a large part identical with the respective C64 mapping, plus the added function- and extra keys.

Note that to use the numpad, you will have to disable the "allow keyset joysticks" setting.

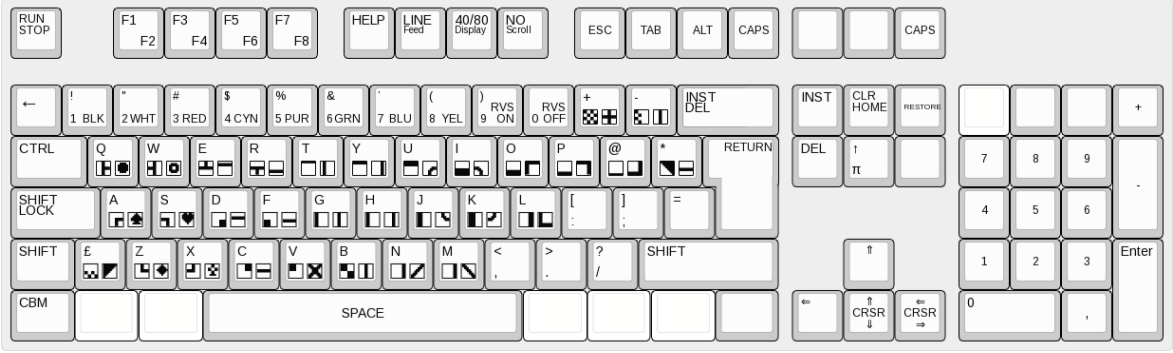
1.2.2.1 Symbolic (US)

1.2.2.2 Positional (US)

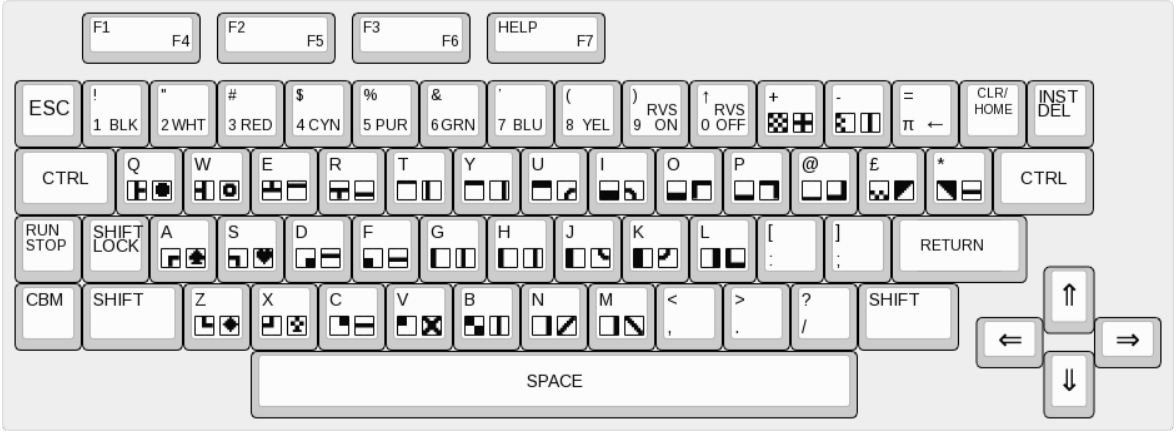


1.2.2.3 Symbolic (DE)

1.2.2.4 Positional (DE)



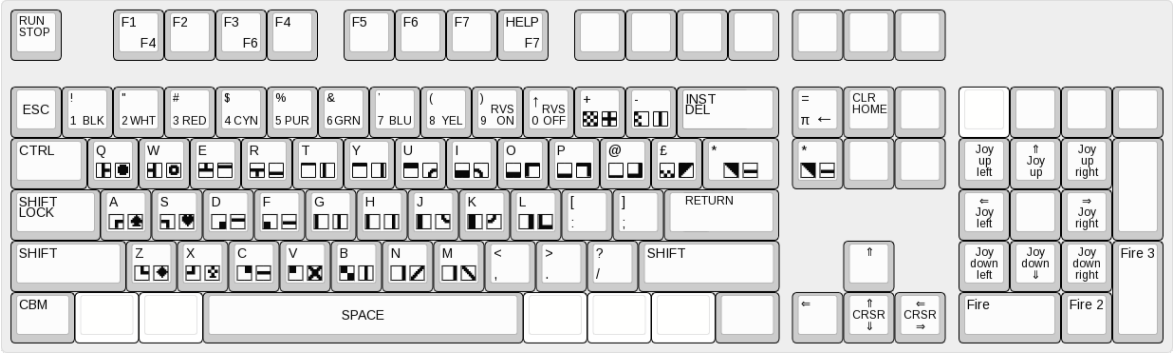
1.2.3 C16 & Plus4 Keyboard Map



This mapping applies to the Plus4 emulator

1.2.3.1 Symbolic (US)

1.2.3.2 Positional (US)

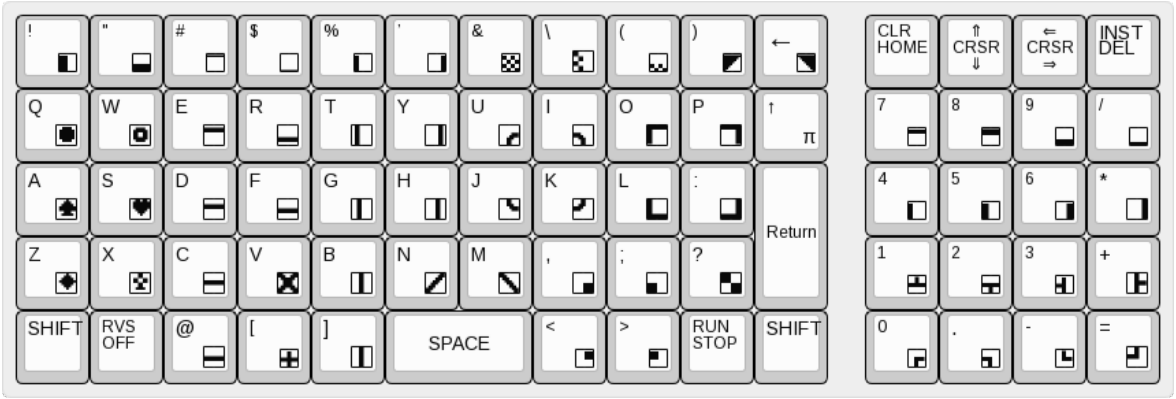


1.2.3.3 Symbolic (DE)

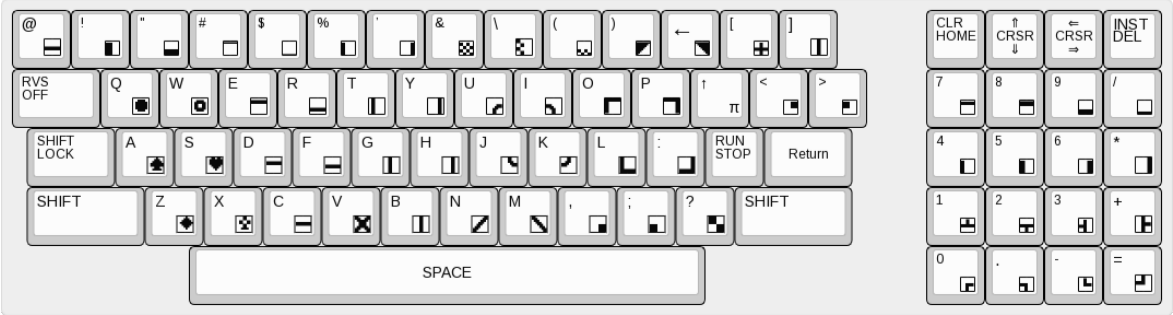
1.2.3.4 Positional (DE)

1.2.4 PET Graphics Keyboard Map

The PET2001 uses the original graphics keyboard made from "chiclet" keys:



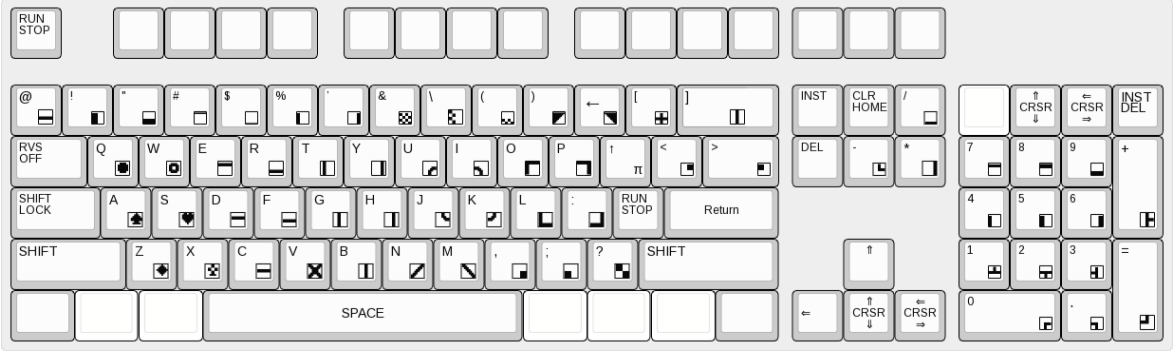
The 3032 and 4032 use "real" graphics keyboard, which is based on the same keyboard matrix and looks like this:



For practical reasons, there is no extra keymap for the chiclet keyboard - any attempt to somehow reproduce that would end up too similar to what we are using now for the graphics keyboard anyway:

1.2.4.1 Symbolic (US)

1.2.4.2 Positional (US)

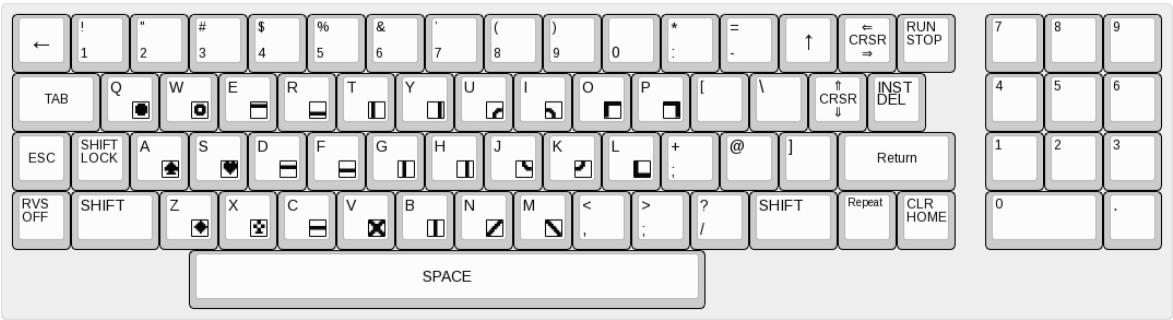


1.2.4.3 Symbolic (DE)

1.2.4.4 Positional (DE)

1.2.5 PET Business Keyboard Map

This Layout is used for the "big" PET models, like the 8032. The matrix is not compatible to the graphics keyboard:

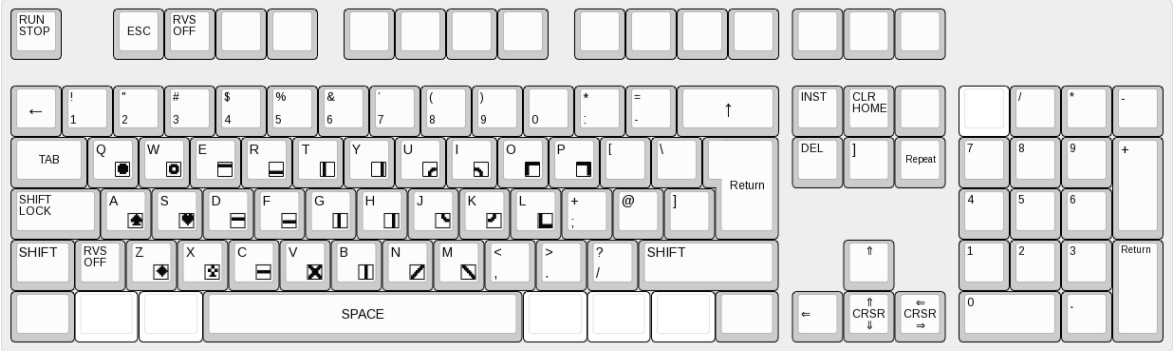


1.2.5.1 Symbolic (US)

1.2.5.2 Positional (US)

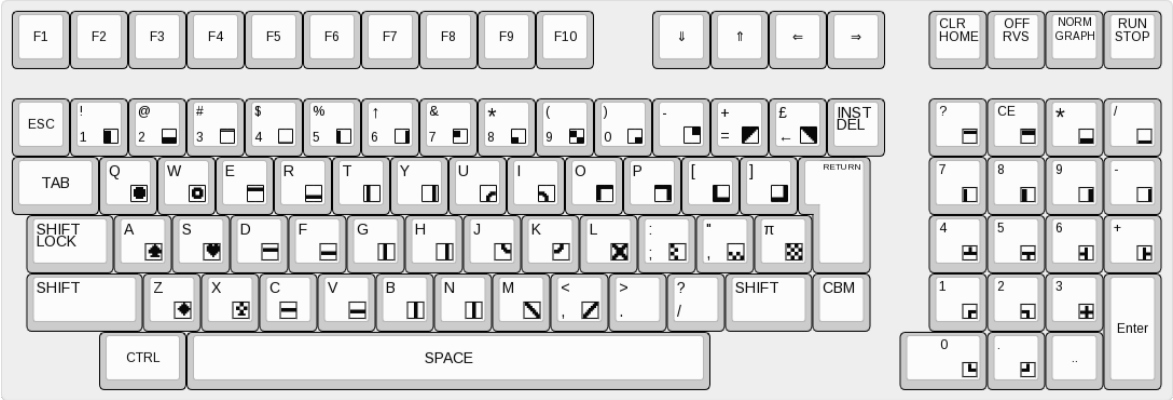
1.2.5.3 Symbolic (DE)

1.2.5.4 Positional (DE)



1.2.6 CBM2 Keyboard Map

This Layout is used for all CBM2 machines:



1.2.6.1 Symbolic (US)

1.2.6.2 Positional (US)

1.2.6.3 Symbolic (DE)

1.2.6.4 Positional (DE)

1.3 The joystick emulation

Joysticks can be emulated both via the keyboard and via a real joystick connected to the host machine.

There are two keyboard layouts for joystick use, known as *numpad* and *custom*.

The *numpad* layout uses the numeric keypad keys, i.e., the numbers `1...9` which emulate all the directions including the diagonal ones; `0` emulates the fire button.

The *custom* layout is configurable to your liking.

For some of the emulators there can be extra joysticks besides the built-in joystick ports, the following list provides the mappings for the currently supported joystick/joypad ports:

The internal joystick port 1 [x64/xscpu64/x64dtv/x128/xvic/xplus4/xcbm5x0] is mapped to joystick device #1.

The internal joystick port 2 [x64/xscpu64/x64dtv/x128/xplus4/xcbm5x0] is mapped to joystick device #2.

The CGA userport joystick adapter ports 1 and 2 (x64/xscpu64/x128/xvic/xpet/xcbm2) are mapped to extra joystick devices #1 & #2 respectively.

The PET userport joystick adapter ports 1 and 2 (x64/xscpu64/x128/xvic/xplus4/xpet/xcbm2) are mapped to extra joystick devices #1 & #2 respectively.

The C64DTV HUMMER userport joystick adapter port (x64/xscpu64/x128/xvic/xplus4/xpet/xcbm2) is mapped to extra joystick device #1.

The OEM userport joystick adapter port (x64/xscpu64/x128/xvic/xplus4/xpet/xcbm2) is mapped to extra joystick device #1.

The HIT userport joystick adapter ports 1 & 2 (x64/xscpu64/x128) are mapped to extra joystick devices #1 & #2 respectively.

The KingSoft userport joystick adapter ports 1 & 2 (x64/xscpu64/x128) are mapped to extra joystick devices #1 & #2 respectively.

The StarByte userport joystick adapter ports 1 & 2 (x64/xscpu64/x128) are mapped to extra joystick devices #1 & #2 respectively.

The Petscii userport SNES pad adapter port (x64/xscpu64/x128/xvic/xcbm2) is mapped to extra joystick device #1.

The Superpad64 userport SNES pad adapter ports 1, 2, 3, 4, 5, 6, 7 & 8 (x64/xscpu64/x128/xvic/xcbm2) are mapped to extra joystick devices #1, #2, #3, #4, #5, #6, #7 & #8 respectively.

The Inception joystick adapter ports 1, 2, 3, 4, 5, 6, 7 & 8 (x64/x64sc/xscpu64/x64dtv/x128/xcbm5x0/xvic) are mapped to extra joystick devices #1, #2, #3, #4, #5, #6, #7 & #8 respectively.

The MultiJoy joystick adapter ports 1, 2, 3, 4, 5, 6, 7 & 8 (x64/x64sc/xscpu64/x64dtv/x128/xcbm5x0) are mapped to extra joystick devices #1, #2, #3, #4, #5, #6, #7 & #8 respectively.

The SpaceBalls joystick adapter ports 1, 2, 3, 4, 5, 6, 7 & 8 (x64/x64sc/xscpu64/x64dtv/x128/xcbm5x0) are mapped to extra joystick devices #1, #2, #3, #4, #5, #6, #7 & #8 respectively.

The Ninja SNES pad adapter ports 1, 2 & 3 (x64/x64sc/xscpu64/x64dtv/x128/xcbm5x0/xplus4) are mapped to extra joystick devices #1, #2 & #3 respectively.

The Stupid Pet Tricks joystick adapter port (x64/x64sc/xscpu64/x128/xcbm2/xpet/xvic) is mapped to port extra joystick device #3.

The ProtoPad SNES pad adapter port is mapped to the same port it is attached to.

The Trap-Them SNES pad adapter port is mapped to the same port it is attached to.

The MicroFlyte analog joystick is mapped to the same port it is attached to.

The SID cartridge joystick port (xplus4) is mapped to extra joystick device #9.

1.4 The disk drive emulation

All the emulators support up to 4 external disk drives as devices 8, 9, 10 and 11. Each of these devices can emulate virtual Commodore 1541, 1541-II, 1571, 1581, 2031, 2040, 3040, 4040, 1001, 8050, 8250, and D9090/60 drives in one of the following ways:

- using disk images, i.e., files that contain a dump of all the blocks contained in a real floppy disk;
- accessing file system directories, thus giving you the use of files without having to copy them to disk images; this also allows you to read and write files in the `P00` format.
- accessing a real device connected to the host machine. This works using the OpenCBM library. You can get it from `https://spiro.trikaliotis.net/opencbm`. Make sure to pick the right library (32 or 64bit) for your system.
  - To use it in the emulators, go to the drive tab in the settings and choose either "Virtual (traps)" or "Virtual (bus)" and in the dropdown labeled "Virtual backend", choose "Real Drive (OpenCBM)". Note that this will not work with all emulators right now. Also note that using the real drives like this provides only very limited compatibility in practise - it is recommended to transfer the disks to images and use the images with an emulated drive in the emulator.
  - If you want to use OpenCBM with c1541:
    - Use "opencbm" as filename (instead of the disk image name).
    - With older linux drivers, that provide a device file, use that device file as filename (eg /dev/cbm).

When using disk images there are three available types of drive emulation. One of them the *virtual (traps)* emulation. It does *not* really emulate the serial line, but patches the kernal ROM (with the so-called *kernal traps*) so that serial line operations can be emulated via C language routines. This emulation is very fast, but only allows use of standard DOS functions (and not even all of them). For real device access it is required to enable this type of emulation.

The IEEE488 drives (2031, 2040, 3040, 4040, 1001, 8050, 8250, and D9090/60) do not use kernal traps. Instead the IEEE488 interface lines are monitored and the data is passed to the drive emulation. This is called *virtual (ieee- or iec-bus)* emulation. To use the IEEE488 disks on the C64, you need to enable the IEEE488 interface emulation. Only if the IEEE488 emulation is enabled, those drives can be selected.

This option also exists for the serial IEC disks (except on the VIC-20).

The third, most accurate, alternative is a *true drive* emulation. The Commodore disk drives are provided with their own CPU (a 6502 as the VIC20 and the PETs) and their own RAM and ROM. So, in order to more closely emulate its features, a complete emulation of this hardware must be provided and that is what the *hardware level* emulation does. When the *hardware level* emulation is used, the kernal routines remain unpatched and the serial line is fully emulated. The problem with this emulation is that it needs a lot of processing power, mainly because the emulator has to emulate two CPUs instead of one.

The PETs do not use a serial IEC bus to communicate with the floppy drive but instead use the parallel IEEE488 bus. This does *byte by byte* transfers, as opposed to the *bit by bit* transfers of the C64 and VIC20, so making it feasible to emulate the parallel line completely while emulating the drive at DOS level only. The IEEE488 line interpreter maps the drives

8-11 (as described above) to the IEEE488 disk units, and no kernal traps are needed. The same emulation of the Commodore IEEE488 bus interface is available for the C64 and the VIC20. With IEEE488 drives you can have true 2031 emulation at unit #8, and still have filesystem access at units #10 or #11, because monitoring the IEEE488 lines does not interfere with the true drive emulation.

The IEEE488 disk units 2040, 3040, 4040, 8050 and 8250 are Dual Drive Floppy Disks. This means that these devices handle two disks. The drives are numbered 0 and 1.

The Commodore 2040, 3040, 4040, 1001, 8050, 8250, and D9090/60 drives are so-called "old-style" disk drives. Their architecture includes not one, but two processors of the 6502 type, namely a 6502 for the file handling and communication with the PET (IP), and a 6504 (which is a 6502 with reduced address space) for the drive handling (FDC). Both processors communicate over a shared memory area. The IP writes commands to read/write blocks to this area and the FDC executes them. To make the emulation feasible, the FDC processor is not emulated cycle-exactly as a 6504, but simply by checking the commands and executing them on the host. This provides a fast FDC emulation, but disallows the sending the FDC processor commands to execute code. Applications where this is necessary are believed to be rather seldom. Only the format command uses this feature, but this is checked for.

The dual disk drive 2040 emulates one of the very first CBM disk drives. This drive has DOS version 1. DOS1 uses an own disk type, that is closely related to the 1541 disk image. Only on tracks 18-24 DOS1 disks have a sector more than 1541 disks. DOS1 disk images have the extension .d67.

The dual disk drives 3040 and 4040 use the same logical disk format as the VC1541 and the 2031. In fact, the 4040 was the first disk with DOS version 2. The 3040 emulated here originally was the same as 2040, only for the european 30xx PET series. As many of the original DOS1 disk drives were upgraded (a simple ROM upgrade!) to DOS2, I use the 3040 number for a DOS 2.0 disk drive, and 4040 for a revised DOS 2 disk drive. It is, however, not yet clear whether the disks here are write compatible to the 1541, as rumors exist that the write gap between sectors is different. But read compatible they are. As VICE emulates the FDC processor in C and not as 6504 emulation, this does not matter in VICE.

The drives 1001, 8050 and 8250 do actually have the very same DOS ROM. Only the code in the FDC is different, which is taken care of by VICE. So for all three of those disk drives, only `dos1001` is needed. The DOS version used is 2.7.

The D9090/60 is the only Commodore branded hard drive produced for the PET series computers, and were often used by C64 and C128 users for their significant storage capacity (29162/19441 free blocks). Just like the other IEEE drives before it, it uses a separate CPU as the FDC which in turn communicates with the SASI-to-ST506 bridge (which is controlled by an AM2910). The hardware design is very similar to the 8050/8250 drive.

Creative Micro Designs (CMD) produced the last drives for the Commodore 8-bit systems. They first released the hard drive (HD) line, and later the floppy drive (FD) line. The CMD HD series can support up to 4 GiB HDs with 255 separate partitions, while the CMD FD series can support up to 3.3 MB extended density floppy disks with 31 separate partitions. The FD series are also backwards compatible with 1581 media. The DOS for the FD series is stored on a ROM (`dos2000` and `dos4000`, the latest versions being 1.40).

The CMD HD uses a small boot ROM (`dosCMDHD`, the latest version is 2.80) which loads the primary DOS (latest is 1.92) off the HD itself. This allows for easy upgrades and

expandability. This is also the only drive to use the front panel buttons to control the mode of the drive on reset. There are three modes of operation: normal, configuration, and installation. VICE supports placing the drive in either of these modes through the "Reset" sub-menu on the status bar for the GTK3 interface, and the "Reset" menu in the SDL interface. When creating a new DHD image, simply create an EMPTY file and VICE will automatically place the drive in installation mode. The DOS will detect the drive as the size specified by "Drive#FixedSize" (or "-drive#fixedsize") which is 8GB by default. To use a specific sized disk, set this value to the maximum size, or set this value to "0" and set the image file on disk to the desired size (it should be a multiple of 512 bytes). Once the DOS is installed, the CMD "hd-tools" program can be used to configure various settings and partition the drive; this is done in configuration mode for safety. When in either configuration or installation mode, the device number is set to 30. Therefore, it is not suggested to place two or more CMD HDs in either of these modes on the same bus at the same time. When migrating from real CMD hardware, use any HDD imaging software ("dd" or GNU "ddrescue" on Linux) to copy the raw contents of a device to a file. The destination file should have a "DHD" extension. For those users with multiple disks, SCSI ID 0, LUN 0 should have the extension "DHD", but any other drives should have ".S<ID><LUN>" where <ID> is the SCSI ID and <LUN> is the LUN or logical unit. Place all the files in the same folder when attaching to the "DHD" file. The other files will automatically be scanned for and connected as well. The CMD HD boot ROM is used for partition management, and ALL versions have a known bug which corrupts data when deleting paritions across multiple SCSI drives. To avoid this scenario, it is highly suggested that another full DHD image be created in VICE (on another unit) and all the files be copied over from multi-disk configurations, using CMD "fcopy" for example, to the new unit. This will allow the user to take advantage of all the CMD HD features without the potential for data loss.

1.5 Supported file formats

VICE supports the most popular Commodore file formats:

- `X64` or `D64` disk image files; Used by the 1541, 2031, 3040, 4040 drives.
- `G64` GCR-encoded 1541 disk image files
- `P64` lowlevel NRZI flux pulse disk image files
- `D67` CBM2040 (DOS1) disk image format
- `D71` VC1571 disk image format
- `D81` VC1581 disk image format
- `D80` CBM8050 disk image format
- `D82` CBM8250/1001 disk image format
- `D90` CBM D9090/60 disk image format
- `D1M` FD2000/FD4000 DD disk image format
- `D2M` FD2000/FD4000 HD disk image format
- `D4M` FD4000 ED disk image format
- `DHD` CMD HD disk image format
- `T64` tape container files (read-only)

- `TAP` lowlevel tape image files
- `P00` program files
- `CRT` C64 cartridge image files
- `TCRT` tapecart image files

A utility (`c1541`, see Chapter 14 [c1541], page 365) is provided to allow transfers and conversions between these formats.

Notice that the use of the `X64` file format is deprecated now.

You can convert an `X64` file back into a `D64` file with the UNIX `dd` command:

```
dd bs=64 skip=1 if=IMAGE.X64 of=IMAGE.D64
```

See Chapter 17 [File formats], page 380. for a technical description of the supported file formats.

1.6 Common problems

This section tries to describe the most common known problems with VICE, and how to resolve them.

1.6.1 Sound problems

VICE should compile and run without major problems on many systems, but there are some known issues related to the sound driver.

If you are having sound problems, such as skipping, first monitor how much CPU time the respective emulator is taking on your system. To run smoothly, on a modern system, it should really never go over 50% or so, except for very short peaks that should also stay well beyond 90%. If you see it takes more, try disabling some of the most CPU intense features (disable CRT emulation, use fastsid instead of reSID, disable true drive emulation).

If the CPU usage is ok, try using a different sound driver. You may also try increasing the sound buffer size (although the default should be ok for modern systems).

All platforms that can run the SDL port (like Amiga, BeOS, etc) should be able to play sound via SDL.

1.6.2 Video problems

If you don't get video output, the problem may be that your system has no suitable OpenGL driver - which is strictly required for the GTK3 port. Have a look at the logfile to see if this is the problem.

1.6.3 Printer problems

VICE supports the emulation of a printer either on the userport or as IEC device 4-7. Unfortunately the Commodore IEC routines do not send all commands to the IEC bus. For example an `OPEN 1,4` is not seen on the IEC bus. Also a `CLOSE 1` after that is not seen. VICE can see from printing that there was an `OPEN`, but it cannot see when the close was. Also a "finish print job" cannot be seen on the userport device. To flush the printer buffer (write to `print.dump` or to the printer) you can manually trigger a form feed in the printer settings.

1.6.4 PET keyboard problems

If you find that the German keyboard mapping (plus German charset) does not print uppercase umlauts, then you are right. The umlauts replace the [,\ and] characters in the charset. The keys that make these characters do not have a different entry in the PET editor ROM tables when shifted. Thus it is not possible to get the uppercase umlauts in the editor. Nevertheless other programs are reported to change the keyboard mapping table and thus allow the use of the shifted (uppercase) umlauts.

Anyway, the VICE keyboard mappings are far from being perfect and we are open to any suggestions.

2 Invoking the emulators

The names of the available emulators are:

- `vsid`, the SID player
- `x64`, the fast C64 emulator
- `x64sc`, the accurate C64 emulator
- `x64dtv`, the C64DTV emulator
- `x128`, the C128 emulator
- `xvic`, the VIC20 emulator
- `xpet`, the PET emulator
- `xplus4`, the PLUS4 emulator
- `xcbm2`, the CBM-II emulator (CRTC models)
- `xcbm5x0`, the CBM-II emulator (VIC-II models)
- `xscpu64`, the SCPU64 emulator

You can run each of them by simply typing the name from a shell or by configuring your window manager for example to use them to open disk images.

If you want to look at the log output run them from a terminal window such as `xterm` or `rxvt`. For example, you could do

```
xterm -e x64sc
```

2.1 Command-line options used during initialization

There are several options you can specify on the command line. Some of them are used to specify emulation settings and will be described in detail later (see Chapter 6 [Settings and resources], page 58, for a complete list). The remaining options are used only to give usage information or to initialize the emulator in some way:

`-help`
`-?` List all the available command-line options and their meaning.

`-version` Show the program name and version.

`-features`
List all compile time features

`-default` Set default resources (see Chapter 6 [Settings and resources], page 58). This will override all the settings specified before, but not the settings specified afterwards on the command line.

`-config <filename>`
Specify config file (see see Chapter 6 [Settings and resources], page 58).

`-addconfig <filename>`
Specify extra config file for loading additional resources. This can be used to add "patch sets" for various configurations. This file uses the same format as the regular config file (see see Chapter 6 [Settings and resources], page 58).

`-dumpconfig <filename>`
: Write the complete config into file. Normally only resources that have been changed from their default value would be written to the config file, however with this option you can see all resources and their current values.

`-settings-node <path>`
: Activate settings dialog at node <path> after starting the emulator (Gtk3 only). Meant for use during development, the <path> is string pointing to a node in the settings tree (see src/arch/gtk3/uisettings.c).

`-logfile <name>`
: Specify log file name (`LogFileName`).

`-logtofile`
`+logtofile`
: Enable/disable logging to the log file. (`LogToFile=1`, `LogToFile=0`).

`-logtostdout`
`+logtostdout`
: Enable/disable logging to stdout. (`LogToStdout=1`, `LogToStdout=0`).

`-logtomonitor`
`+logtomonitor`
: Enable/disable logging to the monitor. (`LogToMonitor=1`, `LogToMonitor=0`).

`-logcolorize`
`+logcolorize`
: Enable/disable colorizing the log output. (`LogColorize=1`, `LogColorize=0`).

`-verbose` Enable verbose log output. (`LogLimit`).

`-debug` Enable all log output. (`LogLimit`).

`-silent` Disable all log output (except errors). (`LogLimit`).

`-loglimit <level>`
: Set log level. (`LogLimit`).

`-no-redirect-streams`
: Disable stream redirection on Windows. Stream redirection is used on Windows to reconnect stdin, stdout and stderr to VICE, so options like -help work as one would expect on a sane OS. Stream redirection is disabled when running from an msys2 shell, or when VICE is compiled with –enable-debug.

`-seed <value>`
: Set the random seed (for debugging).

`-keybuf <string>`
: Put the specified string into the keyboard buffer. Note that you can specify specific keycodes by using C-style escaped hexcodes (`"\x0a"`).

`-console` Console mode (for music playback, or for running the emulator test programs)

`-limitcycles <cycles>`
: Automatically exit the emulator after a given number of cycles.

`-chdir <directory>`
: Change the working directory.

`-autostart <name>`
: Autostart <name> (see Section 2.2 [Command-line autostart], page 27).

`-autoload <name>`
: Attach and autoload tape/disk image <name>

`-1 <Name>` Attach <Name> as a tape image file.

`-2 <Name>` Attach <Name> as a tape image file for datasette #2 (PET)

`-8 <Name>`
`-9 <Name>`
`-10 <Name>`
`-11 <Name>`
: Attach <Name> as a disk image to device 8, 9, 10 or 11.

`-8d1 <Name>`
`-9d1 <Name>`
`-10d1 <Name>`
`-11d1 <Name>`
: Attach <Name> as a disk image to the second drive of a dual-drive device 8, 9, 10 or 11.

`-attach8ro`
`-attach9ro`
`-attach10ro`
`-attach11ro`
: Attach disk image for drive #8-11 read only (`AttachDevice8d0Readonly=1`, `AttachDevice9d0Readonly=1`, `AttachDevice10d0Readonly=1`, `AttachDevice11d0Readonly=1`) (all emulators except vsid).

`-attach8d1ro`
`-attach9d1ro`
`-attach10d1ro`
`-attach11d1ro`
: Attach disk image for second drive of a dual-drive #8-11 read only (`AttachDevice8d1Readonly=1`, `AttachDevice9d1Readonly=1`, `AttachDevice10d1Readonly=1`, `AttachDevice11d1Readonly=1`) (all emulators except vsid).

`-attach8rw`
`-attach9rw`
`-attach10rw`
`-attach11rw`
: Attach disk image for drive #8-11 read write (if possible) (`AttachDevice8d0Readonly=0`, `AttachDevice9d0Readonly=0`, `AttachDevice10d0Readonly=0`, `AttachDevice11d0Readonly=0`) (all emulators except vsid).

`-attach8d1rw`
`-attach9d1rw`
`-attach10d1rw`
`-attach11d1rw`
: Attach disk image for second drive of a dual-drive #8-11 read write (if possible) (`AttachDevice8d1Readonly=0`, `AttachDevice9d1Readonly=0`, `AttachDevice10d1Readonly=0`, `AttachDevice11d1Readonly=0`) (all emulators except vsid).

`-exitscreenshot <name>`
: Specify name of a screenshot file that will be written when the emulator exits. (`ExitScreenshotName`).

`-exitscreenshotvicii <name>`
: Specify name of a screenshot file that will be written when the emulator exits. (`ExitScreenshotName1`). (x128)

2.2 Autostarting programs from the command-line

It is possible to let the emulator *autostart* a disk or tape image file, by simply specifying its name as the *last* argument on the command line, for example

```
x64sc lovelygame.d64
```

will start the C64 emulator, attaching `lovelygame.d64` as a disk image and running the first program on it. You can also specify the name of the program on the fisk image by appending a colon (‘`:`’) the name itself to the argument; for example

```
x64sc "lovelygame.d64:run me"
```

will run the program named `run me` on `lovelygame.d64` instead of the first one.

Using the command-line option `-autostart` is equivalent; so the same result can be obtained with

```
x64sc -autostart "lovelygame.d64:run me"
```

Note that if you specify a raw CBM or P00 file, the emulator will load the file, and after doing so revert all settings to what they were before autostarting.

If you want to allow the started program to access the host file system, you will have to enable "virtual device traps". If you want the autostart logic to disable true drive emulation temporarily to speed up the autostart, then enable "handle TDE at autostart (this is required if true drive emulation would be enabled otherwise - as is the default).

See Section 4.5 [Disk and tape images], page 53. for more information about images and autostart.

3 System files

In order to work properly, the emulators need to load a few system files:

- the *system ROMs*, raw binary files containing copies of the original ROMs of the machine you are emulating;
- the *keyboard maps*, text files describing the keyboard layout;
- the *joymap files*, text files with controller mappings.
- the *palette files*, text files describing the colors of the machine you are emulating.
- the *romset files*, text files describing the different ROMs to load.
- the *hotkey files*, text files with keyboard shortcuts.

The place where they will be searched for depends on the value of the `Directory` resource, which is a colon (`:`)-separated search path list, like the UNIX `PATH` environment variable. The default value is

`$HOME/.local/share/vice/EMU:PREFIX/share/vice/EMU:BOOTPATH/EMU`

Where `PREFIX` is the installation prefix (usually `/usr/local`), `EMU` is the name of the emulated machine (`C64`, `C64DTV`, `C128`, `PET`, `PLUS4`, `CBM-II`, `SCPU64` or `VIC20`) and `BOOTPATH` is the directory where the executable resides. The disk drive ROMs are looked for in a directory with `EMU` set to `DRIVES`. `$HOME` is the user's home directory.

For example, if you have the C64 emulator installed in

`/usr/local/bin/x64sc`

then the value will be

`$HOME/.local/share/vice/C64:/usr/local/share/vice/C64:/usr/local/bin/C64`

And system files will be searched for under the following directories, in the specified order:

1. `$HOME/.local/share/vice/C64`
2. `/usr/local/share/vice/C64`
3. `/usr/local/bin/C64`

System files can still be installed in a different directory if you specify a complete path instead of just a file name. For example, if you specify `./kernal` as the kernal image name, the kernal image will be loaded from the current directory. This can be done by using command-line options or by modifying resource values (see Section 6.1 [Resource files], page 58).

3.1 ROM files

Every emulator requires its own ROM set. For the C64, the ROM set consists of the following files:

- `kernal-901227-03.bin`, the Kernal ROM (8 KiB)
- `basic-901226-01.bin`, the BASIC ROM (8 KiB)
- `chargen-901225-01.bin`, the character generator ROM (4 KiB)

The VIC20 needs the following files:

- `kernal.901486-07.bin`, the Kernal ROM (8 KiB)

- `basic-901486-01.bin`, the BASIC ROM (8 KiB)
- `chargen-901460-03.bin`, the character generator ROM (4 KiB)

The Plus4 needs the following files:

- `kernal-318004-05.bin`, the Kernal ROM (16 KiB)
- `basic-318006-01.bin`, the BASIC ROM (16 KiB)
- `3plus1-317053-01.bin` and `3plus1-317054-01.bin`, the builtin '3plus1' software ROM (32 KiB)

The C128 needs the following files:

- `kernal-318020-05.bin`, the Kernal ROM (8 KiB)
- `basiclo-318018-04.bin` and `basichi-318019-04.bin`, the Basic + Editor ROM (32 KiB)
- `chargen-390059-01.bin`, the character generator ROM (4 KiB)
- `kernal64-901227-03.bin`, the C64 Kernal ROM (8 KiB)
- `basic64-901226-01.bin`, the C64 BASIC ROM (8 KiB)

The C128, VIC20, SCPU64 and C64 emulators also need the following DOS ROMs for the hardware-level emulation of the 1540, 1541, 1571, 1581, 2000, and 4000 disk drives, as well as the CMD hard drive:

- `dos1540-325302+3-01.bin`, the 1540 drive ROM (16 KiB) (DOS 2.6 V170, Commodore ROM images 325302-01 and 325303-01)
- `dos1541-325302-01+901229-05.bin`, the 1541 drive ROM (16 KiB) (DOS 2.6, Commodore ROM images 325302-01 and 901229-05.bin)
- `dos1541ii-251968-03.bin`, the 1541-II drive ROM (16 KiB) (DOS 2.6, Commodore ROM image 251968-03)
- `dos1551-318008-01.bin`, the 1551 drive ROM (16 KiB) (DOS 2.6 TDISK, Commodore ROM image 318008-01)
- `dos1570-315090-01.bin`, the 1570 drive ROM (32 KiB) (DOS 3.0, Commodore ROM image 315090-01)
- `dos1571-310654-05.bin`, the 1571 drive ROM (32 KiB) (DOS 3.0, Commodore ROM image 310654-05)
- `dos1571cr-318047-01.bin`, the 1571CR drive ROM (32 KiB) (DOS 3.0, Commodore ROM image 318047-01)
- `dos1581-318045-02.bin`, the 1581 drive ROM (32 KiB) (DOS 10, Commodore ROM image 318045-02)
- `dos2000-cs-33cc6f.bin`, the CMD FD-2000 drive ROM (32 KiB) (CMD FD DOS 1.4, CMD ROM image cs-33cc6f)
- `dos4000-fd-350022.bin`, the CMD FD-4000 drive ROM (32 KiB) (CMD FD DOS 1.4, CMD ROM image fd-350022.bin)
- `bootromCMDHD-v2-80.bin`, the CMD HD boot ROM (16 KiB) (Boot ROM V2.80, CMD ROM image)

In addition to those all emulators can handle a parallel IEEE488 interface (the C64 and C128 via `$df**` extension, the VIC20 via VIC1112 emulation) so they also need the DOS ROM for the IEEE disk drives:

- `dos2031-901484-03+05.bin`, the 2031 drive ROM (16 KiB) (DOS 2.6, Commodore ROM images 901484-03 and 901484-05)
- `dos2040-901468-06+07.bin`, the 2040 drive ROM (8 KiB) (DOS 1, Commodore ROM images 901468-06, 901468-07)
- `dos3040-901468-11-13.bin`, the 3040 drive ROM (12 KiB) (DOS 2, Commodore ROM images 901468-11, 901468-12 and 901468-13)
- `dos4040-901468-14-16.bin`, the 4040 drive ROM (12 KiB) (DOS 2, Commodore ROM images 901468-14, 901468-15 and 901468-16)
- `dos1001-901887+8-01.bin`, the 1001/8050/8250 drive ROM (16 KiB) (DOS 2.7, Commodore ROM images 901887-01 and 901888-01)
- `dos9000-300516+7-revC.bin`, the D9090/60 drive ROM (16 KiB) (DOS 3.0, Commodore ROM images 300516-RevC and 300517-RevC)

Note that there are other DOS images on the internet. The DOS 2.5 images might be used with the 8050, but it cannot handle the double sided drives of the 1001 and 8250 and it is not supported by VICE.

The PET emulator uses an expanded setup, because there are three major versions of the Basic and the Kernal, and many versions of the Editor ROM. In addition there are cartridge ROM sockets.

The Kernal files contain the memory from range $F000-$FFFF, the Basic ROMs either the range $C000-$DFFF or $B000-$DFFF. To handle the different screen sizes and keyboards, different so-called “editor-ROMs” for the memory range $E000-$E800 are provided. The PET ROMs have the following names:

- `kernal-1.901439-04-07.bin`, the PET2001 Kernal ROM (4 KiB) (Commodore ROM images 901447-06 and 901447-07, same as 901439-04 and 901439-07)
- `kernal-2.901465-03.bin`, the PET3032 Kernal ROM (4 KiB) (Commodore ROM image 901465-03)
- `kernal-4.901465-22.bin`, the PET4032/8032 Kernal ROM (4 KiB) (Commodore ROM image 901465-22)
- `basic-1.901439-09-05-02-06.bin`, the PET2001 Basic 1 ROM (8 KiB) (Commodore ROM images 901447-09, 901447-02, 901447-03, 901447-04.bin. The -09 ROM is the revised -01 ROM. Same as images 901439-09, 901439-05, 901439-02, 901439-06. The -09 ROM is the revised -01 ROM)
- `basic-2.901465-01-02.bin`, the PET3032 Basic 2 ROM (8 KiB) (Commodore ROM images 901465-01 and 901465-01)
- `basic-4.901465-23-20-21.bin`, the PET4032/8032 Basic 4 ROM (12 KiB) (Commodore ROM images 901465-23, 901465-20 and 901465-21. The -23 ROM is a revised -19 ROM)
- `edit-1-n.901439-03.bin`, the PET2001 editor for graphics keyboards (2 KiB) (Commodore ROM image 901447-05, same as 901439-03)

- `edit-2-b.901474-01.bin`, the PET3032 editor for business keyboards (2 KiB) (Commodore ROM image 901474-01)
- `edit-2-n.901447-24.bin`, the PET3032 editor for graphics keyboards (2 KiB) (Commodore ROM image 901447-24)
- `edit-4-40-n-50Hz.901498-01.bin`, the PET4032 editor for graphics keyboards (2 KiB) (Commodore ROM image 901498-01)
- `edit-4-40-b-50Hz.ts.bin`, the PET4032 editor for business keyboards (2 KiB) (Said to be "901498-01 modified to use a business keyboard on a 50Hz 4032")
- `edit-4-80-b-50Hz.901474-04.bin`, the PET8032 editor for business keyboards (2 KiB) (Commodore ROM image 901474-04)
- `characters-1.901447-08.bin`, the character generator ROM (2KiB) for the PET 2001. It has two sets with 128 chars each. The second (inverted) half of each set is computed from the first half by inverting it. This is a PET hardware feature. Compared to the character ROMs for later models, it has the upper case and lower case letters swapped: to get lower case letters, you need to use the shift key (so the basic abbreviation for "LIST" looks like "Li", instead of "lI" for "list". (Commodore ROM image 901447-08)
- `characters-2.901447-10.bin`, the character generator ROM (2KiB). It has two sets with 128 chars each. The second (inverted) half of each set is computed from the first half by inverting it. This is a PET hardware feature. (Commodore ROM image 901447-10)
- `chargen.de`, the character generator ROM (2KiB). This version is a patched German charset, with the characters [, \ and] replaced by umlauts. It has been provided by U. Guettich and he reports that it is supported by some programs.
- `characters.901640-01.bin`, the SuperPET character generator ROM (4KiB). The first half is the same as `characters-2.901447-10.bin`, the second half contains, instead of an upper and lower case set, an ASCII character set and an APL character set. For these sets, the screen code is equal to the ASCII/APL code.
- `waterloo-[abcdf]000.901898-0[1-5].bin`, `waterloo-e000.901897-01.bin`. The Waterloo system ROMs for the 6809 CPU in the SuperPET.
- `hre-9000.324992-02.bin` HiRes Emulator (at $9000) and `hre-a000.324993-02.bin` HiRes BASIC (at $A000). These are the two roms for supporting the HRE on the 8296. The ROMs are initialized by the command `SYS 36864`.

The PETs also have sockets for extension ROMs for the addresses $9000-$9FFF, $A000-$AFFF and $B000-$BFFF (the last one for PET2001 and PET3032 only). You can specify ROM image files for those extensions command line options `-petrom9`, `-petromA` and `-petromB` resp.

An alternative would be to specify a long kernal ROM with the `-kernal` option that includes the extension ROM areas.

Also, you can specify replacements for the basic ROM at $B000-$DFFF with the `-petromBasic` option and for the editor ROM at $E000-$E7FF with the `-petromEditor` option.

The CBM-II emulator again uses another setup. For those models the kernal used is the same for all. However, for different amounts of memory exist different versions of the

BASIC ROMs. The 128KiB RAM version (C610, C710, B128) uses one bank of 64KiB for the BASIC text and another one for all the variables. The 256KiB RAM version uses one bank for text, one for variables, one for arrays and one for strings.

Also the character generator ROMs have a format different from the above. The other character ROMs have 8 bytes of pixel data per character. Those ROMs have 16 bytes per character instead. The C6x0 only uses the first 8 of it, but the C7x0 uses 14 lines per character and needs those larger ROMs. Both ROMs hold, like the PET, two character sets with 128 characters each. Again the second half of the full (256 char) character set is computed by inverting.

- `kernal-901244-04a.bin`, the KERNAL (8KiB) for the business machines (6xx/7xx)
- `kernal-901234-02.bin`, the KERNAL (8KiB) for the personal machine (510) (901234-02)
- `basic-901242+3-04a.bin`, the CBM-II 128KiB BASIC (16KiB)
- `basic-901240+1-03.bin`, CBM-II 256KiB BASIC (16KiB)
- `basic-901235+6-02.bin`, C510 BASIC (16KiB) (901236-02 + 901235-02)
- `chargen-901225-01.bin`, character generator ROM for the C5x0 (4KiB) (901225-01)
- `chargen-901237-01.bin`, character generator ROM for the C6x0 (4KiB)
- `chargen-901232-01.bin`, character generator ROM for the C7x0 (4KiB)

The SCPU64 needs the following files:

- `scpu64`, the SCPU64 ROM (128 KiB)
- `chargen-901225-01.bin`, the character generator ROM (4 KiB)

3.2 Keymap files

Keymap files are used to define the keyboard layout, defining which key (or combination of keys) must be mapped to each keysym.

In other words, the keyboard emulation works like this: whenever the user presses or releases a key while the emulation window has the input focus, the emulator receives an event with a value that identifies that key. That value is called a *keysym* and is unique to that key. The emulator then looks up that keysym in an internal table that tells it which key(s) to press or release on the emulated keyboard.

VICE keymap files have the `.vkm` default extension, and every emulator comes with a default positional mapping and a default symbolic mapping, see Section 1.2 [Keyboard emulation], page 5.

To find out the keycodes to use, incase you want to edit the keymaps yourself, you can enable showing the keycodes in the status bar in the settings.

3.2.1 Comments

- Any line starting with the `#` sign, is completely ignored.
- Any characters in "C-style" comments is ignored (/* this is a comment */).

When creating new keymaps, please copy over the usual set of leading comments (have a look at the default US keymaps).

In particular each file at the very least should contain a line indicating what type of mapping it is supposed to be, for what host layout, what emulator, and which UI, like this:

```
# Symbolic Mapping, US Layout, C64, GTK
```

3.2.2 Control commands

There are some special commands you can put into the keyboard file, these usually appear at the beginning of the file, before any actual keycode definitions; they are recognized because they start with an exclamation mark:

- `!CLEAR` clears the currently loaded keyboard map; it is necessary to put this at the beginning of the file if you want the keymap file to override all of the current internal settings;
- `!INCLUDE` followed by "filename" reads (inserts) file as mapping file. This is useful when adding local mappings to an otherwise generic file (so you dont have to copy the while file, but just add/modify a few keys).
- `!UNDEF` followed by keysym removes keysym from mapping table.
- `!LSHIFT`, `!RSHIFT`, followed by a row and a column value, specify where the left and right shift keys are located on the emulated keyboard; for example, C64 default keymaps will specify

  ```
  !LSHIFT 1 7
  !RSHIFT 6 4
  ```
- `!VSHIFT`, followed by a shiftkey (RSHIFT or LSHIFT), specify what key will be used as a virtual shift key when the shift flag is set.
- `!SHIFTL`, followed by a shiftkey (RSHIFT or LSHIFT), specify what key will be used as a virtual shift-lock key; for example, C64 default keymaps will specify

  ```
  !VSHIFT LSHIFT
  !SHIFTL LSHIFT
  ```

For emulated keyboards that have only one shift key, set both `!LSHIFT` and `!RSHIFT` to the same row/col and use RSHIFT for `!VSHIFT` and `!SHIFTL`.

- `!LCTRL`, followed by a row and a column value, specifiy where the left control key is located on the emulated keyboard.
- `!LCBM`, followed by a row and a column value, specifiy where the left CBM key is located on the emulated keyboard.
- `!VCTRL`, followed by a ctrlkey (LCTRL), specify what key will be used as a virtual control key.
- `!VCBM`, followed by a cbmkey (LCBM), specify what key will be used as a virtual CBM key.

For example, a C64 keymap would usually start with this block:

```
!CLEAR
!LSHIFT 1 7
!RSHIFT 6 4
!VSHIFT RSHIFT
!SHIFTL LSHIFT
!LCBM 7 5
```

```
!VCBM LCBM
!LCTRL 7 2
!VCTRL LCTRL
```

When creating new keymaps, make sure to create the respective set of commands first and make sure the row/column values are correct.

Before you fix the rest of your mapping, make sure to create mappings for all modifier keys that relate to the used row/column pairs and update their `SHIFTFLAG` accordingly. Getting those right is the key to making more complex mappings possible later.

For a example a typical GTK C64 symbolic mapping would contain something like this:

```
Shift_R         6 4 0x0004    /* right SHIFT -> right SHIFT */
Shift_L         1 7 0x0002    /* left SHIFT  -> left SHIFT */
Caps_Lock       1 7 0x0040    /* CAPS lock   -> SHIFT lock */
Tab             7 5 0x2008    /* TAB         -> CBM (can be combined with SHIFT) */
Control_L       7 2 0x4008    /* left CTRL   -> CTRL (can be combined with SHIFT) */
```

3.2.3 Key mappings

This table is described by the keymap file, which is made up of lines like the following:

```
KEYSYM ROW COLUMN SHIFTFLAG /* COMMENT */
```

Where:

- `KEYSYM` identifying the keysym: In (GTK) it is a literal keyboard symbol as a string (for example `"space"`), in (SDL) it is a numeric keycode (for example `"32"`).

  You can use the `"show keycodes in statusbar"` feature to see what keysym is bound to any key.
- `ROW` and `COLUMN` refer to the row and column of the emulated key on the emulated keyboard - all existing keymaps have the respective keyboard matrix in them in comments, have a look.
- The `SHIFTFLAG` controls various aspects of how a host key is mapped to the emulated keyboard. For example you may want to artificially add or remove certain modifiers to/from the emulated keypress.

A keymap is parsed line by line. More complex mappings can be created by mapping the same key with different `SHIFTFLAG` to different emulated keys in multiple lines:

- A key mapping is found when the key symbol matches
- and when none of the following reject conditions apply:

Value	Hex	Description	Action
`0`	`0x0000`	The key is never shifted.	If any SHIFT modifiers pressed, then reject key.
`8`	`0x0008`	The key can be (optionally) shifted by the user.	If any SHIFT modifiers pressed on the host, and this flag **is not** set, then reject key.
`128`	`0x0080`	SHIFT modifier required on host.	If SHIFT modifier(s) **not** pressed on the host, and this flag **is** set, then reject key.

256	0x0100	Key is used for an alternative keyboard mapping	if no alternative mapping is enabled (eg C64 mode in x128), reject key.
512	0x0200	ALT-R (ALT-GR) modifier required on host.	If ALT-R modifier **not** pressed on the host, and this flag **is** set, then reject key.
1024	0x0400	CTRL modifier required on host.	If CTRL modifier **not** pressed on the host, and this flag **is** set, then reject key.

- If no reject condition was true then we found a possible match. If no other mapping will be found, this is the one that will be used. We may or may not look for another match, depending on:

Value	Hex	Description
32	0x0020	Another definition for this keysym/scancode follows later in the file. If this flag **is not** set, stop looking for another match.

- Once a match was found, the virtual modifiers are applied:

Value	Hex	Description	Action
1	0x0001	The key is shifted on the emulated keyboard	virtual shift will be used when it is not shifted on the host keyboard.
16	0x0010	Deshift key for this keysym/scancode.	When the key is pressed with shift on the host keyboard, shift will **not** be used on the emulated keyboard.
2048	0x0800	Key is combined with CBM for this keysym/scancode	virtual CBM will be used when it is not combined with CBM via another mapped key.
4096	0x1000	Key is combined with CTRL for this keysym/scancode	virtual CTRL will be used when it is not combined with CTRL via another mapped key.

- and last not least these are the flags left:

Value	Hex	Description
32768	0x8000	Do **not** emulate a "locked" switch for this key. This can be useful when your host keyboard provides physical locking by itself. (currently works only for the C128 40/80 and CAPS-lock keys)

For example this is is from the SDL C64 symbolic DE mapping:

```
# 32+1 not shifted on host, shifted on c64
60 5 7 0x0021          /*             < -> <            */
# 32+128 shifted on host, shifted on c64
60 5 4 0x00a0          /*             > -> >            */
# 512+16 alt-gr on host, deshift for c64
60 6 0 0x0210          /*        altr+< -> pound        */
```

3.2.3.1 Special Rows

Besides the keyboard matrix of the emulated keyboard, there are a bunch of special keys available in the negative rows as below:

Row	Column	Description
-1	n	Joystick keymap A, direction n
-2	n	Joystick keymap B, direction n
-3	0	First RESTORE key
-3	1	Second RESTORE key
-4	0	40/80 column key (x128)
-4	1	CAPS (ASCII/DIN) key (x128)
-5	n	Joyport keypad, key n (not supported in x128)

For example, a lot of (GTK) keymaps would map RESTORE like this:

```
# Restore key mapping
F12     -3 0
Page_Up -3 1
```

Joystick keymap direction column values for rows -1 and -2:

6 (N/W)	**7** (North)	**8** (N/E)
4 (West)	**0** (Fire)	**5** (East)
1 (S/W)	**2** (South)	**3** (S/E)

Joyport keypad direction column values for row -5:

0	**1**	**2**	**3**	**4**
5	**6**	**7**	**8**	**9**
10	**11**	**12**	**13**	**14**
15	**16**	**17**	**18**	**19**

When a bigger spaced key is used, it uses the upper left most column value.

3.2.3.2 Modifier Flags

The `SHIFTFLAG` can have one of the following values. Flags can be combined by simply ORing (or adding) them together:

Value	Hex	Description
0	0x0000	The key is never shifted.
1	0x0001	The key is shifted on the emulated keyboard, virtual shift will be used when it is not shifted on the host keyboard.
2	0x0002	The key is the left shift key.
4	0x0004	The key is the right shift key. Use only this flag for emulated keyboards that have only one shift key.
8	0x0008	The key can be (optionally) shifted by the user.
16	0x0010	Deshift key for this keysym/scancode. That means when the key is pressed with shift on the host keyboard, shift will **not** be used on the emulated keyboard.
32	0x0020	Another definition for this keysym/scancode follows later in the file
64	0x0040	Key is SHIFT-lock on the emulated machine. Usually you'd use this for the CAPS lock key on the host keyboard.
128	0x0080	SHIFT modifier required on host.

256	0x0100	Key is used for an alternative keyboard mapping (e.g. C64 mode in x128). Note that **any alternative mappings must occur before the normal mappings for the same key/symbol in the file**.
512	0x0200	ALT-R (ALT-GR) modifier required on host.
1024	0x0400	CTRL modifier required on host.
2048	0x0800	Key is combined with CBM for this keysym/scancode.
4096	0x1000	Key is combined with CTRL for this keysym/scancode.
8192	0x2000	Key is (left) CBM on emulated machine.
16384	0x4000	Key is (left) CTRL on emulated machine.
32768	0x8000	Do **not** emulate a "locked" switch for this key. This can be useful when your host keyboard provides physical locking by itself. (currently works only for the C128 40/80 and CAPS-lock keys)

The `SHIFTFLAG` can be used to control various aspects of how the host key will map to the emulated keyboard. For example it is useful if you want certain keys to be "artificially" shifted by the emulator, and not by the user. Or the other way around, a key can be "deshifted".

For example, `F2` is shifted on the C64 keyboard, but you might want it to be mapped to the unshifted `F2` key on the PC keyboard. For example, a typical C64 keymap would contain the following:

```
F1 0 4 8        /* F1 -> F1 (+SHIFT allowed) */
F2 0 4 1        /* F2 -> F1 + SHIFT */
F3 0 5 8        /* F3 -> F3 (+SHIFT allowed) */
F4 0 5 1        /* F4 -> F3 + SHIFT */
F5 0 6 8        /* F5 -> F5 (+SHIFT allowed) */
F6 0 6 1        /* F6 -> F5 + SHIFT */
F7 0 3 8        /* F7 -> F7 (+SHIFT allowed) */
F8 0 3 1        /* F8 -> F7 + SHIFT */
```

where `0` and `4` identify the key (row 0, column 4 on the keyboard matrix), and `1` specifies that every time the user presses `F2` the shift key on the C64 keyboard must be pressed.

3.3 Joymap files

Joymap files are used to define how a host controller is mapped to the emulated joysticks. VICE uses one file per emulated machine to store the current default mapping, these files contain the mapping for several controllers. Files saved by the UI contain the mapping for one controller only, and can be reused.

VICE joymap files have the `.vjm` default extension.

3.3.1 Syntax of the hotkeys files

3.3.1.1 Comments

Comments are started with `#` and occupy the rest of the line, they can also appear inline after a directive or mapping.

When creating new joymaps, please copy over the usual set of leading comments (which contain brief documentation). Please also add a line that indicates which controller exactly

this mapping is for, and which OS and port (GTK/SDL) was used. If you save the mapping from the UI, all of this is automatically included.

3.3.1.2 Directives

Directives are special commands for the parser. They start with `!` and are case-insensitive.

!CLEAR

`!clear`

Clear all registered mappings. Usually used as the first directive in the default joymap file.

3.3.1.3 Joystick mappings

A line has 4 mandatory columns:

1. Joystick number (zero-based index of the hardware joystick)
2. Input type
    1. Axis (JOY_INPUT_AXIS)
    2. Button (JOY_INPUT_BUTTON)
    3. Hat (JOY_INPUT_HAT)
    4. Ball (JOY_INPUT_BALL)
3. Input index
    - For buttons, it is the zero-based index of the button
    - For axes:
        - if the action is 6 (POT_AXIS), it is the zero-based index of the axis
        - otherwise, 0 and 1 are respectively the positive and negative directions of axis 0; 2 and 3 are respectively the positive and negative directions of axis 1 etc. In formulas: axis=inputindex/2 (integer division), direction = positive if inputindex % 2 == 0 , negative if inputindex % 2 == 1.
    - for hats, 0, 1, 2 and 3 are respectively up, down, left and right of hat 0; 4, 5, 6 and 7 are respectively up, down, left and right of hat 1, etc.
4. Action
    0. NONE: none
    1. JOYSTICK: emulated joystick action
    2. KEYBOARD: emulated keyboard action
    3. MAP: SDL-only. Only works while the SDL settings menu is active. If a joystick action mapped to this is performed, the user is asked to press a key, which will become a hotkey to select the currently-selected menu
    4. UI_ACTIVATE: go to the settings menu (SDL) show the settings dialog (Gtk3)
    5. UI_FUNCTION: shortcut to a specific UI function. A string argument (unquoted) indicating the UI action to trigger. See [List of action names], page 45, for a list of action names.
    6. POT_AXIS: (only for axes of hardware joysticks) the analog value of the axis is the value of the emulated potentiometer

5. For some actions, more columns are present:
   - JOYSTICK
     5. emulated joystick pin:

1	Joystick up / SNES pad up
2	Joystick down / SNES pad down
4	Joystick left / SNES pad left
8	Joystick right / SNES pad right
16	Joystick fire 1 / SNES pad B button
32	Joystick fire 2 / SNES pad Y button
64	Joystick fire 3 / SNES pad A button
128	SNES pad X button
256	SNES pad left bumber
512	SNES pad right bumber
1024	SNES pad select button
2048	SNES pad start button

   - KEYBOARD
     5. row in the keyboard matrix layout
     6. column in the keyboard matrix layout
     7. flags (bit0: shift pressed)
   - POT_AXIS
     5. which potentiometer is affected:
        1. Pot-X
        2. Pot-Y

3.3.1.4 Example

Take this sample line from data/joymaps/sdl_joymap_ds3.vjm

```
0 1 10 5 pause-toggle
```

- First column: HW joystick 0
- Second column: input type is button (1)
- Third column: input index 10, so button 10
- Fourth column: action UI_FUNCTION (5)
- Fifth column: Toggle pause mode (pause-toggle)

3.3.2 Default mapping

When the emulator starts up, and no mapping was previously configured, a minimal default mapping is applied: The first two axis will be used for the joystick directions, and the first 3 buttons will be fire buttons. Use the UI to further configure the mapping to your liking.

We also provide a couple mapping files made for popular controllers, these follow the following scheme (if possible):

- First all existing buttons are mapped 1:1 (positional) to the SNES controller, ie DPad to DPad, Start to Start, Select to Select etc
- Left analog stick to joystick directions

- Right analog stick to paddle X/Y
- the "menu" key (on Playstation controllers it is the PS button) to the "pause" gui action

This way most stuff will work out of the Box. If you want to provide a mapping for your favourite controller, please use the same setup. We recommend to use the "joyride" program for testing, and to use one of the available SNES-controller emulations in VICE.

3.3.3 Microflyte Joystick

The Microflyte joystick is a very rare analog joystick specifically made to be used with flight simulators.

To not clutter the UI too much, it does not show the descriptions of the buttons/axes for this device, refer to the following to table for creating a mapping:

Joystick	**Microflyte**
Up	Throttle up
Down	Throttle down
Left	Brake
Right	Flaps
Fire	Reset
Pot X	Left/Right
Pot Y	Up/Down

3.4 Palette files

Palette files are used to specify the colors used in the emulators. They are made up of lines like the following:

```
RED GREEN BLUE
```

where `RED`, `GREEN` and `BLUE` are hexadecimal values ranging from 0 to FF and specifying the amount of red, green and blue you want for each color.

You have to include as many lines as the number of colors the emulated machine has, and the order of the lines must respect the one used in the machine (so the N'th line must contain the specifications for color N - 1 in the emulated machine).

Lines starting with the `#` sign are completely ignored. This is useful for adding comments (such as color names) within the palette file. That said, it is not the whole truth - there are special comment lines, which the user interface uses to identify and refer to palettes:

```
# NAME:<NAME>
```

with <NAME> being the name of the Palette, as shown in the menus.

```
# TYPE:<CHIP>
```

with <CHIP> being one the following: "VIC, VICII, TED, VDC, Crtc"

These special comment lines must appear exactly as shown above, or they will be ignored.

For example, the default PET palette file (which has only two colors, 0 for background and 1 for foreground), looks like the following:

```
#
# VICE Palette file
```

```
#
# Syntax:
# Red Green Blue
#
# NAME:Black/White
# TYPE:Crtc

# Background
00 00 00

# Foreground
00 FF 00
```

3.5 Romset files

The Romset files are not used by default on all emulators. You might have recognized that the names of the ROM images are saved in resources. Loading a Romset file now just means a 'shortcut' to changing all the resources with ROM image names and reloading the ROMs.

The PET and CBM-II emulators use this feature to change between the different ROM versions available for those machines. E.g. the Romset file for the PET 2001 is

```
KernalName="pet2001"
EditorName=
ChargenName="chargen"
RomModule9Name=
RomModuleAName=
RomModuleBName=
```

As you can see, the file even uses the same syntax as the resource file, it is just a bit stripped down.

While a Romset file is processed, the directory where the Romset file was found is temporarily prepended to the search path (`Directory` resource). This also means that if you have a setting for `Directory` in it, its effect is limited to the Romset file itself.

3.5.1 Romset command line options

`-romsetfile <File>`
: load the given romset file

`-romsetarchive <File>`
: load the given romset archive

`-romsetarchiveselect <Item number>`
: select the given item from the current romset archive

3.6 Hotkeys files

VICE allows setting custom keyboard shortcuts, which we refer to as hotkeys. These hotkeys can be set either through the user interface or by editing hotkeys files, which are stored in the VICE data directory, in the user's VICE configuration directory or at a custom location. Hotkeys files have the `.vhk` extension.

VICE's hotkeys files are read from VICE's data directory, which we'll refer to as `$VICEDIR` and the (optional) user's hotkeys will be in the user's VICE configuration directory, which we'll refer to as `$USERDIR`. When saving hotkeys without explicitly specifying a custom path, the name of the user's hotkeys file will be in the form `$UI-hotkeys-$MACHINE.vhk`, for example "gtk3-hotkeys-C64SC.vhk".

On Unix, `$VICEDIR` will point to `/usr/local/share/vice/`, when using the default install prefix, and `$USERDIR` will point to `$HOME/.config/vice/`. On Windows, `$VICEDIR` will point to the root directory of the bindist and `$USERDIR` will point to `%APPDATA%\vice`, which usually is `C:\Users\%USERNAME%\AppData\Roaming\vice`.

A custom path can be specified by using the command line interface: `x64sc -hotkeyfile <some-file>`, or by using the UI (**TODO**).

3.6.1 Hotkeys command line options

`-hotkeyfile <File>`
: Load the given hotkeys file

3.6.2 Syntax of the hotkeys files

The syntax of the hotkeys files is pretty straightforward, a file can contain mappings, directives and comments. Leading and trailing whitespace is ignored by the parser.

3.6.2.1 Comments

Comments are started with either `;` or `#` and occupy the rest of the line, they can also appear inline after a directive or mapping.

For example:

```
# This is a comment

monitor-open    <Alt>h      # This is an inline comment
```

3.6.2.2 Directives

Directives are special commands for the parser. They start with `!` and are case-insensitive.

!CLEAR

`!clear`

Clear all registered hotkeys. Best used as the first directive in the (main) hotkeys file.

!DEBUG

`!debug <enable|disable|on|off>`

Enable or disable debugging messages via VICE's log system. Messages will be prefixed with 'Hotkeys:'. Debugging is disabled by default.

!ELSE

`!else`

Take the false branch of an `!if` condition. See **!IF** for more on conditionals in hotkeys files.

!ENDIF

`!endif`

Close `!if` [`!else`] condition body. See **!IF** for more on condtionals in hotkeys.

!IF

```
@!if <condition>
```

Conditionally execute hotkeys file contents. If *condition* is true, the lines following `!if` will be executed. May be followed by `!else` to specify lines that must be executed when *condition* is false. Must be followed by `!endif` to end the conditional execution of lines.

Conditions can contain boolean expressions: the boolean operators `!` (not), `||` (or) and `&&` (and) are supported, as well as parenthesis to change (or emphasize) precedence. Conditionals can be nested.

Only predefined boolean symbolic constants are available for use in the expressions, these are:

`true`
`false` Boolean literals, useful for commenting out multiple lines of a hotkeys file, akin to the `#if 0` construct in C.

`C64` The current emulator is x64

`C64SC` The current emulator is x64sc

`C64DTV` The current emulator is x64dtv

`SCPU64` The current emulator is xscpu64

`C128` The current emulator is x128

`PET` The current emulator is xpet

`PLUS4` The current emulator is xplus4

`CBM5X0` The current emulator is xcbm5x0

`CBM6X0` The current emulator is xcbm2

`VIC20` The current emulator is xvic

`SDL1`
`SDL2`
`GTK3` UI identifiers. The can be used to only assign certain hotkeys for certain UIs. Please note that to check for SDL regardless of library version the condition `SDL1 || SDL2` must be used.

`MACOS`
`UNIX`
`WINDOWS` Operation system identifiers. Since MacOS is genetically Unix, the `UNIX` identifier includes MacOS, while the `MACOS` identifier is only valid for MacOS. To register a hotkey for all Unix except MacOs use `UNIX && !MACOS`.

Some examples of using conditionals

```
!if MACOS
    # map hotkey if the host OS is MacOS
    monitor-open    <Command>Escape
!else
```

```
    # map hotkey if not MacOS
    monitor-open    <Alt>h
!endif
```

!INCLUDE

`!include <file>`

Process `<file>` as if its contents were injected into the current file being processed. This can be used recursively. The `<file>` argument can be inside quotes (") to be able to use paths or filenames with spaces in them, and quotes inside quotes can be used by escaping them with \, for example: `!include "foo \"bar\".vhk"`.

TODO: document search order/logic for included files

For example:

`!include "hotkeys-drive.vhk"`

will include `/usr/local/share/vice/hotkeys/hotkeys-drive.vhk`, assuming the default install prefix for VICE was used.

!UNDEF

`!undef [<modifier>..]<keyname>`

Remove a hotkey from whatever action it is mapped to.

For example:

```
!undef <Alt>r               # Unmap Alt+r from 'restore display'
machine-reset-cpu   <Alt>r  # Map Alt+r to reset
```

3.6.2.3 Hotkey mappings

Syntax

`<action-name>   [<modifier>...]<keyname>`

Create a mapping of a hotkey to an action, where `<action-name>` is a string refering to an operation triggered by a menu item – such as toggling Warp Mode, or attaching a disk to a drive – followed by a keyname, optionally prefixed with one or more modifiers.

For example:

```
monitor-open    <Alt>m
settings-open   KP_Divide       # map '/' on the keypad to the settings dialog
edit-paste      <Control><Alt>Insert
```

The key names are case-sensitive and are taken from the key names X11 uses. See `src/arch/shared/hotkeys/vhkkeysyms.h` for a list of the symbolic constants for the keys, the names are the same as the symbolic constants, excluding the `VHK_KEY_` prefix.

In the above example the string 'Insert' would map to `VHK_KEY_Insert`.

3.6.3 List of modifiers

Windows/Unix	MacOS	GDK symbolic constant
<Alt>	<Option>	GDK_MOD1_MASK
<Control>	<Command>	GDK_CONTROL_MASK
<Hyper>		GDK_HYPER_MASK
<Shift>	<Shift>	GDK_SHIFT_MASK
<Super>	<Super>	GDK_SUPER_MASK

Please be aware that some modifier+key combinations are either mapped to the emulated machine's keyboard – such as `<Control>1` being mapped to CBM+1 when using a positional keymap – or to the operating system/window manager. Mappings using `<Alt>` or `<Alt><Shift>` are usually fine.

3.6.4 List of action names

name	description
advance-frame	Advance emulation one frame
border-mode-debug	Set border mode to Debug
border-mode-full	Set border mode to Full
border-mode-none	Set border mode to None
border-mode-normal	Set border mode to Normal
cart-attach	Attach CRT cartridge image
cart-attach-raw	Attach raw cartridge image
cart-attach-raw-1000	Attach raw cartridge image at $1000
cart-attach-raw-2000	Attach raw cartridge image at $2000
cart-attach-raw-4000	Attach raw cartridge image at $4000
cart-attach-raw-6000	Attach raw cartridge image at $6000
cart-attach-raw-a000	Attach raw cartridge image at $A000
cart-attach-raw-b000	Attach raw cartridge image at $B000
cart-attach-raw-behrbonz	Attach Behr Bonz cartridge image
cart-attach-raw-c1-full	Attach full C1 cartridge image
cart-attach-raw-c1-high	Attach high C1 cartridge image
cart-attach-raw-c1-low	Attach low C1 cartridge image
cart-attach-raw-c2-full	Attach full C2 cartridge image
cart-attach-raw-c2-high	Attach high C2 cartridge image
cart-attach-raw-c2-low	Attach low C2 cartridge image
cart-attach-raw-final	Attach Final Expansion cartridge image
cart-attach-raw-jacint1mb	Attach 1MB Cartridge image
cart-attach-raw-magic	Attach c264 magic cart image
cart-attach-raw-megacart	Attach Mega-Cart image
cart-attach-raw-multi	Attach multi cart image
cart-attach-raw-ultimem	Attach UltiMem cartridge image
cart-attach-raw-vicfp	Attach Vic Flash Plugin cartridge image
cart-detach	Detach cartridge
cart-detach-1000	Detach cartridge image at $1000
cart-detach-2000	Detach cartridge image at $2000
cart-detach-4000	Detach cartridge image at $4000
cart-detach-6000	Detach cartridge image at $6000
cart-freeze	Press cartridge freeze button
debug-autoplayback-frames	Set autoplayback frames
debug-blitter-log-toggle	Toggle blitter logging
debug-core-dump-toggle	Toggle saving core dump
debug-dma-log-toggle	Toggle DMA logging
debug-flash-log-toggle	Toggle Flash logging
debug-trace-cpu-toggle	Toggle CPU trace

`debug-trace-drive-10-toggle`	Toggle drive 10 CPU trace
`debug-trace-drive-11-toggle`	Toggle drive 11 CPU trace
`debug-trace-drive-8-toggle`	Toggle drive 8 CPU trace
`debug-trace-drive-9-toggle`	Toggle drive 9 CPU trace
`debug-trace-iec-toggle`	Toggle IEC bus trace
`debug-trace-ieee488-toggle`	Toggle IEEE-488 bus trace
`debug-trace-mode`	Select machine/drive CPU trace mode
`diagnostic-pin-toggle`	Toggle PET userport diagnostic pin
`drive-attach-10:0`	Attach disk to unit 10, drive 0
`drive-attach-10:1`	Attach disk to unit 10, drive 1
`drive-attach-11:0`	Attach disk to unit 11, drive 0
`drive-attach-11:1`	Attach disk to unit 11, drive 1
`drive-attach-8:0`	Attach disk to unit 8, drive 0
`drive-attach-8:1`	Attach disk to unit 8, drive 1
`drive-attach-9:0`	Attach disk to unit 9, drive 0
`drive-attach-9:1`	Attach disk to unit 9, drive 1
`drive-create`	Create and attach empty disk image
`drive-detach-10:0`	Detach disk from unit 10, drive 0
`drive-detach-10:1`	Detach disk from unit 10, drive 1
`drive-detach-11:0`	Detach disk from unit 11, drive 0
`drive-detach-11:1`	Detach disk from unit 11, drive 1
`drive-detach-8:0`	Detach disk from unit 8, drive 0
`drive-detach-8:1`	Detach disk from unit 8, drive 1
`drive-detach-9:0`	Detach disk from unit 9, drive 0
`drive-detach-9:1`	Detach disk from unit 9, drive 1
`edit-copy`	Copy screen content to clipboard
`edit-paste`	Paste clipboard content into machine
`fliplist-add-10:0`	Add current disk in unit 10, drive 0 to fliplist
`fliplist-add-10:1`	Add current disk in unit 10, drive 1 to fliplist
`fliplist-add-11:0`	Add current disk in unit 11, drive 0 to fliplist
`fliplist-add-11:1`	Add current disk in unit 11, drive 1 to fliplist
`fliplist-add-8:0`	Add current disk in unit 8, drive 0 to fliplist
`fliplist-add-8:1`	Add current disk in unit 8, drive 1 to fliplist
`fliplist-add-9:0`	Add current disk in unit 9, drive 0 to fliplist
`fliplist-add-9:1`	Add current disk in unit 9, drive 1 to fliplist
`fliplist-clear-10:0`	Clear fliplist of unit 10, drive 0
`fliplist-clear-10:1`	Clear fliplist of unit 10, drive 1
`fliplist-clear-11:0`	Clear fliplist of unit 11, drive 0
`fliplist-clear-11:1`	Clear fliplist of unit 11, drive 1
`fliplist-clear-8:0`	Clear fliplist of unit 8, drive 0
`fliplist-clear-8:1`	Clear fliplist of unit 8, drive 1
`fliplist-clear-9:0`	Clear fliplist of unit 9, drive 0

`fliplist-clear-9:1`	Clear fliplist of unit 9, drive 1
`fliplist-load-10:0`	Load fliplist for unit 10, drive 0
`fliplist-load-10:1`	Load fliplist for unit 10, drive 1
`fliplist-load-11:0`	Load fliplist for unit 11, drive 0
`fliplist-load-11:1`	Load fliplist for unit 11, drive 1
`fliplist-load-8:0`	Load fliplist for unit 8, drive 0
`fliplist-load-8:1`	Load fliplist for unit 8, drive 1
`fliplist-load-9:0`	Load fliplist for unit 9, drive 0
`fliplist-load-9:1`	Load fliplist for unit 9, drive 1
`fliplist-next-10:0`	Attach next disk in fliplist to unit 10, drive 0
`fliplist-next-10:1`	Attach next disk in fliplist to unit 10, drive 1
`fliplist-next-11:0`	Attach next disk in fliplist to unit 11, drive 0
`fliplist-next-11:1`	Attach next disk in fliplist to unit 11, drive 1
`fliplist-next-8:0`	Attach next disk in fliplist to unit 8, drive 0
`fliplist-next-8:1`	Attach next disk in fliplist to unit 8, drive 1
`fliplist-next-9:0`	Attach next disk in fliplist to unit 9, drive 0
`fliplist-next-9:1`	Attach next disk in fliplist to unit 9, drive 1
`fliplist-previous-10:0`	Attach previous disk in fliplist to unit 10, drive 0
`fliplist-previous-10:1`	Attach previous disk in fliplist to unit 10, drive 1
`fliplist-previous-11:0`	Attach previous disk in fliplist to unit 11, drive 0
`fliplist-previous-11:1`	Attach previous disk in fliplist to unit 11, drive 1
`fliplist-previous-8:0`	Attach previous disk in fliplist to unit 8, drive 0
`fliplist-previous-8:1`	Attach previous disk in fliplist to unit 8, drive 1
`fliplist-previous-9:0`	Attach previous disk in fliplist to unit 9, drive 0
`fliplist-previous-9:1`	Attach previous disk in fliplist to unit 9, drive 1
`fliplist-remove-10:0`	Remove current disk in unit 10, drive 0 from fliplist
`fliplist-remove-10:1`	Remove current disk in unit 10, drive 1 from fliplist

`fliplist-remove-11:0`	Remove current disk in unit 11, drive 0 from fliplist
`fliplist-remove-11:1`	Remove current disk in unit 11, drive 1 from fliplist
`fliplist-remove-8:0`	Remove current disk in unit 8, drive 0 from fliplist
`fliplist-remove-8:1`	Remove current disk in unit 8, drive 1 from fliplist
`fliplist-remove-9:0`	Remove current disk in unit 9, drive 0 from fliplist
`fliplist-remove-9:1`	Remove current disk in unit 9, drive 1 from fliplist
`fliplist-save-10:0`	Save fliplist of unit 10, drive 0
`fliplist-save-10:1`	Save fliplist of unit 10, drive 1
`fliplist-save-11:0`	Save fliplist of unit 11, drive 0
`fliplist-save-11:1`	Save fliplist of unit 11, drive 1
`fliplist-save-8:0`	Save fliplist of unit 8, drive 0
`fliplist-save-8:1`	Save fliplist of unit 8, drive 1
`fliplist-save-9:0`	Save fliplist of unit 9, drive 0
`fliplist-save-9:1`	Save fliplist of unit 9, drive 1
`fullscreen-decorations-toggle`	Show menu/status in fullscreen
`fullscreen-toggle`	Toggle fullscreen
`help-about`	Show About dialog
`help-command-line`	Show command line options
`help-compile-time`	Show compile time features
`help-hotkeys`	Show hotkeys
`help-manual`	Browse VICE manual
`history-milestone-reset`	Return to recording milestone
`history-milestone-set`	Set recording milestone
`history-playback-start`	Start playing back events
`history-playback-stop`	Stop playing back events
`history-record-start`	Start recording events
`history-record-stop`	Stop recording events
`hotkeys-clear`	Clear all hotkeys
`hotkeys-default`	Load default hotkeys
`hotkeys-load`	Load hotkeys from current file
`hotkeys-load-from`	Load hotkeys from custom file
`hotkeys-save`	Save hotkeys to current file
`hotkeys-save-to`	Save hotkeys to custom file
`keyset-joystick-toggle`	Allow keyset joysticks
`machine-power-cycle`	Power cycle the machine
`machine-reset-cpu`	Reset the machine CPU
`media-record`	Start recording media
`media-record-audio`	Start recording audio
`media-record-screenshot`	Take screenshot
`media-record-video`	Start recording video

`media-stop`	Stop media recording
`monitor-open`	Open monitor
`mouse-grab-toggle`	Toggle Mouse Grab
`pause-toggle`	Toggle Pause
`printer-formfeed-4`	Send form feed to printer #4
`printer-formfeed-5`	Send form feed to printer #5
`printer-formfeed-6`	Send form feed to plotter #6
`printer-formfeed-userport`	Send form feed to userport printer
`psid-ffwd`	Fast forward
`psid-load`	Load PSID file
`psid-loop-toggle`	Toggle looping
`psid-override-toggle`	Override PSID settings
`psid-pause`	Pause playback
`psid-play`	Play
`psid-playlist-add`	Add files to the playlist
`psid-playlist-clear`	Clear the playlist
`psid-playlist-first`	Play first tune in the playlist
`psid-playlist-last`	Play last tune in the playlist
`psid-playlist-load`	Load a playlist
`psid-playlist-next`	Play next tune in the playlist
`psid-playlist-previous`	Play previous tune in the playlist
`psid-playlist-save`	Save the playlist
`psid-stop`	Stop playback
`psid-subtune-1`	Play subtune #1
`psid-subtune-10`	Play subtune #10
`psid-subtune-11`	Play subtune #11
`psid-subtune-12`	Play subtune #12
`psid-subtune-13`	Play subtune #13
`psid-subtune-14`	Play subtune #14
`psid-subtune-15`	Play subtune #15
`psid-subtune-16`	Play subtune #16
`psid-subtune-17`	Play subtune #17
`psid-subtune-18`	Play subtune #18
`psid-subtune-19`	Play subtune #19
`psid-subtune-2`	Play subtune #2
`psid-subtune-20`	Play subtune #20
`psid-subtune-21`	Play subtune #21
`psid-subtune-22`	Play subtune #22
`psid-subtune-23`	Play subtune #23
`psid-subtune-24`	Play subtune #24
`psid-subtune-25`	Play subtune #25
`psid-subtune-26`	Play subtune #26
`psid-subtune-27`	Play subtune #27
`psid-subtune-28`	Play subtune #28
`psid-subtune-29`	Play subtune #29
`psid-subtune-3`	Play subtune #3
`psid-subtune-30`	Play subtune #30

`psid-subtune-4`	Play subtune #4
`psid-subtune-5`	Play subtune #5
`psid-subtune-6`	Play subtune #6
`psid-subtune-7`	Play subtune #7
`psid-subtune-8`	Play subtune #8
`psid-subtune-9`	Play subtune #9
`psid-subtune-default`	Play default subtune
`psid-subtune-next`	Play next subtune
`psid-subtune-previous`	Play previous subtune
`quit`	Quit emulator
`reset-drive-10`	Reset drive 10
`reset-drive-10-config`	Reset drive 10 in configuration mode
`reset-drive-10-install`	Reset drive 10 in installation mode
`reset-drive-11`	Reset drive 11
`reset-drive-11-config`	Reset drive 11 in configuration mode
`reset-drive-11-install`	Reset drive 11 in installation mode
`reset-drive-8`	Reset drive 8
`reset-drive-8-config`	Reset drive 8 in configuration mode
`reset-drive-8-install`	Reset drive 8 in installation mode
`reset-drive-9`	Reset drive 9
`reset-drive-9-config`	Reset drive 9 in configuration mode
`reset-drive-9-install`	Reset drive 9 in installation mode
`restore-display`	Resize window to fit content
`scpu-jiffy-switch-toggle`	Toggle SCPU JiffyDOS switch
`scpu-speed-switch-toggle`	Toggle SCPU Speed switch
`screenshot-quicksave`	Quicksave screenshot
`settings-default`	Restore default settings
`settings-dialog`	Open settings
`settings-load`	Load settings
`settings-load-extra`	Load additional settings
`settings-load-from`	Load settings from custom file
`settings-save`	Save settings
`settings-save-to`	Save settings to custom file
`show-statusbar-secondary-toggle`	Show secondary status bar
`show-statusbar-toggle`	Show status bar
`smart-attach`	Smart-attach a medium to the emulator
`snapshot-load`	Load snapshot file
`snapshot-quickload`	Quickload snapshot
`snapshot-quicksave`	Quicksave snapshot
`snapshot-save`	Save snapshot file
`speed-cpu-10`	Set CPU speed to 10%
`speed-cpu-100`	Set CPU speed to 100%
`speed-cpu-200`	Set CPU speed to 200%
`speed-cpu-25`	Set CPU speed to 25%
`speed-cpu-50`	Set CPU speed to 50%
`speed-cpu-custom`	Set custom CPU speed
`speed-fps-50`	Set video clock to 50Hz

`speed-fps-60`	Set video clock to 60Hz
`speed-fps-custom`	Set custom video clock
`speed-fps-real`	Set real video clock
`swap-controlport-toggle`	Swap controlport joysticks
`tape-attach-1`	Attach tape to datasette 1
`tape-attach-2`	Attach tape to datasette 2
`tape-create-1`	Create tape and attach to datasette 1
`tape-create-2`	Create tape and attach to datasette 2
`tape-detach-1`	Detach tape from datasette 1
`tape-detach-2`	Detach tape from datasette 2
`tape-ffwd-1`	Press FFWD on datasette 1
`tape-ffwd-2`	Press FFWD on datasette 2
`tape-play-1`	Press PLAY on datasette 1
`tape-play-2`	Press PLAY on datasette 2
`tape-record-1`	Press RECORD on datasette 1
`tape-record-2`	Press RECORD on datasette 2
`tape-reset-1`	Reset datasette 1
`tape-reset-2`	Reset datasette 2
`tape-reset-counter-1`	Reset datasette 1 counter
`tape-reset-counter-2`	Reset datasette 2 counter
`tape-rewind-1`	Press REWIND on datasette 1
`tape-rewind-2`	Press REWIND on datasette 2
`tape-stop-1`	Press STOP on datasette 1
`tape-stop-2`	Press STOP on datasette 2
`virtual-keyboard`	Activate virtual keyboard
`warp-mode-toggle`	Toggle Warp Mode

To see which emulators support which actions please look at `src/arch/shared/uiactions.c` or use "Help" -> "Hotkeys" in an emulator.

3.6.5 Syntax highlighting

Vim syntax highlighting files can be found in doc/vim/. Currently there are two files: `syntax/vhk.vim` and `ftdetect/vhk.vim`, these can be copied to `$VIMFILES/` to enable hotkeys syntax highlighting in Vim.

4 Basic operation

This section describes the basic things you can do once the emulator has been fired up.

4.1 The emulation window

When the emulator is run, the screen of the emulated machine is displayed in a window which we will call the *emulation window*. This window will be updated in real time, displaying the same contents that a real monitor or TV set would.

Below the emulation window there is an area which is used to display information about the state of the emulator; we will call this area the *status bar*.

On the extreme left of the status bar, there is a *performance meter*. This displays the current relative speed of the emulator (as a percentage) and the update frequency (in frames per second). All the machines emulated are PAL, so the update frequency will be 50 frames per second if your system is fast enough to allow emulation at the speed of the real machine.

On the extreme right of the status bar, there is a *drive status indicator*. This is only visible if the hardware-level ("True") 1541 emulation is turned on. In that case, the drive status indicator will contain a rectangle emulating the drive LED and will display the current track position of the drive's read/write head.

4.2 Using the menus

It is possible to execute some commands and change emulation parameters while the emulator is running: most emulation settings can be changed in the *options menu*. Additionally clicking on the various widgets in the status bar gives access to related settings. Settings can be saved and later used with the "Save settings" and "Load settings" menu items, respectively. Also by default, settings will get saved when exiting the emulator. "Restore default settings" restores the factory defaults. See See Chapter 6 [Settings and resources], page 58, for more information about how settings work in VICE.

A lot of settings and actions can be reached via *shortcuts* or *hotkeys*, i.e., it is possible to execute them by pressing a sequence of keys instead of going through the menu with the mouse. Where shortcuts exist, they are displayed in parentheses at the right edge of the menu item. In VICE, all shortcuts must begin with the `Meta` or `Alt` key. So, for example, to attach a disk image to drive #8 (the corresponding menu item displays "M-8"), you have to press the `Meta` (or `Alt`) and then `8`.

Note that no other key presses are passed on to the emulated machine while either `Meta` or `Alt` are held down.

4.3 Getting help

At any time, if you get stuck or do not remember how to perform a certain action, you can use the "Browse manuals" command (from the help menu). This will open either the PDF or popup a browser and open the HTML version of the documentation.

Notice that on Linux this requires VICE to be properly (and fully) installed, eg with a '`make install`'.

4.4 Using the file selector

In those situations where it is necessary to specify a file name, all of the VICE emulators will pop up a file selector window allowing you to select or specify a file interactively.

To the left of the file selector, there is a list of ancestor directories: by clicking on them, you can ascend the directory tree. To the right, there is a list of the files in the current directory; files can be selected by clicking on them. If you click on a directory, that directory becomes the current one; if you click on an ordinary file, it becomes the active selection.

At the top, there is a *directory box*, with the complete path of the current directory, and a *file name box*, with the name of the currently selected file. At the bottom there are two buttons: "OK" confirms the selected file and "Cancel" abandons the file selector without cancelling the operation.

It is also possible to specify what files you want to show in the file selector by writing an appropriate shell-like pattern in the directory box; e.g., '`~/*.[dx]64`' will only show files in the home directory whose name ends with either `.d64` or with `.x64`.

4.5 Using disk and tape images

The emulator is able to emulate disk drives and tape recorders if provided with suitable *disk images* or *tape images*. An *image* is a raw dump of the contents of the media, and must be *attached* before the emulator can use it. "Attaching" a disk or tape image is like "virtually" inserting a diskette or a cassette into the disk drive or the tape recorder: once an image is attached, the emulator is able to use it as a storage media.

There are five commands that deal with disk and tape images:

- Attach Disk Image
- Detach Disk Image
- Attach Tape Image
- Detach Tape Image
- Smart-attach a file

The first four commands are used to insert and remove the virtual disks and cassettes from the respective units. On the other hand, the last commands tries to guess the type of the image you are attaching from its name and size, and attaches it to the most reasonable device.

Supported formats are `D64`, `G64`, `P64` for disk images (devices 8, 9, 10 and 11) and `T64` and `TAP` for tape images.

The ancient `X64` format is deprecated and subject for removal (and support for it is disabled at compile time by default).

The legacy `T64` format is supported partially, and it has several drawbacks. (see Section 17.2 [T64], page 382) It is only supported *read-only*, and the cassette is automatically rewound when you reach its end. Most importantly `T64` support is only available via virtual device traps, which have to be enabled in the options. (The emulator will temporarily enable it when autostarting programs from these containers.)

For actually emulating tape, the `TAP` format is highly recommended.

You can attach a disk for which you do not have write permissions: when this happens, the 1541 emulator will emulate a write-protected disk. This is also useful if you want to

prevent certain disk images from being written to; in the latter case, just remove the write permission for that file, e.g., by doing a `chmod a-w`.

4.5.1 "Autostarting" an image

If you want to reset the machine and run the first program on a certain image without typing any commands at the Commodore BASIC prompt, you can use the "Autostart" button in the file selector window after selecting a proper disk or tape image file.

If true drive emulation is turned on, and the *Handle TDE at Autostart* setting is enabled, true drive emulation will be turned off before loading the program and then turned on again after it has been loaded. This way, you get the maximum possible speed while loading the file, but you do not lose compatibility once the program itself is running. This method is not completely safe, because some autostarting methods might cause the true drive emulation not to be turned on again, which is why it is disabled by default.

4.5.2 Using compressed files

It is also possible to attach disk or tape images that have been compressed through various algorithms; compression formats are identified from the file extension. The following formats are supported (the expected file name extension is in parenthesis):

- GNU Zip (`.gz` or `.z`);
- BZip version 2 (`.bz2`);
- PkZip (`.zip`);
- GNU Zipped TAR archives (`.tar.gz`, `.tgz`);
- Zoo (`.zoo`).

PkZip, `tar.gz`, `lha` and `zoo` support is *read-only* and always uses the *first* `T64` or `D64` file in the archive. So archives containing multiple files will always be handled as if they contain only a single file.

Windows and DOS don't contain the needful programs to handle compressed archives. Get gzip and unzip for Windows and for DOS at `http://infozip.sourceforge.net`. Don't use pkunzip for DOS, it doesn't work. The programs to use BZip2 archives may be found at `https://sourceware.org/bzip2/`. Just put the programs (`unzip.exe`, `gzip.exe`, `bzip2.exe`) into a directory of your search path (e.g. `C:\DOS` or `C:\WINDOWS\COMMAND`; have a look at the PATH variable).

4.5.3 Using Zipcode and Lynx images

Since version 0.15, the VICE emulators have been able to attach disks packed with Zipcode or Lynx directly, removing the need to manually convert them into `D64` files with `c1541`. This is achieved by automatically invoking `c1541`, letting it decode the file into a temporary image and attaching the resulting temporary image read-only. For this to work, the directory containing `c1541` must be in your `PATH`.

This uses the `-unlynx` and `-unzip` options of `c1541` (see Section 14.3 [c1541 commands and options], page 366); these commands are not very reliable yet, and could fail with certain kinds of Lynx and Zipcode images (for example, they cannot deal with `DEL` files properly). So please use them with caution.

Lynx files usually come as `.lnx` files which are unpacked into single disk images. On the other hand, Zipcode files do not have a particular extension (although `.z64` is sometimes used), and represent a disk by means of component files, named as follows:

- `1!NAME`
- `2!NAME`
- `3!NAME`
- `4!NAME`

If you attach as a disk image (or smart-attach) any one of these files, the emulator will simply pick up the other three (by examining the name) and then build a disk image using all four.

4.6 Using printer emulation

- Go to "Preferences -> Peripheral devices -> Printer", select the printer you want to use. Note that not all types of printers are available at all ports/device numbers.
- Enable either "Virtual (traps)" or "Virtual (bus)" (There is no "True Printer Emulation")
- Set "Emulation type" to "File system access" if you want to output to a file or pipe through a program. If you want to use a real printer via OpenCBM, select "Real device (OpenCBM)".
- Set "Driver" to the type of printer you want to emulate. Use "Raw" when talking to OpenCBM. Use "ASCII" when writing into a simple text file.
- Select the "Output mode" to "Graphics" if you want to produce a .bmp file. For all other practical purposes use "Text".
- Select the "Output device". This is one of 3 strings which can be either:
  - a file name. When the graphics output is used, 3 digits will be appended to the name, and a new file generated for each new page.

    `print.dump`
  - if the string starts with a pipe character, it is used as a series of shell commands to pipe the output to. This only works for the "Text" output mode!

    `|petlp -F PS |lpr`

Now you are ready to print something. For example:

```
open4,4
cmd4:list
print#4,"test":close4
```

Note that, when printing to a file, the data may only get fully flushed after a form feed occurs. This occurs automatically when a new page is started, or when the emulator is closed. You may also trigger a form feed manually in the printer settings dialog.

4.7 Resetting the machine

You can reset the emulated machine at any time by using the “Reset” command from the file menu. There are two types of reset:

- *Reset*, which simply resets the CPU and all the other chips;

- *Power cycle*, which also clears up the contents of RAM.

A *Reset* is the same as a hardware reset achieved by pulling the RESET line down; a *Power cycle* is more like a power on/power off sequence in that it makes sure the whole RAM is cleared.

It is possible that a reset may not be enough to take the machine to the OS initialization sequence: in such cases, you will have to do a power cycle instead.

This is especially the case for the CBM-II emulators. Those machines examine a memory location and if they find a certain "magic" value they only do what you know from the C64 as `Run/Stop-Restore`. Therefore, to really reset a CBM-II use power cycle.

5 Quick setup

This chapter is mostly aimed at people who are used to older VICE releases. The default configuration has shifted from "make the emulator fast enough to work on consumer hardware" to "pedantic focus on emulation accuracy", which is why newer releases have been perceived as "slower" by some people.

However, a couple things can be reverted and "improved" - if better performance is what you can afford to trade for a little less emulation accuracy. **Note that if you encounter any strange problems, you should always try to reset settings to default first.**

- To speed up loading .prg files, enable Options->Host->Autostart->Autostart PRG Settings->Autostart mode->Inject into RAM (`AutostartPrgMode` = 1)
- To speed up loading .d64 files, enable Options->Peripherial devices->Drive->Virtual (traps) (`TrapDevice8..11` = 1) and enable Options->Host->Autostart->Handle True Drive Emulation on autostart (`AutostartHandleTrueDriveEmulation` = 1)
- To speed up warp mode, disable Options->Audio->Sound driver->Enable sound emulation in warpmode (`SoundEmulateOnWarp` = 0)

Last not least (does not really fit into the above list):

- If you are coming from WinVICE to GTK3, you might want to restore the original WinVICE hotkeys. Go to Options->Host->Hotkeys->Load From, and navigate to data/hotkeys/hotkeys-winvice-legacy.vhk

6 Settings and resources

In the VICE emulators, all the settings are stored in entities known as called *resources*. Each resource has a name and a value which may be either an integer or a string. Integer values are often used as boolean values with the usual convention of using zero for "false" and any other value for "true".

Resource values can be changed via the the *options* menu, via command-line options, using monitor commands, or via the *resource file*.

The *resource file* is a human-readable file containing resource values: On windows systems it is called `vice.ini` and is usually stored in the users roaming profile directory (something like `C:\Users\<username>\AppData\Roaming\vice`). Additionally the emulators will look in the program directory at startup, and use `vice.ini` if it exists there. On unix systems the resource file is called `vicerc` and is stored in the users config directory according to the XDG specification (usually `/home/<username>/.config/vice/`). Additionally the emulators will look if `.vicerc` exists in the users home directory, and use that if so.

It is possible to dump the current values of the resources into that file or load the values stored into that file as the current values, at any time. This is achieved with the "Save settings" and "Load settings" right menu items. A third menu item, "Restore Default Settings", can be used to reset all the values to the factory defaults.

Note that, when saving the settings to a new file, using "Save settings to...", only the resources for the currently running emulator will be stored into this file, and the settings related to other emulators (which might exist in a previously loaded file) will not be copied to it.

You can also store some extra settings, for example to configure a certain hardware setup, in an extra file, and load these using "Load extra settings from...". In this case the resources will not be reset to default first and only the resources present in that file will be changed/used.

A special resource, `SaveResourcesOnExit`, if set to a non zero value, causes the emulator to save the current (changed) settings before exiting, and can be toggled with the "Save settings on exit" item from the options menu.

6.1 Format of resource files

A resource file is made up of several sections; sections have the purpose of separating the resources of a certain emulator from the ones of the other emulators. A section starts with the name of an emulator in brackets (e.g., '`[C64]`') and ends when another section starts or when the file ends.

Every line in a section has the following format:

```
RESOURCE=VALUE
```

where `RESOURCE` is the name of a resource and `VALUE` is its assigned value. Resource names are case-sensitive and resource values are either strings or integers. Strings must start and end with a double quote character (`"`), while integers must be given in decimal notation.

Here is an example of a stripped-down `.vice/vicerc` file:

```
[VIC20]
SaveResourcesOnExit=0
```

```
FileSystemDevice8=1
FSDevice8ConvertP00=1
FSDevice8Dir="/home/ettore/cbm/stuff/vic20p00"
FSDevice8SaveP00=1
FSDevice8HideCBMFiles=1
[C64]
SaveResourcesOnExit=1
FileSystemDevice8=1
FSDevice8ConvertP00=1
FSDevice8Dir="/home/ettore/cbm/stuff/c64p00"
FSDevice8SaveP00=1
FSDevice8HideCBMFiles=1
```

Notice that, when resource values are saved with "Save settings", the emulator only modifies its own section, leaving the others unchanged.

6.2 Using command-line options to change resources

Resources can also be changed via command-line options.

Command-line options always override the defaults from `.vice/vicerc`, and their assignments last for the whole session. So, if you specify a certain command-line option that changes a certain resource from its default value and then use "Save Settings", the value specified with the command-line option will be saved back to the resource file.

Command-line options can begin with with a minus sign ('`-`') or with a plus sign ('`+`'). Options beginning with a minus sign may require an additional parameter, while the ones beginning with the plus sign never require one.

Moreover, options beginning with a plus sign always have a counterpart with the same name, but with a minus sign; in that case, the option beginning with a minus sign is used to *enable* a certain feature, while the one beginning with a plus sign is used to *disable* the same feature (this is an X11 convention). For example, `-warp` enables initial warp mode, while `+warp` disables it.

6.3 Autostart settings

6.3.1 Autostart resources

All these resources are available for all emulators except vsid.

`AutostartPrgDiskImage`
: String specifying the filename of the disk image used when autostarting a prg file and "copy to D64" is enabled (all emulators except vsid).

`AutostartBasicLoad`
: Boolean, if true load to basic start using ,8 when autostarting from disk, else use ,8,1 to load absolute. (all emulators except vsid).

`AutostartTapeBasicLoad`
: Boolean, if true load to basic start using ,1 when autostarting from tape, else use ,1,1 to load absolute. (all emulators except vsid).

`AutostartRunWithColon`
: Boolean, if true put a colon after the load command when autostarting (all emulators except vsid).

`AutostartHandleTrueDriveEmulation`
: Boolean, if true handle (enable/disable) True Drive Emulation on autostart (all emulators except vsid). This is disabled by default to ensure maximum compatibility. It *must* be enabled, if true drive emulation is enabled, and `AutostartPrgMode` is 0.

`AutostartWarp`
: Boolean, if true temporarily enable warp mode when autostarting (all emulators except vsid).

`AutostartPrgMode`
: Integer specifying the autostart mode for prg files (all emulators except vsid). (0: virtual filesystem, 1: inject to RAM, 2: copy to D64) Note that `AutostartHandleTrueDriveEmulation` must be enabled, if true drive emulation is enabled.

`AutostartDelayRandom`
: Boolean, enables a short (0-10 frames) random delay on autostart. This is added to the `AutostartDelay` (all emulators except vsid).

`AutostartDelay`
: Integer specifying the delay in seconds required to wait for the kernal reset routine before autostart. (0: use builtin value for standard kernal) When tweaking this value start with 'large' values and then lower it, a value that is too small results in autostart not happening. (all emulators except vsid). (0..1000)

`AutostartOnDoubleClick`
: Use autostart when double clicking on a file in the file list. (only in the GTK3 GUI) (all emulators except vsid)

`AutostartDropMode`
: Integer specifying autostart behaviour when dropping an image on the emulator window using drag-n-drop (all emulators except vsid). (0: attach only, 1: attach and load, 2: attach, load and run)

6.3.2 Autostart command-line options

All these command-line options are available for all emulators except vsid.

`-autostartprgdiskimage <Name>`
: Set disk image for autostart of PRG files (`AutostartPrgDiskImage`) (all emulators except vsid).

`-basicload`
: On autostart from disk, load to BASIC start (without ',1') (`AutostartBasicLoad=1`) (all emulators except vsid).

`+basicload`
: On autostart from disk, load with ',1' (`AutostartBasicLoad=0`) (all emulators except vsid).

`-tapebasicload`
: On autostart from tape, load to BASIC start (without ',1') (`AutostartTapeBasicLoad=1`) (all emulators except vsid).

`+tapebasicload`
: On autostart from tape, load with ',1' (`AutostartTapeBasicLoad=0`) (all emulators except vsid).

`-autostarttapoffset <Offset>`
: Set initial offset in the .tap file (useful for emulator frontends)

`-autostartwithcolon`
: On autostart, use the 'RUN' command with a colon, i.e., 'RUN:' (`AutostartRunWithColon=1`). (all emulators except vsid)

`+autostartwithcolon`
: On autostart, do not use the 'RUN' command with a colon; i.e., 'RUN' (`AutostartRunWithColon=0`) (all emulators except vsid).

`-autostart-handle-tde`
`+autostart-handle-tde`
: Handle/Do not handle True Drive Emulation on autostart (`AutostartHandleTrueDriveEmulation=1`, `AutostartHandleTrueDriveEmulation=0`) (all emulators except vsid).

`-autostart-warp`
`+autostart-warp`
: Enable/disable warp mode during autostart (`AutostartWarp=1`, `AutostartWarp=0`) (all emulators except vsid).

`-autostartprgmode <Mode>`
: Set autostart mode for PRG files (`AutostartPrgMode`) (all emulators except vsid). (0: virtual filesystem, 1: inject to RAM, 2: copy to D64)

`-autostart-delay-random`
`+autostart-delay-random`
: Enable/disable random delay on autostart (`AutostartDelayRandom`) (all emulators except vsid).

`-autostart-delay <seconds>`
: Set initial autostart delay in seconds for the kernal reset routine before autostart. (0: use builtin value for standard kernal). When tweaking this value start with 'large' values and then lower it, a value that is too small results in autostart not happening. (`AutostartDelay`) (all emulators except vsid). (0..1000)

`-autostart-on-doubleclick`
`+autostart-on-doubleclick`
: Enable/disable using autostart when double clicking on a file in the file list. (`AutostartOnDoubleClick`) (only in the GTK3 GUI) (all emulators except vsid)

`-autostart-drop-mode <mode>`
: Set autostart behaviour on dropping an image onto the emulator window (`AutostartDropMode`) (all emulators exceot vsid). (0/"attach": attach only, 1/"load": attach and load, 2/"run": attach, load and run)

6.4 Performance settings

It is possible to control the emulation speed by using the "Maximum speed" item in the speed setting. The default setting is `100`, which causes the emulation to never run faster than the real machine. A higher value allows the emulator to run faster, a lower one may force it to run slower. A specific FPS may also be targeted.

Moreover, a special *warp speed* mode is provided and can be toggled with the "Enable Warp Mode" menu item. If this mode is enabled, it will cause the emulator to disable any speed limit, turn sound output off and use a 1/10 refresh rate, so that it will run at the maximum possible speed.

6.4.1 Performance resources

`Speed`
: Integer specifying the maximum relative speed, as a percentage. `0` stands for "no limit".

`InitialWarpMode`
: Booolean specifying whether "warp mode" is initially enabled.

6.4.2 Performance command-line options

`-speed <value>`
: A positive <value> specifies the maximum speed as a percentage. A negative <value> results in a target FPS of 0 - <value> (`Speed`).

`-warp`
`+warp`
: Enable/Disable the initial warp mode.

6.5 Video settings

The following right-button menu items control the video output. On emulators that include two video chips (like `x128`) all options exist twice, once for each chip.

- "Video Cache" enables a video cache that can speed up the emulation when little graphics activity is going on; it is especially useful when you run the emulator on a networked X terminal as it can reduce the network bandwidth required. However, this setting can actually make the emulator slower when there is little graphics activity and the amount of work needed to maintain the cache is greater than the amount of work that would be wasted by not using it (if any).
- "Double Size" toggles *double-size mode*, which makes the emulation window twice as big. It also selects a more accurate CRT emulation renderer with scanlines.
- "Double Scan" toggles *double-scan mode*, which causes the emulator to draw only odd lines when running in double-size mode (this saves some CPU time and also makes the emulation window look more like an old monitor).

6.6 Keyboard settings

It is possible to specify whether the "positional" or "symbolic" keyboard mapping should be used with the "Keyboard mapping type" submenu (see Section 1.2 [Keyboard emulation], page 5, for an explanation of positional and symbolic mappings).

The keyboard settings also allows you to:

- Load custom-made positional and symbolic keymap files ("Set symbolic keymap file" and "Set positional keymap file").
- Dump the current keymap to a user-defined keymap file ("Dump to keymap file").

6.6.1 Keyboard resources

`KeymapIndex`
: Integer identifying which keymap is being used. (0: symbolic, 1: positional, 2: symbolic (user), 3: positional (user)).

`KeymapSymFile`
: String specifying the name of the keymap file for the symbolic mapping (see Section 1.2 [Keyboard emulation], page 5, (this will be set indirectly by changing `KeymapIndex`, `KeyboardMapping` or `KeyboardType`).

`KeymapPosFile`
: String specifying the name of the keymap file for the positional mapping (see Section 1.2 [Keyboard emulation], page 5, (this will be set indirectly by changing `KeymapIndex`, `KeyboardMapping` or `KeyboardType`).

`KeymapUserSymFile`
: String specifying the name of the user keymap file for symbolic mapping (see Section 1.2 [Keyboard emulation], page 5,

`KeymapUserPosFile`
: String specifying the name of the user keymap file for positional mapping (see Section 1.2 [Keyboard emulation], page 5,

`KeyboardMapping`
: Integer specifying the keyboard layout of the host. (0: American 1: British 2: Danish 3: Dutch 4: Finnish 5: French 6: German 7: Italian 8: Norwegian 9: Spanish 10: Swedish 11: Swiss 12: Turkish).

`KeyboardType`
: Integer specifying the emulated type of keyboard. (machine specific, currently always 0 except for xpet: 0: Business (us) 1: Business (uk) 2: Business (de) 3: Business (jp) 4: Graphics (us)).

6.6.2 Keyboard command-line options

`-keymap <number>`
: Specifies which keymap is being used (`KeymapIndex` 0: symbolic, 1: positional, 2: symbolic (user), 3: positional (user)).

`-symkeymap <Name>`
: Specify filename of the symbolic user keymap file (`KeymapUserSymFile`).

`-poskeymap <Name>`
: Specify filename of the positional user keymap file (`KeymapUserPosFile`).

`-keyboardmapping <number>`
: Specifies the keyboard layout of the host (`KeyboardMapping` (0: American 1: British 2: Danish 3: Dutch 4: Finnish 5: French 6: German 7: Italian 8: Norwegian 9: Spanish 10: Swedish 11: Swiss 12: Turkish).

`-keyboardtype <number>`
: Specifies the emulated type of keyboard (`KeyboardType` machine specific, currently always 0 except for xpet: 0: Business (us) 1: Business (uk) 2: Business (de) 3: Business (jp) 4: Graphics (us)).

6.7 Control port settings

The control port settings submenu allows you to select which control port device is connected to a control port.

6.7.1 Control port resources

`JoysticksAreSwapped`
: Boolean indicating whether the first two joysticks/controllers are swapped.

`JoyPort1Device`
: Set the device attached to: control port 1 (x64, x64sc, x64dtv, xscpu64, x128, xcbm5x0, xplus4) control port (xvic). (Section 6.7.3 [Control port and devices], page 66).

`JoyPort2Device`
: Set the device attached to: control port 2 (x64, x64sc, x64dtv, xscpu64, x128, xcbm5x0, xplus4) (Section 6.7.3 [Control port and devices], page 66).

`JoyPort3Device`
: Set the device attached to: joystick adapter port 1 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xpet, xvic, xplus4) (Section 6.7.3 [Control port and devices], page 66).

`JoyPort4Device`
: Set the device attached to: joystick adapter port 2 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xpet, xvic, xplus4) (Section 6.7.3 [Control port and devices], page 66).

`JoyPort5Device`
: Set the device attached to: joystick adapter port 3 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xpet, xvic, xplus4) (Section 6.7.3 [Control port and devices], page 66).

`JoyPort6Device`
: Set the device attached to: joystick adapter port 4 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xvic) Sid Cartridge on the Plus4 (xplus4) (Section 6.7.3 [Control port and devices], page 66).

`JoyPort7Device`
: Set the device attached to: joystick adapter port 5 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xvic) (Section 6.7.3 [Control port and devices], page 66).

`JoyPort8Device`
: Set the device attached to: joystick adapter port 6 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xvic) (Section 6.7.3 [Control port and devices], page 66).

`JoyPort9Device`
: Set the device attached to: joystick adapter port 7 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xvic) (Section 6.7.3 [Control port and devices], page 66).

`JoyPort10Device`
: Set the device attached to: joystick adapter port 8 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xvic) (Section 6.7.3 [Control port and devices], page 66).

`JoyPort11Device`
: Set the device attached to: joystick adapter port 9 (xplus4) (Section 6.7.3 [Control port and devices], page 66).

`BBRTCSave`
: Enable saving of the battery-backed real time clock data.

6.7.2 Control port command-line options

`-swapjoysticks`
`+swapjoysticks`
: Swap/do not swap the first two joysticks/controllers. (`JoysticksAreSwapped`)

`-controlport1device <device>`
: Set the device attached to: control port 1 (x64, x64sc, x64dtv, xscpu64, x128, xcbm5x0, xplus4) control port (xvic) (`JoyPort1Device`) (Section 6.7.3 [Control port and devices], page 66).

`-controlport2device <device>`
: Set the device attached to: control port 2 (x64, x64sc, x64dtv, xscpu64, x128, xcbm5x0, xplus4) (`JoyPort2Device`) (Section 6.7.3 [Control port and devices], page 66).

`-controlport3device <device>`
: Set the device attached to: joystick adapter port 1 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xpet, xvic, xplus4) (`JoyPort3Device`) (Section 6.7.3 [Control port and devices], page 66).

`-controlport4device <device>`
: Set the device attached to: joystick adapter port 2 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xpet, xvic, xplus4) (`JoyPort4Device`) (Section 6.7.3 [Control port and devices], page 66).

`-controlport5device <device>`
: Set the device attached to: joystick adapter port 3 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xpet, xvic, xplus4) (`JoyPort5Device`) (Section 6.7.3 [Control port and devices], page 66).

`-controlport6device <device>`
: Set the device attached to: joystick adapter port 4 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xvic) Sid Cartridge on the Plus4 (xplus4) (`JoyPort6Device`) (Section 6.7.3 [Control port and devices], page 66).

`-controlport7device <device>`
: Set the device attached to: joystick adapter port 5 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xvic) (`JoyPort7Device`) (Section 6.7.3 [Control port and devices], page 66).

`-controlport8device <device>`
: Set the device attached to: joystick adapter port 6 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xvic) (`JoyPort8Device`) (Section 6.7.3 [Control port and devices], page 66).

`-controlport9device <device>`
: Set the device attached to: joystick adapter port 7 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xvic) (`JoyPort9Device`) (Section 6.7.3 [Control port and devices], page 66).

`-controlport10device <device>`
: Set the device attached to: joystick adapter port 8 (x64, x64sc, xscpu64, x128, x64dtv, xcbm2, xcbm5x0, xvic) (`JoyPort10Device`) (Section 6.7.3 [Control port and devices], page 66).

`-controlport11device <device>`
: Set the device attached to: joystick adapter port 9 (xplus4) (`JoyPort11Device`) (Section 6.7.3 [Control port and devices], page 66).

`-bbrtcsave`
`+bbrtcsave`
: Enable/Disable saving of the battery-backed real time clock data (`BBRTCSave`).

6.7.3 Control port and devices

x64/x64sc/xscpu64 control ports and devices:

nr	device	N1	N2	I1	I2	I3	I4	I5	I6	I7	I8	E1	E2	E3	E4	E5	E6
0	None	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
1	Joystick	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
2	Paddles	X	X														
3	Mouse (1351)	X	X														
4	Mouse (NEOS)	X	X														
5	Mouse (AMIGA)	X	X	X	X	X	X	X	X	X	X						
6	Trackball (CX22)	X	X	X	X	X	X	X	X	X	X						
7	Mouse (ST)	X	X	X	X	X	X	X	X	X	X						

8	Mouse (SmartMouse)	X	X														
9	Mouse (Micromys)	X	X														
10	KoalaPad	X	X														
11	Lightpen (up trigger)	X															
12	Lightpen (left trigger)	X															
13	Lightpen (Datel)	X															
14	Lightgun (Magnum Phaser)	X															
15	Lightgun (Stack Rifle)	X															
16	Lightpen (Inkwell)	X															
18	Sampler (2bit)	X	X	X	X	X	X	X	X	X	X						
19	Sampler (4bit)	X	X	X	X	X	X	X	X	X	X						
20	RTC (BBRTC)	X	X														
21	Dongle (Paperclip64D)	X	X														
22	Keypad (Coplin)	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
23	Keypad (Cardco)	X	X														
24	Keypad (CX85)	X	X														
25	Keypad (RushWare)	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
26	Keypad (CX21)	X	X														
27	Dongle (Script 64)	X	X														
28	Dongle (VizaWrite 64)	X	X														
29	Dongle (WaaSoft)	X	X														
30	SNES Pad (TrapThem)	X	X														
31	SNES Pad (Ninja)	X	X														
32	Spaceballs joy adapter	X	X														
33	Inception joy adapter	X	X														
34	MultiJoy adapter	X	X														
36	ProtoPad	X	X														
37	I/O Simulation device	X	X														
38	Microflyte joystick	X	X														
39	Dongle (Paperclip64E)	X	X														
40	Dongle (Paperclip64SC)	X	X														
41	Dongle (Paperclip2)	X	X														
42	Chalkboard Powerpad	X	X														
44	Mouse (Joystick mouse)	X	X														

x128 control ports and devices:

nr	**device**	**N1**	**N2**	**I1**	**I2**	**I3**	**I4**	**I5**	**I6**	**I7**	**I8**	**E1**	**E2**	**E3**	**E4**	**E5**	**E6**
0	None	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
1	Joystick	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
2	Paddles	X	X														
3	Mouse (1351)	X	X														
4	Mouse (NEOS)	X	X														
5	Mouse (AMIGA)	X	X	X	X	X	X	X	X	X	X						
6	Trackball (CX22)	X	X	X	X	X	X	X	X	X	X						
7	Mouse (ST)	X	X	X	X	X	X	X	X	X	X						
8	Mouse (SmartMouse)	X	X														

9	Mouse (Micromys)	X	X								
10	KoalaPad	X	X								
11	Lightpen (up trigger)	X									
12	Lightpen (left trigger)	X									
13	Lightpen (Datel)	X									
14	Lightgun (Magnum Phaser)	X									
15	Lightgun (Stack Rifle)	X									
16	Lightpen (Inkwell)	X									
18	Sampler (2bit)	X	X	X	X	X	X	X	X	X	X
19	Sampler (4bit)	X	X	X	X	X	X	X	X	X	X
20	RTC (BBRTC)	X	X								
21	Dongle (Paperclip64D)	X	X								
27	Dongle (Script 64)	X	X								
28	Dongle (VizaWrite 64)	X	X								
29	Dongle (WaaSoft)	X	X								
30	SNES Pad (TrapThem)	X	X								
31	SNES Pad (Ninja)	X	X								
32	Spaceballs joy adapter	X	X								
33	Inception joy adapter	X	X								
34	MultiJoy adapter	X	X								
36	ProtoPad	X	X								
37	I/O Simulation device	X	X								
38	Microflyte joystick	X	X								
39	Dongle (Paperclip64E)	X	X								
40	Dongle (Paperclip64SC)	X	X								
41	Dongle (Paperclip2)	X	X								
42	Chalkboard Powerpad	X	X								
44	Mouse (Joystick mouse)	X	X								

x64dtv control ports and devices:

nr	**device**	**N1**	**N2**	**E1**
0	None	X	X	X
1	Joystick	X	X	X
22	Keypad (Coplin)	X	X	X
25	Keypad (RushWare)	X	X	X
37	I/O Simulation device	X	X	

xcbm2/xpet can handle the following devices on all control ports:

nr	**device**
0	None
1	Joystick

xcbm5x0 control ports and devices:

nr	**device**	**N1**	**N2**	**I1**	**I2**	**I3**	**I4**	**I5**	**I6**	**I7**	**I8**	**E1**	**E2**	**E3**	**E4**	**E5**	**E6**
0	None	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X

1	Joystick	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
2	Paddles	X	X														
3	Mouse (1351)	X	X														
4	Mouse (NEOS)	X	X														
5	Mouse (AMIGA)	X	X	X	X	X	X	X	X	X	X						
6	Trackball (CX22)	X	X	X	X	X	X	X	X	X	X						
7	Mouse (ST)	X	X	X	X	X	X	X	X	X	X						
8	Mouse (SmartMouse)	X	X														
9	Mouse (Micromys)	X	X														
10	KoalaPad	X	X														
18	Sampler (2bit)	X	X	X	X	X	X	X	X	X	X						
19	Sampler (4bit)	X	X	X	X	X	X	X	X	X	X						
20	RTC (BBRTC)	X	X														
30	SNES Pad (TrapThem)	X	X														
31	SNES Pad (Ninja)	X	X														
33	Inception joy adapter	X	X														
34	MultiJoy adapter	X	X														
36	ProtoPad	X	X														
37	I/O Simulation device	X	X														
38	Microflyte joystick	X	X														

xplus4 control ports and devices:

nr	**device**	**N1**	**N2**	**E1**	**E2**	**E3**	**E4**	**E5**	**E6**	**E7**	**E8**	**SC**
0	None	X	X	X	X	X	X	X	X	X	X	X
1	Joystick	X	X	X	X	X	X	X	X	X	X	X
2	Paddles											X
3	Mouse (1351)											X
4	Mouse (NEOS)											X
5	Mouse (AMIGA)	X	X									X
6	Trackball (CX22)	X	X									X
7	Mouse (ST)	X	X									X
8	Mouse (SmartMouse)											X
9	Mouse (Micromys)											X
10	KoalaPad											X
18	Sampler (2bit)	X	X									X
19	Sampler (4bit)	X	X									X
20	RTC (BBRTC)											X
22	Keypad (Coplin)	X	X									X
23	Keypad (Cardco)											X
24	Keypad (CX85)											X
25	Keypad (RushWare)	X	X									X
26	Keypad (CX21)											X
30	SNES Pad (TrapThem)											X
36	ProtoPad											X
37	I/O Simulation device	X	X									
38	Microflyte joystick											X

42	Chalkboard Powerpad										X					
44	Mouse (Joystick mouse)										X					

xvic control ports and devices:

nr	**device**	**N1**	**I1**	**I2**	**I3**	**I4**	**I5**	**I6**	**I7**	**I8**	**E1**	**E2**	**E3**	**E4**	**E5**	**E6**	**E7**
0	None	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
1	Joystick	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
2	Paddles	X															
3	Mouse (1351)	X															
4	Mouse (NEOS)	X															
5	Mouse (AMIGA)	X	X	X	X	X	X	X	X	X							
6	Trackball (CX22)	X	X	X	X	X	X	X	X	X							
7	Mouse (ST)	X	X	X	X	X	X	X	X	X							
8	Mouse (SmartMouse)	X															
9	Mouse (Micromys)	X															
10	KoalaPad	X															
11	Lightpen (up trigger)	X															
12	Lightpen (left trigger)	X															
13	Lightpen (Datel)	X															
14	Lightgun (Magnum Phaser)	X															
15	Lightgun (Stack Rifle)	X															
16	Lightpen (Inkwell)	X															
18	Sampler (2bit)	X	X	X	X	X	X	X	X	X							
19	Sampler (4bit)	X	X	X	X	X	X	X	X	X							
20	RTC (BBRTC)	X															
22	Keypad (Coplin)	X	X	X	X	X	X	X	X	X							
23	Keypad (Cardco)	X															
24	Keypad (CX85)	X															
25	Keypad (RushWare)	X	X	X	X	X	X	X	X	X							
26	Keypad (CX21)	X															
30	SNES Pad (TrapThem)	X															
31	SNES Pad (Ninja)	X															
32	Spaceballs joy adapter	X															
33	Inception joy adapter	X															
36	ProtoPad	X															
37	I/O Simulation device	X															
38	Microflyte joystick	X															
42	Chalkboard Powerpad	X															
44	Mouse (Joystick mouse)	X															

Meaning of abreviations used in the tables above:

abr	**meaning**
N1	Native control port 1
N2	Native control port 2
I1	Inception Joystick adapter port 1
I2	Inception Joystick adapter port 2

I3 Inception Joystick adapter port 3
I4 Inception Joystick adapter port 4
I5 Inception Joystick adapter port 5
I6 Inception Joystick adapter port 6
I7 Inception Joystick adapter port 7
I8 Inception Joystick adapter port 8
E1 Other Joystick adapter port 1
E2 Other Joystick adapter port 2
E3 Other Joystick adapter port 3
E4 Other Joystick adapter port 4
E5 Other Joystick adapter port 5
E6 Other Joystick adapter port 6
E7 Other Joystick adapter port 7
E8 Other Joystick adapter port 8
SC xplus4 SidCart joystick port

6.8 Joystick settings

6.8.1 Joystick resources

`JoyDevice1`
`JoyDevice2`
`JoyDevice3`
`JoyDevice4`
`JoyDevice5`
`JoyDevice6`
`JoyDevice7`
`JoyDevice8`
`JoyDevice9`
`JoyDevice10`
`JoyDevice11`

Integer specifying which joystick device the emulator should use for joystick emulation for ports 1 - 10, respectively. (0=None, 1=Joystick 1, 2=Joystick 2, 4=Numpad, 8=Keyset 1, 16=Keyset 2 on OS/2) (0=None, 1=Numpad, 2=Keyset 1, 3=Keyset 2, 4=HID joystick 0, 5=HID joystick 1 on Mac OS X) The available joysticks might differ depending on operating system and joystick support in the OS (Linux joystick module must be available for example).

`JoyStick1AutoFire`
`JoyStick2AutoFire`
`JoyStick3AutoFire`
`JoyStick4AutoFire`
`JoyStick5AutoFire`
`JoyStick6AutoFire`
`JoyStick7AutoFire`
`JoyStick8AutoFire`
`JoyStick9AutoFire`
`JoyStick10AutoFire`
`JoyStick11AutoFire`
: Boolean, enables autofire for joystick emulation for ports 1 - 10, respectively. (0=Off, 1=On)

`JoyStick1AutoFireMode`
`JoyStick2AutoFireMode`
`JoyStick3AutoFireMode`
`JoyStick4AutoFireMode`
`JoyStick5AutoFireMode`
`JoyStick6AutoFireMode`
`JoyStick7AutoFireMode`
`JoyStick8AutoFireMode`
`JoyStick9AutoFireMode`
`JoyStick10AutoFireMode`
`JoyStick11AutoFireMode`
: Integer specifying the autofire mode to be used for for joystick emulation for ports 1 - 10, respectively. (0=Autofire when fire is pressed, 1=Permanent autofire except when fire is pressed)

`JoyStick1AutoFireSpeed`
`JoyStick2AutoFireSpeed`
`JoyStick3AutoFireSpeed`
`JoyStick4AutoFireSpeed`
`JoyStick5AutoFireSpeed`
`JoyStick6AutoFireSpeed`
`JoyStick7AutoFireSpeed`
`JoyStick8AutoFireSpeed`
`JoyStick9AutoFireSpeed`
`JoyStick10AutoFireSpeed`
`JoyStick11AutoFireSpeed`
: Integer specifying the amount of fire button presses per second for autofire for joystick emulation for ports 1 - 10, respectively. (1-255)

`JoyOpposite`
: Boolean, if true allow (usually impossible) bitcombinations for opposite directions. (all emulators except vsid)

`Mouse` Boolean, enables mouse emulation

`SmartMouseRTCSave`
: Boolean, specified whether to save real time clock data for the SmartMouse

`PaddlesInput1`
: Integer speciftying the host input for the potentiometers of joystick port 1 (all emulators except xcbm2 and xpet) (0: Mouse, 1: Joystick axis)

`PaddlesInput2`
: Integer speciftying the host input for the potentiometers of joystick port 2 (all emulators except xcbm2, xplus4 and xvic) (0: Mouse, 1: Joystick axis)

`KeySet1NorthWest`
`KeySet1North`
`KeySet1NorthEast`
`KeySet1East`
`KeySet1SouthEast`
`KeySet1South`
`KeySet1SouthWest`
`KeySet1West`
`KeySet1Fire`
`KeySet1Fire2`
`KeySet1Fire3`
`KeySet1Fire4`
`KeySet1Fire5`
`KeySet1Fire6`
`KeySet1Fire7`
`KeySet1Fire8`
: Integers specifying the keycodes for keyset 1 (all emulators except vsid).

`KeySet2NorthWest`
`KeySet2North`
`KeySet2NorthEast`
`KeySet2East`
`KeySet2SouthEast`
`KeySet2South`
`KeySet2SouthWest`
`KeySet2West`
`KeySet2Fire`
`KeySet2Fire2`
`KeySet2Fire3`
`KeySet2Fire4`
`KeySet2Fire5`
`KeySet2Fire6`
`KeySet2Fire7`
`KeySet2Fire8`
: Integers specifying the keycodes for keyset 2 (all emulators except vsid).

`KeySetEnable`
: Boolean that specifies whether user defined keysets are enabled (all emulators except vsid).

`KbdbufDelay`
: Integer specifying the additional keyboard delay. (0: use default)

6.8.2 Joystick command-line options

`-joydev1 <range>`
`-joydev2 <range>`
: Set the device for joystick emulation of port 1 and 2, respectively. (`JoyDevice1`, `JoyDevice2`).

`-extrajoydev1 <0-8>`
`-extrajoydev2 <0-8>`
`-extrajoydev3 <0-8>`
`-extrajoydev4 <0-8>`
`-extrajoydev5 <0-8>`
`-extrajoydev6 <0-8>`
`-extrajoydev7 <0-8>`
`-extrajoydev8 <0-8>`
`-extrajoydev9 <0-8>`
: Set device for extra joystick port 1, 2, 3, 4, 5, 6, 7 and 8. (`JoyDevice3`, `JoyDevice4`, `JoyDevice5`, `JoyDevice6`, `JoyDevice7`, `JoyDevice8`, `JoyDevice9`, `JoyDevice10`, `JoyDevice11`).

`-joystick1autofire`
`+joystick1autofire`
: Enable/disable autofire for native joystick port 1 (`JoyStick1AutoFire=1`, `JoyStick1AutoFire=0`). (all emulators except xcbm2, xpet and vsid)

`-joystick2autofire`
`+joystick2autofire`
: Enable/disable autofire for native joystick port 2 (`JoyStick2AutoFire=1`, `JoyStick2AutoFire=0`). (all emulators except xcbm2, xpet, xvic and vsid)

`-extrajoystick1autofire`
`+extrajoystick1autofire`
: Enable/disable autofire for joystick adapter port 1 (`JoyStick3AutoFire=1`, `JoyStick3AutoFire=0`). (all emulators except vsid)

`-extrajoystick2autofire`
`+extrajoystick2autofire`
: Enable/disable autofire for joystick adapter port 2 (`JoyStick4AutoFire=1`, `JoyStick4AutoFire=0`). (all emulators except vsid)

`-extrajoystick3autofire`
`+extrajoystick3autofire`
: Enable/disable autofire for joystick adapter port 3 or plus4 sidcard joystick port (`JoyStick5AutoFire=1`, `JoyStick5AutoFire=0`). (all emulators except vsid)

`-extrajoystick4autofire`
`+extrajoystick4autofire`
: Enable/disable autofire for joystick adapter port 4 (`JoyStick6AutoFire=1`, `JoyStick6AutoFire=0`). (all emulators except vsid)

`-extrajoystick5autofire`
`+extrajoystick5autofire`
Enable/disable autofire for joystick adapter port 5 (`JoyStick7AutoFire=1`, `JoyStick7AutoFire=0`). (all emulators except vsid)

`-extrajoystick6autofire`
`+extrajoystick6autofire`
Enable/disable autofire for joystick adapter port 6 (`JoyStick8AutoFire=1`, `JoyStick8AutoFire=0`). (all emulators except vsid)

`-extrajoystick7autofire`
`+extrajoystick7autofire`
Enable/disable autofire for joystick adapter port 7 (`JoyStick9AutoFire=1`, `JoyStick9AutoFire=0`). (all emulators except vsid)

`-extrajoystick8autofire`
`+extrajoystick8autofire`
Enable/disable autofire for joystick adapter port 8 (`JoyStick10AutoFire=1`, `JoyStick10AutoFire=0`). (all emulators except vsid)

`-extrajoystick9autofire`
`+extrajoystick9autofire`
Enable/disable autofire for joystick adapter port 9 (`JoyStick11AutoFire=1`, `JoyStick11AutoFire=0`). (xplus4 only)

`-joystick1autofiremode <Mode>`
Set autofire mode for native joystick port 1 (`JoyStick1AutoFireMode`) (all emulators except xcbm2, xpet and vsid). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-joystick2autofiremode <Mode>`
Set autofire mode for native joystick port 2 (`JoyStick2AutoFireMode`) (all emulators except xcbm2, xpet, xvic and vsid). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-extrajoystick1autofiremode <Mode>`
Set autofire mode for joystick adapter port 1 (`JoyStick3AutoFireMode`) (all emulators except vsid). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-extrajoystick2autofiremode <Mode>`
Set autofire mode for joystick adapter port 2 (`JoyStick4AutoFireMode`) (all emulators except vsid). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-extrajoystick3autofiremode <Mode>`
Set autofire mode for joystick adapter port 3 or plus4 sidcard joystick port (`JoyStick5AutoFireMode`) (all emulators except vsid). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-extrajoystick4autofiremode <Mode>`
: Set autofire mode for joystick adapter port 4 (`JoyStick6AutoFireMode`) (all emulators except vsid). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-extrajoystick5autofiremode <Mode>`
: Set autofire mode for joystick adapter port 5 (`JoyStick7AutoFireMode`) (all emulators except vsid). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-extrajoystick6autofiremode <Mode>`
: Set autofire mode for joystick adapter port 6 (`JoyStick8AutoFireMode`) (all emulators except vsid). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-extrajoystick7autofiremode <Mode>`
: Set autofire mode for joystick adapter port 7 (`JoyStick9AutoFireMode`) (all emulators except vsid). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-extrajoystick8autofiremode <Mode>`
: Set autofire mode for joystick adapter port 8 (`JoyStick10AutoFireMode`) (all emulators except vsid). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-extrajoystick9autofiremode <Mode>`
: Set autofire mode for joystick adapter port 9 (`JoyStick11AutoFireMode`) (xplus4 only). (0: Autofire when fire is pressed, 1: Permanent autofire except when fire is pressed)

`-joystick1autofirespeed <1-255>`
`-joystick2autofirespeed <1-255>`
: Set the amount of presses per second for autofire for joystick emulation of port 1 and 2, respectively. (`JoyStick1AutoFireSpeed`, `JoyStick2AutoFireSpeed`).

`-extrajoystick1autofirespeed <1-255>`
`-extrajoystick2autofirespeed <1-255>`
`-extrajoystick3autofirespeed <1-255>`
`-extrajoystick4autofirespeed <1-255>`
`-extrajoystick5autofirespeed <1-255>`
`-extrajoystick6autofirespeed <1-255>`
`-extrajoystick7autofirespeed <1-255>`
`-extrajoystick8autofirespeed <1-255>`
`-extrajoystick9autofirespeed <1-255>`
: Set the amount of presses per second for autofire for extra joystick port 1, 2, 3, 4, 5, 6, 7, 8 and 9. (`JoyStick3AutoFireSpeed`, `JoyStick4AutoFireSpeed`, `JoyStick5AutoFireSpeed`, `JoyStick6AutoFireSpeed`, `JoyStick7AutoFireSpeed`, `JoyStick8AutoFireSpeed`, `JoyStick9AutoFireSpeed`, `JoyStick10AutoFireSpeed`, `JoyStick11AutoFireSpeed`).

`-joyopposite`
`+joyopposite`
: Enable/disable opposite joystick directions (`JoyOpposite=1`, `JoyOpposite=0`). (all emulators except vsid)

`-mouse`
`+mouse`
: Enable/Disable mouse grab

`-smartmousertcsave`
`+smartmousertcsave`
: Enable/Disable saving of the real time clock data for the SmartMouse (`SmartMouseRTCSave=1`, `SmartMouseRTCSave=0`)

`-paddles1inputmouse`
: Set the host input for the potentiometers of joystick port 1 to the mouse. (`PaddlesInput1=0`)

`-paddles1inputmouse`
: Set the host input for the potentiometers of joystick port 1 to a joystick axis. (`PaddlesInput1=1`)

`-paddles2inputmouse`
: Set the host input for the potentiometers of joystick port 2 to the mouse. (`PaddlesInput2=0`)

`-paddles2inputmouse`
: Set the host input for the potentiometers of joystick port 2 to a joystick axis. (`PaddlesInput2=1`)

`-keyset`
`+keyset`
: Enable/disable user defined keyset (`KeySetEnable=1`, `KeySetEnable=0`) (all emulators except vsid).

`-keybuf-delay <value>`
: Set additional keyboard buffer delay (`KbdbufDelay`). (0: use default)

6.9 Sound settings

The following menu items control sound output:

- "Enable sound playback" turns sound emulation on and off.
- "Sound synchronization" specifies the method for syncronizing the sound playback. Possible settings are:
  - "Flexible", i.e., the audio renderer flexibly adds/removes samples to the output to smoothly adapt the playback to slight changes in the speed of the emulator.
  - "Adjusting" works like "flexible", but supports bigger differences in speed. For example, if the emulation speed drops down from from 100% to 50%, audio slows down by the same amount too.
  - "Exact", instead, makes the audio renderer output always the same sounds you would hear from the real thing, without trying to adapt the ratio; to compensate the tolerances in speed, some extra frames will be skipped or added.

- "Sample rate" specifies the sampling frequency, ranging from 8000 to 48000 Hz (not all the sound cards and/or sound drivers can support all the frequencies, so actually the nearest candidate will be chosen).
- "Buffer size" specifies the size of the audio buffer; the bigger the buffer, the longer the delay with which sounds are played. You should pick the smallest value your machine can handle without problems.
- "Sound suspend time", will cause the audio playback to pause for the specified number of seconds whenever some clicking happens. If "Keep going" is selected, no pausing is done.

  The following menu items control sound input:
- "Sampler Device" specifies the host device/method used for sampling/sound input, currently portaudio (if linked in) and file methods are supported.
- "Sampler Gain" specifies the amount of gain (increase the input volume if above 100 or decrease the input volume if below 100) for the input of the sampler device.
- "Sampler File" specifies the name of the file to be uses as an input source for the 'file' method/device.

6.9.1 Sound resources

`Sound` Boolean specifying whether audio emulation is turned on.

`SoundEmulateOnWarp`
: Boolean specifying whether sound chips should be emulated in warp mode. (0: do not emulate sound chips in warp mode, 1: emulate sound chips also in warp mode)

`SoundSampleRate`
: Integer specifying the sampling frequency in Hz (not all the sound cards and/or sound drivers can support all the frequencies, so actually the nearest candidate will be chosen). (8000..48000)

`SoundBufferSize`
: Integer specifying the size of the audio buffer, in milliseconds.

`SoundDeviceName`
: String specifying the audio driver.

  Implemented drivers are:

  - `alsa`, for the linux ALSA sound driver.
  - `beos`, for the BeOS/Zeta/Haiku sound driver.
  - `bsp`, for the BeOS/Zeta/Haiku BeOS Media Kit sound driver.
  - `coreaudio`, for the Mac OS X sound driver (`SoundDeviceArg` specifies the audio device, default system output by default).
  - `dummy`, fully emulating the sound output chip(s), but not actually playing samples.
  - `dump`, writing all the write accesses to the registers to a file (specified by `SoundDeviceArg`, default value is `vicesnd.sid`);
  - `dx`, for the Windows Direct-X sound driver.

- `netbsd`, for the NetBDS audio device (unfinished; `SoundDeviceArg` specifies the audio device, `/dev/audio` by default).
- `pulse`, for the Pulseaudio sound driver.
- `sdl`, for the Simple DirectMedia Layer audio driver.
- `sun`, for the Solaris audio device (unfinished; `SoundDeviceArg` specifies the audio device, `/dev/audio` by default).
- `wmm`, for the Windows Multimedia Waveout sound device.

These drivers will actually be present only if the VICE configuration script detected the corresponding development support at the time of compilation.

`SoundDeviceArg`
String specifying an additional parameter for the audio driver (see `SoundDeviceName`).

`SoundRecordDeviceName`
String specifying the driver used for sound recording.

Implemented drivers are:

- `aiff`, for the Apple Interchange File Format 16bit sound recorder driver.
- `fs`, writing samples to a file (specified by `SoundDeviceArg`; default is `vicesnd.raw`);
- `iff`, for the Amiga Interchange File Format (8SVX) 8bit sound recorder driver.
- `mp3`, for the MP3 sound recorder driver.
- `flac`, for the FLAC sound recorder driver.
- `ogg`, for the ogg/vorbis sound recorder driver.
- `voc`, for the Creative Voice (VOC) sound recorder driver.
- `wav`, for the RIFF/WAV sound recorder driver.

These drivers will actually be present only if the VICE configuration script detected the corresponding development support at the time of compilation.

`SoundRecordDeviceArg`
String specifying additional arguments for sound recording.

`SoundFragmentSize`
Integer specifying the fragment size. (0: very small, 1: small, 2: medium, 3: large, 4: very large)

`SoundVolume`
Integer specifying the master volume in percent. (0..100)

`SoundOutput`
Integer specifying the type of sound output. Output is selectable between 'system' (system decides to use mono or stereo output based on the presence of a stereo sid), 'always mono' (output is always mono, stereo streams are mixed into a mono stream) or 'always stereo' (output is always stereo, mono streams are multiplexed to a stereo stream). (0: system, 1: mono, 2: stereo)

`SamplerDevice`
: Integer specifying the device/method to be used for sound input. (0: sample file device, 1: PortAudio device)

`SamplerGain`
: Integer specifying the gain to be used for sound input. (>100 increase input volume, <100: decrease input volume)

`SampleName`
: String specifying the name of the file/sample to be used as the input source for the 'file' sampler device.

6.9.2 Sound command-line options

`-sound`
`+sound` Enable/disable sound emulation (`Sound=1`, `Sound=0`).

`-soundwarpmode <mode>`
: Specify if sound chips should be emulated in warp mode. (`SoundEmulateOnWarp`). (0: do not emulate sound chips in warp mode, 1: emulate sound chips also in warp mode)

`-soundrate <value>`
: Specify the sound playback sample rate (`SoundSampleRate`). (8000..48000)

`-soundoutput <output mode>`
: Sound output mode (`SoundOutput`). (0: system decides mono/stereo, 1: always mono, 2: always stereo)

`-soundbufsize <value>`
: Specify the size of the audio buffer in milliseconds (`SoundBufferSize`).

`-soundfragsize <value>`
: Set sound fragment size (`SoundFragmentSize`). (0: very small, 1: small, 2: medium, 3: large, 4: very large)

`-sounddev <Name>`
: Specifies the name of the audio device (`SoundDeviceName`). (alsa, beos, bsp, coreaudio, dummy, dump, dx, netbsd, pulse, sdl, sun, wmm)

`-soundarg <args>`
: Specifies an additional parameter for the audio device (`SoundDeviceArg`).

`-soundrecdev <name>`
: Specify recording sound driver (`SoundRecordDeviceName`). (aiff, fs, iff, mp3, flac, ogg, voc, wav)

`-soundrecarg <args>`
: Specify initialization parameters for recording sound driver (`SoundRecordDeviceArg`).

`-soundvolume <volume>`
: Specify the sound volume (`SoundVolume`). (0..100)

`-samplerdev <device number>`
: Specify the device to use for audio input (`SamplerDevice`). (0: file device, 1: portaudio device)

`-samplergain <percent>`
: Specify the amount of gain (volume increase/decrease) for the audio input device (`SamplerGain`). (0..200)

`-samplename <name>`
: Specify the name of the file to use for the 'file' audio input device (`SampleFile`).

6.10 Tape settings

These settings are used to control the hardware-level emulation of the Tape drive.

6.10.1 Tape resources

`DatasetteResetWithCPU`
: Boolean specifying whether to reset (rewind) the tape when resetting the CPU.

`DatasetteZeroGapDelay`
: Integer specifying the delay in cycles for a zero gap length in the tap. Mostly relevant for v0 .tap files which could not properly encode gaps > 255. The default is 2500, which is what "mtap" is using.

`DatasetteSpeedTuning`
: Integer specifying the deviation from the correct motor speed. 100000 equals 100% deviation. Default is 0.

`DatasetteTapeWobbleFrequency`
: Integer specifying the tape wobble frequency. 1000 equals 1Hz, default is 3Hz.

`DatasetteTapeWobbleAmplitude`
: Integer specifying the tape wobble amplitude. 100000 equals 100% deviation of the correct motor speed. Default is 0.5%.

`DatasetteTapeAzimuthError`
: Integer specifying the amount of azimuth alignment error. This is right now very inaccurately simulated by adding a random amount of cycles. 1000 equals +/- one cycle. Default is 0.

`DatasetteSound`
: Boolean specifying whether to produce audible sound when playing a tape on the datasette

`DatasetteSoundVolume`
: Integer specifying the volume of the tape sound. Meaningful values are in the range 1-32767

6.10.2 Tape command-line options

`-dsresetwithcpu`
`+dsresetwithcpu`
: Enable/disable automatic Datasette-Reset (`DatasetteResetWithCPU=1`, `DatasetteResetWithCPU=0`).

`-dszerogapdelay <value>`
: Set delay in cycles for a zero gap in the tap. Mostly relevant for v0 .tap files which could not properly encode gaps > 255. (`DatasetteZeroGapDelay`).

`-dsspeedtuning <value>`
: Set amount of deviation from the correct motor speed. 100000 equals 100% deviation. (`DatasetteSpeedTuning`).

`-dstapewobblefreq <value>`
: Set tape wobble frequency. 1000 equals 1Hz, default is 3Hz. (`DatasetteTapeWobbleFrequency`).

`-dstapewobbleamp <value>`
: Set tape wobble amplitude. 100000 equals 100% deviation of the correct motor speed. (`DatasetteTapeWobbleAmplitude`).

`-dstapeerror <value>`
: Set tape alignment error. This is right now very inaccurately simulated by adding a random amount of cycles. 1000 equals +/- one cycle. (`DatasetteTapeAzimuthError`).

`-datasettesound`
`+datasettesound`
: Enable/disable Datasette sound emulation (`DatasetteSound=1`, `DatasetteSound=0`).

`-dssoundvolume <value>`
: Set the volume of the Datasette sound (`DatasetteSoundVolume`).

6.11 Drive settings

These settings are used to control the hardware-level emulation of the Disk drives. When hardware-level emulation is turned on, only drives 8 and 9 are being emulated.

The following settings affect both drives:

- "Enable true drive emulation" enables the (slow) hardware-level emulation of the drives for maximum compatibility. This must be turned on for any of the following settings to have effect.
- "Drive sync factor" specifies the speed of the drive's CPU. This can be used to help loading certain programs that have trouble with the default PAL setting (for example, programs designed for NTSC machines). The ratio is calculated as follows:

  ```
  sync_factor = 65536 * clk_drive / clk_machine
  ```

  where `clk_drive` and `clk_machine` are clock speeds in MHz. The menu lets you choose between the PAL and NTSC values, and also lets you specify whatever value you want. Be careful when changing it, though, because a wrong value can break things and even corrupt disk images.

The following settings, instead, are specific of each drive:

- "Drive model" specifies the model of the drive being emulated. **Warning:** This will reset the drive.

- "Enable parallel cable" enables emulation of a SpeedDOS parallel cable; if you switch this option on and replace the original Commodore ROMs with SpeedDOS-compatible ones, you can speed up loading/saving times.
- "Idle method" specifies which method the drive emulation should use to save CPU cycles in the host CPU. There are four methods:
  - *No idle*: The drives execute side by side with the main cpu, no matter what. So basically the drive works exactly as with the real thing, and nothing is done to reduce the power needs of the drive emulation. This is the default.
  - *Skip cycles*: Each time the serial line is accessed by the C64, the drive executes all the cycles since the last time it ran. If the number of elapsed cycles is larger than a certain value, the drive discards part of them.
  - *Trap idle*: The disk drive is still emulated upon serial line accesses as with the previous option, but it is also always emulated at the end of each screen frame. If the drive gets into the DOS idle loop, only pending interrupts are emulated to save time.
  - *Frame idle*: Again, the disk drive is still emulated upon serial line accesses as with the previous option, but it is also always emulated at the end of each video frame. This was the default for many years, but actually has no real advantage over *No idle*.

  The second option ("Skip cycles") is usually best for performance, as the drive is emulated as little as possible; on the other hand, you may notice sudden slowdowns (when the drive executes several cycles at once) and the LED status is never updated (because it would not be possible to do correctly so). Moreover, if the drive tries to get in sync with the computer in some weird way and the computer does not access the serial line for a long time, it is possible that some cycles are discarded and the sync is lost. Notice that this hack will have no effect on performance if a program continuously reads from the IEC port, as the drive will have to be fully emulated in any case (some stupid programs do this, even when they don't actually need to use the drive).

  The third option ("Trap idle") is usually a bit slower, as at least interrupts are always emulated, but ensures the LED state is always updated correctly and always keeps the drive and the computer in sync. On the other hand, if a program installs a non-standard idle loop in the drive, the drive CPU has to be emulated even when not necessary and the global emulation speed is then *much* slower.
- "40-track image support" specifies how 40-track ("extended") disk images should be supported. There are three possible ways:
  - "Never extend" never extends disk images at all (so if a program tries to write tracks beyond the 35th, it is not allowed to do so);
  - "Ask on extend" prompts the user as soon as a program tries to write tracks beyond the 35th, and the user can then choose whether he wants the disk image to be extended or not;
  - "Extend on access" simply extends the disk image as soon the program needs it, without prompting the user.

6.11.1 Drive resources

`Drive8TrueEmulation`
`Drive9TrueEmulation`
`Drive10TrueEmulation`
`Drive11TrueEmulation`
: Boolean controlling whether the "true" drive emulation is turned on.

`DriveSoundEmulation`
: Boolean controlling whether the drive noise emulation is turned on (all emulators except vsid).

`DriveSoundEmulationVolume`
: Integer specifying the volume of the drive noise emulation (all emulators except vsid). (0..4000, 4000 equals 100.0%.)

`Drive8Type`
`Drive9Type`
`Drive10Type`
`Drive11Type`
: Integers specifying the model number for drives 8 to 11. Possible values are `1541` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `1542` (1541-II) [all emulators except xcbm2, xcbm5x0, xpet and vsid], `1570` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `1571` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `1573` (1571CR) [x128 only], `1551` [xplus4 only], `1581` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `1001` [all emulators except x64dtv, xplus4 and vsid], `2000` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `2031` [all emulators except x64dtv, xplus4 and vsid], `2040` [all emulators except x64dtv, xplus4 and vsid], `3040` [all emulators except x64dtv, xplus4 and vsid], `4000` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `4040` [all emulators except x64dtv, xplus4 and vsid], `4844` (CMD HD) [all emulators except xcbm2, xcbm5x0, xpet and vsid], `8050` [all emulators except x64dtv, xplus4 and vsid], `8250` [all emulators except x64dtv, xplus4 and vsid]. `9000` [all emulators except x64dtv, xplus4 and vsid].

`Drive8RTCSave`
`Drive9RTCSave`
`Drive10RTCSave`
`Drive11RTCSave`
: Integer specifying whether the RTC data of drive 8 to 11 should be saved when changed or not (drive type 2000/4000/4844 only).

`Drive8ParallelCable`
`Drive9ParallelCable`
`Drive10ParallelCable`
`Drive11ParallelCable`
: integers controlling what type of parallel cable is emulated for drives 8 to 11 (x64, x64sc, xscpu64, x128 and xplus4 only). x64, x64sc, xscpu64, x128: (0: None, 1: Standard, 2: Dolphin DOS 3, 3: Formel64) xplus4: (0: None, 1: Standard)

`Drive8DolphinDOS3`
`Drive9DolphinDOS3`
`Drive10DolphinDOS3`
`Drive11DolphinDOS3`
: Booleans controlling whether Dolphin DOS 3 is emulated or not for drives 8 to 11 (x64, x64sc, xscpu64 and x128 only).

`Drive8ProfDOS`
`Drive9ProfDOS`
`Drive10ProfDOS`
`Drive11ProfDOS`
: Booleans controlling whether Professional DOS is emulated or not for drives 8 to 11 (x64, x64sc, xscpu64 and x128 only).

`Drive8SuperCard`
`Drive9SuperCard`
`Drive10SuperCard`
`Drive11SuperCard`
: Booleans controlling whether Supercard is emulated or not for drives 8 to 11 (x64, x64sc, xscpu64 and x128 only).

`Drive8StarDos`
`Drive9StarDos`
`Drive10StarDos`
`Drive11StarDos`
: Booleans controlling whether StarDOS is emulated or not for drives 8 to 11 (x64, x64sc, xscpu64 and x128 only).

`Drive8RAM2000`
`Drive9RAM2000`
`Drive10RAM2000`
`Drive11RAM2000`
: Booleans controlling whether a RAM block is emulated at $2000-$3FFF for drive 8 to 11.

`Drive8RAM4000`
`Drive9RAM4000`
`Drive10RAM4000`
`Drive11RAM4000`
: Booleans controlling whether a RAM block is emulated at $4000-$5FFF for drive 8 to 11.

`Drive8RAM6000`
`Drive9RAM6000`
`Drive10RAM6000`
`Drive11RAM6000`
: Booleans controlling whether a RAM block is emulated at $6000-$7FFF for drive 8 to 11.

`Drive8RAM8000`
`Drive9RAM8000`
`Drive10RAM8000`
`Drive11RAM8000`
: Booleans controlling whether a RAM block is emulated at $8000-$9FFF for drive 8 to 11.

`Drive8RAMA000`
`Drive9RAMA000`
`Drive10RAMA000`
`Drive11RAMA000`
: Booleans controlling whether a RAM block is emulated at $A000-$BFFF for drive 8 to 11.

`Drive8ExtendImagePolicy`
`Drive9ExtendImagePolicy`
`Drive10ExtendImagePolicy`
`Drive11ExtendImagePolicy`
: Integer specifying the policy for 40-track support for drives 8 to 11. (0: never extend, 1: ask on extend, 2: extend on access)

`Drive8IdleMethod`
`Drive9IdleMethod`
`Drive10IdleMethod`
`Drive11IdleMethod`
: Integers specifying the idling method for the drive CPU. See Section 6.11 [Drive settings], page 82. (0: none, 1: skip cycles, 2: trap idle, 3: frame idle)

`Drive8RPM`
`Drive9RPM`
`Drive10RPM`
`Drive11RPM`
: Integers specifying the rotation speed of the drive, multiplied by 100, so 300rpm equals 30000.

`Drive8WobbleFrequency`
`Drive9WobbleFrequency`
`Drive10WobbleFrequency`
`Drive11WobbleFrequency`
: Integers specifying the drive wobble frequency. 1000 equals 1Hz.

`Drive8WobbleAmplitude`
`Drive9WobbleAmplitude`
`Drive10WobbleAmplitude`
`Drive11WobbleAmplitude`
: Integers specifying the drive wobble amplitude. 1000 equals +/- 1 RPM.

`Drive8FixedSize`
`Drive9FixedSize`
`Drive10FixedSize`
`Drive11FixedSize`

Integer specifying the size of the diskimage (in bytes) returned to SCSI capacity inquiries for CMD HDs. A disk image must be attached for a valid size to be returned. A value of "0" will report back the current image size. Values may also be followed by the suffix "K", "M", or "G" for kilobyte, megabyte, and gigabyte respectively. Default is "8G".

`DosName1540`
`DosName1541`
`DosName1541ii`
`DosName1570`
`DosName1571`
`DosName1581`
`DosName2000`
`DosName4000`
`DosNameCMDHD`

Strings specifying the names of the ROM images for the drive emulation. (all emulators except xcbm2, xcbm5x0, xpet and vsid)

`DosName1551`

String specifying the name of the ROM image for the drive emulation. (xplus4 only)

`DosName1571cr`

String specifying the name of the ROM image for the drive emulation. (x128 only)

`DosName2031`
`DosName2040`
`DosName3040`
`DosName4040`
`DosName1001`
`DosName9000`

Strings specifying the names of the ROM images for the drive emulation. (all emulators except x64dtv, xplus4 and vsid)

`DriveProfDOS1571Name`

String specifying the filename of the 1571 professional DOS ROM image (x64, x64sc, xscpu64 and x128 only).

`DriveSuperCardName`

String specifying the filename of the Supercard ROM image (x64, x64sc, xscpu64 and x128 only).

`DriveStarDosName`

String specifying the filename of the image of the lower half of the StarDOS ROM (x64, x64sc, xscpu64 and x128 only).

`FliplistName`
: String specifying the filename of the current flip list. (Drive 8 only) (all emulators except vsid).

`AttachDevice8d0Readonly`
`AttachDevice9d0Readonly`
`AttachDevice10d0Readonly`
`AttachDevice11d0Readonly`
: Booleans that specify whether to attach images on drives 8 to 11 read-only or not (all emulators except vsid).

`AttachDevice8d1Readonly`
`AttachDevice9d1Readonly`
`AttachDevice10d1Readonly`
`AttachDevice11d1Readonly`
: Booleans that specify whether to attach images on the second drive of dual-drives 8 to 11 read-only or not (all emulators except vsid).

6.11.2 Drive command-line options

`-drive8truedrive, +drive8truedrive`
`-drive9truedrive, +drive9truedrive`
`-drive10truedrive, +drive10truedrive`
`-drive11truedrive, +drive11truedrive`
: Enable/disable true drive emulation (`Drive8TrueEmulation=1`, `Drive8TrueEmulation=0`, `Drive9TrueEmulation=1`, `Drive9TrueEmulation=0`, `Drive10TrueEmulation=1`, `Drive10TrueEmulation=0`, `Drive11TrueEmulation=1`, `Drive11TrueEmulation=0`).

`-drivesound`
`+drivesound`
: Enable/disable drive sound emulation (`DriveSoundEmulation=1`, `DriveSoundEmulation=0`) (all emulators except vsid).

`-drivesoundvolume <Volume>`
: Set the volume of the drive sound emulation. (4000 equals 100.0%.) (`DriveSoundEmulationVolume=0..4000`) (all emulators except vsid).

`-drive8type <Type>`
`-drive9type <Type>`
`-drive10type <Type>`
`-drive11type <Type>`
: Specifies the drive types for drives 8-11, respectively. Possible values for `TYPE` are `1541` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `1542` (meaning 1541-II) [all emulators except xcbm2, xcbm5x0, xpet and vsid], `1551` [xplus4 only], `1570` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `1571` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `1573` (meaning 1571cr) [x128 only], `1581` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `2000` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `4000` [all emulators except xcbm2, xcbm5x0, xpet and vsid], `4844` (meaning CMD HD) [all emulators

except xcbm2, xcbm5x0, xpet and vsid], `2031` [all emulators except x64dtv, xplus4 and vsid], `2040` [all emulators except x64dtv, xplus4 and vsid], `3040` [all emulators except x64dtv, xplus4 and vsid], `4040` [all emulators except x64dtv, xplus4 and vsid], `1001` [all emulators except x64dtv, xplus4 and vsid], `8050` [all emulators except x64dtv, xplus4 and vsid] `8250` [all emulators except x64dtv, xplus4 and vsid]. and `9000` [all emulators except x64dtv, xplus4 and vsid].

`-drive8rtcsave, +drive8rtcsave`
`-drive9rtcsave, +drive9rtcsave`
`-drive10rtcsave, +drive10rtcsave`
`-drive11rtcsave, +drive11rtcsave`
Enable/disable the saving of the RTC data for drive 8 to 11 when changed (drive type 2000/4000/4844 only) (`Drive8RTCSave=1`, `Drive8RTCSave=0`, `Drive9RTCSave=1`, `Drive9RTCSave=0`, `Drive10RTCSave=1`, `Drive10RTCSave=0`, `Drive11RTCSave=1`, `Drive11RTCSave=0`).

`-parallel8 <type>`
`-parallel9 <type>`
`-parallel10 <type>`
`-parallel11 <type>`
Set parallel cable type for drives 8-11 respectively (`Drive8ParallelCable`, `Drive9ParallelCable`, `Drive10ParallelCable`, `Drive11ParallelCable`) (x64, x64sc, xscpu64, x128 and xplus4 only). x64, x64sc, xscpu64, x128: (0: None, 1: Standard, 2: Dolphin DOS 3, 3: Formel64) xplus4: (0: None, 1: Standard)

`-drive8idle <method>`
`-drive9idle <method>`
`-drive10idle <method>`
`-drive11idle <method>`
Specifies `<method>` as the idling method for drives 8-11 respectively (`Drive8IdleMethod`, `Drive9IdleMethod`, `Drive10IdleMethod`), `Drive11IdleMethod`). (0: none, 1: skip cycles, 2: trap idle, 3: frame idle)

`-drive8extend <method>`
`-drive9extend <method>`
`-drive10extend <method>`
`-drive11extend <method>`
Specifies `<method>` as the track 40 extend policy in drives 8-11 respectively (`Drive8ExtendImagePolicy`, `Drive9ExtendImagePolicy`, `Drive10ExtendImagePolicy`, `Drive11ExtendImagePolicy`). (0: never extend, 1: ask on extend, 2: extend on access)

`-drive8rpm <rpm>`
`-drive9rpm <rpm>`
`-drive10rpm <rpm>`
`-drive11rpm <rpm>`
Specifies the rotation speed of the drive, multiplied by 100 so 300rpm equal 30000.

`-drive8wobblefrequency <frequency>`
`-drive9wobblefrequency <frequency>`
`-drive10wobblefrequency <frequency>`
`-drive11wobblefrequency <frequency>`
Specifies frequency of the drive wobble. 1000 equals 1Hz.

`-drive8wobbleamplitude <amplitude>`
`-drive9wobbleamplitude <amplitude>`
`-drive10wobbleamplitude <amplitude>`
`-drive11wobbleamplitude <amplitude>`
Specifies amplitude of the drive wobble. 1000 equals +/- 1RPM.

`-drive8fixedsize`
`-drive9fixedsize`
`-drive10fixedsize`
`-drive11fixedsize`
Specifies the size of the diskimage (in bytes) returned to SCSI capacity inquiries for CMD HDs. A disk image must be attached for a valid size to be returned. A value of "0" will report back the current image size. Values may also be followed by the suffix "K", "M", or "G" for kilobyte, megabyte, and gigabyte respectively. Default is "8G".

`-dos1540 <name>`
Specify the ROM name for the 1540 emulation (`DosName1540`). (all emulators except xcbm2, xcbm5x0, xpet and vsid)

`-dos1541 <name>`
Specify the ROM name for the 1541 emulation (`DosName1541`). (all emulators except xcbm2, xcbm5x0, xpet and vsid)

`-dos1541II <name>`
Specify the ROM name for the 1541-II emulation (`DosName1541ii`). (all emulators except xcbm2, xcbm5x0, xpet and vsid)

`-dos1551 <name>`
Specify the ROM name for the 1551 emulation (`DosName1551`). (xplus4 only)

`-dos1570 <name>`
Specify the ROM name for the 1570 emulation (`DosName1570`). (all emulators except xcbm2, xcbm5x0, xpet and vsid)

`-dos1571 <name>`
Specify the ROM name for the 1571 emulation (`DosName1571`). (all emulators except xcbm2, xcbm5x0, xpet and vsid)

`-dos1571cr <name>`
Specify the ROM name for the 1571 emulation (`DosName1571CR`). (x128 only)

`-dos1581 <name>`
Specify the ROM name for the 1581 emulation (`DosName1581`). (all emulators except xcbm2, xcbm5x0, xpet and vsid)

`-dos2000 <name>`
Specify the ROM name for the FD2000 emulation (`DosName2000`). (all emulators except xcbm2, xcbm5x0, xpet and vsid)

`-dos4000 <name>`
Specify the ROM name for the FD4000 emulation (`DosName4000`). (all emulators except xcbm2, xcbm5x0, xpet and vsid)

`-dosCMDHD <name>`
Specify the boot ROM name for the CMD HD emulation (`DosNameCMDHD`). (all emulators except xcbm2, xcbm5x0, xpet and vsid)

`-dos2031 <name>`
Specify the ROM name for the 2031 emulation (`DosName2031`). (all emulators except x64dtv, xplus4 and vsid)

`-dos2040 <name>`
Specify the ROM name for the 2040 emulation (`DosName2040`). (all emulators except x64dtv, xplus4 and vsid)

`-dos3040 <name>`
Specify the ROM name for the 3040 emulation (`DosName3040`). (all emulators except x64dtv, xplus4 and vsid)

`-dos4040 <name>`
Specify the ROM name for the 4040 emulation (`DosName4040`). (all emulators except x64dtv, xplus4 and vsid)

`-dos1001 <name>`
Specify the ROM name for the 1001, 8050 and 8250 emulations (`DosName1001`). (all emulators except x64dtv, xplus4 and vsid)

`-dos9000 <name>`
Specify the ROM name for the D9090/60 emulations (`DosName9000`). (all emulators except x64dtv, xplus4 and vsid)

`-drive8ram2000, +drive8ram2000`
`-drive9ram2000, +drive9ram2000`
`-drive10ram2000, +drive10ram2000`
`-drive11ram2000, +drive11ram2000`
Enable/disable 8KiB RAM expansion at $2000-$3FFF for drive 8 to 11 (`Drive8RAM2000=1`, `Drive8RAM2000=0`, `Drive9RAM2000=1`, `Drive9RAM2000=0`, `Drive10RAM2000=1`, `Drive10RAM2000=0`, `Drive11RAM2000=1`, `Drive11RAM2000=0`).

`-drive8ram4000, +drive8ram4000`
`-drive9ram4000, +drive9ram4000`
`-drive10ram4000, +drive10ram4000`
`-drive11ram4000, +drive11ram4000`
Enable/disable 8KiB RAM expansion at $4000-$5FFF for drive 8 to 11 (`Drive8RAM4000=1`, `Drive8RAM4000=0`, `Drive9RAM4000=1`, `Drive9RAM4000=0`, `Drive10RAM4000=1`, `Drive10RAM4000=0`, `Drive11RAM4000=1`, `Drive11RAM4000=0`).

`-drive8ram6000`, `+drive8ram6000`
`-drive9ram6000`, `+drive9ram6000`
`-drive10ram6000`, `+drive10ram6000`
`-drive11ram6000`, `+drive11ram6000`
Enable/disable 8KiB RAM expansion at $6000-$7FFF for drive 8 to 11 (`Drive8RAM6000=1`, `Drive8RAM6000=0`, `Drive9RAM6000=1`, `Drive9RAM6000=0`, `Drive10RAM6000=1`, `Drive10RAM6000=0`, `Drive11RAM6000=1`, `Drive11RAM6000=0`).

`-drive8ram8000`, `+drive8ram8000`
`-drive9ram8000`, `+drive9ram8000`
`-drive10ram8000`, `+drive10ram8000`
`-drive11ram8000`, `+drive11ram8000`
Enable/disable 8KiB RAM expansion at $8000-$9FFF for drive 8 to 11 (`Drive8RAM8000=1`, `Drive8RAM8000=0`, `Drive9RAM8000=1`, `Drive9RAM8000=0`, `Drive10RAM8000=1`, `Drive10RAM8000=0`, `Drive11RAM8000=1`, `Drive11RAM8000=0`).

`-drive8rama000`, `+drive8rama000`
`-drive9rama000`, `+drive9rama000`
`-drive10rama000`, `+drive10rama000`
`-drive11rama000`, `+drive11rama000`
Enable/disable 8KiB RAM expansion at $A000-$BFFF for drive 8 to 11 (`Drive8RAMA000=1`, `Drive8RAMA000=0`, `Drive9RAMA000=1`, `Drive9RAMA000=0`, `Drive10RAMA000=1`, `Drive10RAMA000=0`, `Drive11RAMA000=1`, `Drive11RAMA000=0`).

`-drive8dd3`, `+drive8dd3`
`-drive9dd3`, `+drive9dd3`
`-drive10dd3`, `+drive10dd3`
`-drive11dd3`, `+drive11dd3`
Enable/disable Professional DOS for drive 8 to 11 (`Drive8DolphinDOS3=1`, `Drive8DolphinDOS3=0`, `Drive9DolphinDOS3=1`, `Drive9DolphinDOS3=0`, `Drive10DolphinDOS3=1`, `Drive10DolphinDOS3=0`, `Drive11DolphinDOS3=1`, `Drive11DolphinDOS3=0`) (x64, x64sc, xscpu64 and x128 only).

`-drive8profdos`, `+drive8profdos`
`-drive9profdos`, `+drive9profdos`
`-drive10profdos`, `+drive10profdos`
`-drive11profdos`, `+drive11profdos`
Enable/disable Professional DOS for drive 8 to 11 (`Drive8ProfDOS=1`, `Drive8ProfDOS=0`, `Drive9ProfDOS=1`, `Drive9ProfDOS=0`, `Drive10ProfDOS=1`, `Drive10ProfDOS=0`, `Drive11ProfDOS=1`, `Drive11ProfDOS=0`) (x64, x64sc, xscpu64 and x128 only).

`-profdos1571 <name>`
Specify name of Professional DOS 1571 ROM image (`DriveProfDOS1571Name`) (x64, x64sc, xscpu64 and x128).

`-drive8supercard, +drive8supercard`
`-drive9supercard, +drive9supercard`
`-drive10supercard, +drive10supercard`
`-drive11supercard, +drive11supercard`
Enable/disable Supercard for drive 8 to 11 (`Drive8SuperCard=1`, `Drive8SuperCard=0`, `Drive9SuperCard=1`, `Drive9SuperCard=0`, `Drive10SuperCard=1`, `Drive10SuperCard=0`, `Drive11SuperCard=1`, `Drive11SuperCard=0`) (x64, x64sc, xscpu64 and x128 only).

`-supercard <name>`
Specify name of Supercard ROM image (`DriveSuperCardName`) (x64, x64sc, xscpu64 and x128 only).

`-drive8stardos, +drive8stardos`
`-drive9stardos, +drive9stardos`
`-drive10stardos, +drive10stardos`
`-drive11stardos, +drive11stardos`
Enable/disable StarDOS for drive 8 to 11 (`Drive8StarDos=1`, `Drive8StarDos=0`, `Drive9StarDos=1`, `Drive9StarDos=0`, `Drive10StarDos=1`, `Drive10StarDos=0`, `Drive11StarDos=1`, `Drive11StarDos=0`) (x64, x64sc, xscpu64 and x128 only).

`-stardos <name>`
Specify name of the image of the lower half of the StarDOS ROM. (Attach the upper half using the -dos1541 option.) (`DriveStarDosName`) (x64, x64sc, xscpu64 and x128 only).

`-flipname <name>`
Specify name of the flip list file image (Drive 8 only) (`FliplistName`) (all emulators except vsid).

6.12 Peripheral settings

VICE is able to support some special peripherals:

- *file system devices*, pseudo-drives accessing the Unix file system;
- printers.

These features depend on some *kernal traps* that replace the existing routines in the original Commodore operating system with custom-made C routines.

6.12.1 Settings for file system devices

These settings deal with the drive-like peripherals connected to the bus of the emulated machine. The first setting relates to the parallel IEEE488 interface. With this interface a special engine is used to listen to the bus lines to translates them to the filesystem code. Thus the PET will always detect a drive for example, but it can also use drives 10 and 11 even together with true disk drive emulation.

- "Enable virtual devices", enables the peripheral access via the fast disk emulation (either kernal traps or IEEE488 interface). Both, filesystem and disk image access via fast drive emulation, are affected.

Four peripherals, numbered from 8 to 11, are accessible; each of them provides the following settings:

- "File system access", if enabled, allows the device to emulate a drive accessing a file system directory; note that when a disk image is attached to the same drive, the directory is no longer visible and the attached disk is used instead.
- "File system directory" specifies the directory to be accessed by the drive.
- "Convert P00 file names", if enabled, allows access to P00 files using their built-in name instead of the Unix one.
- "Create P00 files on save", if enabled, creates P00 files (instead of raw CBM files) whenever a program creates a file.

Note that, by default, all drives create P00 files on save.

6.12.1.1 Resources for file system devices

`BusDevice8`
`BusDevice9`
`BusDevice10`
`BusDevice11`

Booleans that specify whether IEC or IEEE-488 (bus) device emulation for virtual device #8 to #11 is enabled.

`FileSystemDevice8`
`FileSystemDevice9`
`FileSystemDevice10`
`FileSystemDevice11`

Integers specifying the device type for device 8-11 respectively (all emulators except vsid). (0: None, 1: Filesystem, 2: OpenCBM (Real))

`FSDevice8ConvertP00`
`FSDevice9ConvertP00`
`FSDevice10ConvertP00`
`FSDevice11ConvertP00`

Booleans specifying whether on-read support for P00 files is enabled on drives 8, 9, 10 and 11 respectively (all emulators except vsid).

`FSDevice8SaveP00`
`FSDevice9SaveP00`
`FSDevice10SaveP00`
`FSDevice11SaveP00`

Booleans specifying whether the drives should create P00 files instead of plain CBM ones for drives 8, 9, 10 and 11 respectively (all emulators except vsid).

`FSDevice8HideCBMFiles`
`FSDevice9HideCBMFiles`
`FSDevice10HideCBMFiles`
`FSDevice11HideCBMFiles`

Booleans specifying whether non-P00 files should be invisible for drives 8, 9, 10 and 11 respectively (all emulators except vsid).

`FSDevice8Dir`
`FSDevice9Dir`
`FSDevice10Dir`
`FSDevice11Dir`
: Strings specifying the directories to which drives 8, 9, 10 and 11 have access (all emulators except vsid).

`FSDeviceLongNames`
: Boolean specifying whether filenames will get converted to 16 character long short names or not. Disabled by default.

`FSDeviceOverwrite`
: Boolean specifying whether files in the file system can be overwritten without using `"@:name"`, or not. Disabled by default.

6.12.1.2 Command-line options for file system devices

`-busdevice8`
`+busdevice8`
: Enable/disable IEC or IEEE-488 (bus) device emulation for device #8 (`BusDevice8=1`, `BusDevice8=0`).

`-busdevice9`
`+busdevice9`
: Enable/disable IEC or IEEE-488 (bus) device emulation for device #9 (`BusDevice9=1`, `BusDevice9=0`).

`-busdevice10`
`+busdevice10`
: Enable/disable IEC or IEEE-488 (bus) device emulation for device #10 (`BusDevice10=1`, `BusDevice10=0`).

`-busdevice11`
`+busdevice11`
: Enable/disable IEC or IEEE-488 (bus) device emulation for device #11 (`BusDevice11=1`, `BusDevice11=0`).

`-devicebackend8 <type>`
`-devicebackend9 <type>`
`-devicebackend10 <type>`
`-devicebackend11 <type>`
: Set backend device type for trap or bus device 8-11 respectively (`FileSystemDevice8`, `FileSystemDevice9`, `FileSystemDevice10`, `FileSystemDevice11`) (all emulators except vsid). (0: None, 1: Filesystem, 2: OpenCBM (Real))

`-fs8 <Name>`
`-fs9 <Name>`
`-fs10 <Name>`
`-fs11 <Name>`
: Specify the paths for the file system access on drives 8, 9, 10 and 11, respectively (`FSDevice8Dir`, `FSDevice9Dir`, `FSDevice10Dir` and `FSDevice11Dir`) (all emulators except vsid).

`-fs8convertp00`
`+fs8convertp00`
: Enable/disable on-read support for P00 files on drive 8 (`FSDevice8ConvertP00=1`, `FSDevice8ConvertP00=0`) (all emulators except vsid).

`-fs9convertp00`
`+fs9convertp00`
: Enable/disable on-read support for P00 files on drive 9 (`FSDevice9ConvertP00=1`, `FSDevice9ConvertP00=0`) (all emulators except vsid).

`-fs10convertp00`
`+fs10convertp00`
: Enable/disable on-read support for P00 files on drive 10 (`FSDevice10ConvertP00=1`, `FSDevice10ConvertP00=0`) (all emulators except vsid).

`-fs11convertp00`
`+fs11convertp00`
: Enable/disable on-read support for P00 files on drive 11 (`FSDevice11ConvertP00=1`, `FSDevice11ConvertP00=0`) (all emulators except vsid).

`-fs8savep00`
`+fs8savep00`
: Enable/disable saving P00 files on drive 8 (`FSDevice8SaveP00=1`, `FSDevice8SaveP00=0`) (all emulators except vsid).

`-fs9savep00`
`+fs9savep00`
: Enable/disable saving P00 files on drive 9 (`FSDevice9SaveP00=1`, `FSDevice9SaveP00=0`) (all emulators except vsid).

`-fs10savep00`
`+fs10savep00`
: Enable/disable saving P00 files on drive 10 (`FSDevice10SaveP00=1`, `FSDevice10SaveP00=0`) (all emulators except vsid).

`-fs11savep00`
`+fs11savep00`
: Enable/disable saving P00 files on drive 11 (`FSDevice11SaveP00=1`, `FSDevice11SaveP00=0`) (all emulators except vsid).

`-fs8hidecbm`
`+fs8hidecbm`
: Enable/disable hiding of CBM files for drive 8 (`FSDevice8HideCBMFiles=1`, `FSDevice8HideCBMFiles=0`) (all emulators except vsid).

`-fs9hidecbm`
`+fs9hidecbm`
: Enable/disable hiding of CBM files for drive 9 (`FSDevice9HideCBMFiles=1`, `FSDevice9HideCBMFiles=0`) (all emulators except vsid).

`-fs10hidecbm`
`+fs10hidecbm`
: Enable/disable hiding of CBM files for drive 10 (`FSDevice10HideCBMFiles=1`, `FSDevice10HideCBMFiles=0`) (all emulators except vsid).

`-fs11hidecbm`
`+fs11hidecbm`
: Enable/disable hiding of CBM files for drive 11 (`FSDevice11HideCBMFiles=1`, `FSDevice11HideCBMFiles=0`) (all emulators except vsid).

`-fslongnames`
`+fslongnames`
: Enable/disable whether filenames will get converted to 16 character long short names (`FSDeviceLongNames=1`, `FSDeviceLongNames=0`) (all emulators except vsid).

`-fsoverwrite`
`+fsoverwrite`
: Enable/disable whether existing files in the file system can be overwritten by default. Using `OPEN` or `SAVE"@:name"` this is always possible. (`FSDeviceOverwrite=1`, `FSDeviceOverwrite=0`) (all emulators except vsid).

6.12.2 Printer settings

The VICE emulators can emulate printers connected to either the IEC buffer or the user port. Emulation can be achieved by redirecting the printer output to a file or by piping it through an external process. This is defined by so-called *printer device file names*; a printer device file name can be either a simple path, or a command name preceeded by a pipe symbol '`|`'.

For example, printer device '`filename`' will cause the output to be appended to the file `filename`, while printer device '`|lpr`' will cause the `lpr` command to be executed and be fed the printer output. The printer output will not be converted but saved as printed by the emulated machine.

Up to three printer devices may be specified through the following resources:

- device 1, whose default value is `print.dump`;
- device 2, whose default value is `|lpr`.
- device 3, whose default value is `|petlp -F PS|lpr`;

So, basically, by default printer device 1 will dump printer output to `print.dump`; printer device 2 will print it via `lpr` directly to the printer and device 3 will print it via `petlp` (a not-yet-complete utility that will produce Postscript output from the Commodore printer code) and then to the printer via `lpr`.

6.12.2.1 Printer resources

`BusDevice4`
`BusDevice5`
`BusDevice6`
`BusDevice7`
: Booleans that specify whether IEC or IEEE-488 (bus) device emulation for device #4, #5, #6 and #7 is enabled.

`PrinterTextDevice1`
`PrinterTextDevice2`
`PrinterTextDevice3`
: Strings specifying the printer devices (see Section 6.12.2 [Printer settings], page 97).

`Printer4TextDevice`
`Printer5TextDevice`
`Printer6TextDevice`
: Integer (ranging from 0 to 2, for device 1-3) specifying what printer device (see Section 6.12.2 [Printer settings], page 97) the IEC printer is using.

`Printer4`
`Printer5`
`Printer6` Integer specifying how the printer (device 4-6) is being emulated. (0: None, 1: Filesystem, 2: Real)

`Printer7` Integer specifying how printer 7 is being emulated. (0: None, 2: Real)

`Printer4Driver`
: String specifying the printer output driver. (raw, ascii, 2022, 4023, 8023, mps801, mps802, mps803, nl10)

`Printer5Driver`
: String specifying the printer output driver. (raw, ascii, 2022, 4023, 8023, mps801, mps802, mps803, nl10)

`Printer6Driver`
: String specifying the printer output driver. (raw, 1520)

`Printer4Output`
`Printer5Output`
`Printer6Output`
: Strings specifying the IEC printer output device. (text, graphics)

`PrinterUserportTextDevice`
: Integer (ranging from 0 to 2, for device 1-3) specifying what printer device the user-port printer is using.

`PrinterUserportDriver`
: String specifying the user-port printer output driver. (ascii/nl10/raw)

`PrinterUserportOutput`
: String specifying the user-port printer output device. (text, graphics)

6.12.2.2 Printer command-line options

`-busdevice4`
`+busdevice4`
: Enable/disable IEC or IEEE-488 (bus) device emulation for device #4 (`BusDevice4=1`, `BusDevice4=0`).

`-busdevice5`
`+busdevice5`
: Enable/disable IEC or IEEE-488 (bus) device emulation for device #5 (`BusDevice5=1`, `BusDevice5=0`).

`-busdevice6`
`+busdevice6`
: Enable/disable IEC or IEEE-488 (bus) device emulation for device #6 (`BusDevice6=1`, `BusDevice6=0`).

`-busdevice7`
`+busdevice7`
: Enable/disable IEC or IEEE-488 (bus) device emulation for device #7 (`BusDevice7=1`, `BusDevice7=0`).

`-devicebackend4 <type>`
: Set device type for device 4 (`Printer4`). (0: None, 1: Filesystem, 2: Real)

`-devicebackend5 <type>`
: Set device type for device 5 (`Printer5`). (0: None, 1: Filesystem, 2: Real)

`-devicebackend6 <type>`
: Set device type for device 6 (`Printer6`). (0: None, 1: Filesystem, 2: Real)

`-devicebackend7 <type>`
: Set device type for device 7 (`Printer7`). (0: None, 2: Real)

`-prtxtdev1 <name>`
`-prtxtdev2 <name>`
`-prtxtdev3 <name>`
: Specify name of printer text device or dump file (`PrinterTextDevice1`, `PrinterTextDevice2`, `PrinterTextDevice3`).

`-pr4txtdev <0-2>`
`-pr5txtdev <0-2>`
`-pr6txtdev <0-2>`
: Specify printer text output device for IEC printer #4-6 (`Printer4TextDevice`, `Printer5TextDevice`, `Printer6TextDevice`).

`-pr4output <name>`
: Specify name of output device for device #4 (`Printer4Output`). (text, graphics)

`-pr5output <name>`
: Specify name of output device for device #5 (`Printer5Output`). (text, graphics)

`-pr6output <name>`
: Specify name of output device for device #6 (`Printer6Output`). (text, graphics)

`-pr4drv <name>`
: Specify name of printer driver for device #4 (`Printer4Driver`). (raw, ascii, 2022, 4023, 8023, mps801, mps802, mps803, nl10)

`-pr5drv <name>`
: Specify name of printer driver for device #5 (`Printer5Driver`). (raw, ascii, 2022, 4023, 8023, mps801, mps802, mps803, nl10)

`-pr6drv <name>`
: Specify name of printer driver for device #6 (`Printer6Driver`). (raw, 1520)

`-prusertxtdev <0-2>`
: Specify printer text output device for userport printer (`PrinterUserportTextDevice`).

`-pruseroutput <name>`
: Specify name of output device for the userport printer (`PrinterUserportOutput`). (text, graphics)

`-pruserdrv <name>`
: Specify name of printer driver for the userport printer (`PrinterUserportDriver`).

6.12.3 Disabling kernal traps

If you have compatibility problems, you can completely disable Kernal traps with the "Disable kernal traps" option. This will of course disable all the features that depend on it, such as the fast 1541 emulation (so you will have to turn true 1541 emulation on if you want to be able to read or write disk images) and tape (t64) support.

Since the 3.0 release kernal traps have been disabled by default to ensure maximum compatibility.

6.12.3.1 Resources to control Kernal traps

`TrapDevice1`
`TrapDevice2`
`TrapDevice4`
`TrapDevice5`
`TrapDevice6`
`TrapDevice7`
`TrapDevice8`
`TrapDevice9`
`TrapDevice10`
`TrapDevice11`
: Boolean specifying whether the kernal trap mechanism for virtual device emulation should be enabled. Serial IEC devices can use kernal traps.

6.12.3.2 Command-line options to control Kernal traps

`-trapdevice1`, `+trapdevice1`
`-trapdevice2`, `+trapdevice2`
`-trapdevice4`, `+trapdevice4`
`-trapdevice5`, `+trapdevice5`
`-trapdevice6`, `+trapdevice6`
`-trapdevice7`, `+trapdevice7`
`-trapdevice8`, `+trapdevice8`
`-trapdevice9`, `+trapdevice9`
`-trapdevice10`, `+trapdevice10`
`-trapdevice11`, `+trapdevice11`

Enable/disable kernal traps for virtual devices (`TrapDevice1=1`, `TrapDevice1=0`, `TrapDevice2=1`, `TrapDevice2=0`, `TrapDevice4=1`, `TrapDevice4=0`, `TrapDevice5=1`, `TrapDevice5=0`, `TrapDevice6=1`, `TrapDevice6=0`, `TrapDevice7=1`, `TrapDevice7=0`, `TrapDevice8=1`, `TrapDevice8=0`, `TrapDevice9=1`, `TrapDevice9=0`, `TrapDevice10=1`, `TrapDevice10=0`, `TrapDevice11=1`, `TrapDevice11=0`).

6.13 RS232 settings

The VICE emulators can emulate the RS232 device most of the machines have. The C64, C128 and VIC20 emulators emulate the userport RS232 interface from 300 to 57600 baud. The C64 can also use the UP9600 interface by Daniel Dallmann, using the shift registers of the two CIA 6526 chips. The PET can have a 6551 ACIA RS232 interface when running as a SuperPET, and the CBM-II has such an ACIA by default. The C64 and C128 emulators can emulate an ACIA 6551 (also known as Swiftlink, Datapump or Turbo232 for example) as extension at `$de**`, `$df**` or `$d7**` (C128 only).

Emulation can be achieved by either:

- connecting a real serial device/port;
- dumping to a file;
- piping through a process.
- connect through a network socket.

It is possible to define up to four serial devices, and then decide which interface should be connected to which device. This is done by so-called *RS232 device file names*; an RS232 device file name can be either a simple path, a network address, or a command name preceeded by a pipe symbol '`|`'. If the path specifies a special device (e.g. `/dev/ttyS0` or `com1`) it is recognized by VICE and the emulator can set the baudrate. (Note that in Windows you may have to use the full device path for higher COM ports, eg `\\.\com10`)

For example, RS232 device '`filename`' will cause the output to be written (not appended) to the file `filename`, while printer device '`|lpr`' will cause the `lpr` command to be executed and be fed the RS232 output. The RS232 output will not be converted but saved as sent by the emulated machine. The same holds true for the RS232 input. If the command writes data to the standard output it will be caught by VICE and sent back to the emulator. Also the data sent by the pseudo device will be sent back to VICE.

For example you can setup a null-modem cable between two serial ports of your PC, setup one port for login and use the other in VICE. Then you can login from your emulator via the RS232 emulation and the null-modem cable to your machine again.

Up to four RS232 devices may be specified through the following resources:

- device 1, whose default value is `/dev/ttyS0`;
- device 2, whose default value is `/dev/ttyS1`;
- device 3, whose default value is `127.0.0.1:25232`;
- device 4, whose default value is `|nc 127.0.0.1:25232`.

(Only) when using physical RS232 ports, you'll also have to specify it on the commandline or in the menu.

6.13.1 RS232 resources

`RsDevice1`
`RsDevice2`
`RsDevice3`
`RsDevice4`

Strings specifying the RS232 devices (see Section 6.13 [RS232 settings], page 101).

The windows driver understands various options passed in the so called "mode string" after a colon after the ports name: `COMx[:] [baud=b] [parity=p] [data=d] [stop=s] [to=on|off] [xon=on|off] [odsr=on|off] [octs=on|off] [dtr=on|off|hs] [rts=on|off|hs|tg] [idsr=on|off]`

`RsDevice1ip232`
`RsDevice2ip232`
`RsDevice3ip232`
`RsDevice4ip232`

Boolean specifying whether the respective RS232 devices (see Section 6.13 [RS232 settings], page 101) use the IP232 protocol supported by tcpser. This only works with Socket connections.

`Acia1Enable`

Boolean specifying whether the ACIA (Swiftlink, Turbo232) cartridge should be emulated or not (x64, x64sc, xscpu64, x128 and xvic only, and only if RS232 support is enabled and supported at compile time).

`Acia1Dev` Integer specifying what RS232 device (see Section 6.13 [RS232 settings], page 101) the ACIA is using (all emulators except x64dtv and vsid, and only if RS232 support is enabled and supported at compile time).

`Acia1Base`

Integer specifying the base address for the emulated ACIA chip (x64, x64sc, xscpu64, xvic and x128 only, and only if RS232 support is enabled and supported at compile time). (xvic: $9800/$9C00, x128: $D700/$DE00/$DF00, x64, x64sc, xscpu64: $DE00/$DF00)

`Acia1Mode`
: Integer specifying the type of emulated RS232 interface (x64, x64sc, xscpu64, xvic and x128 only, and only if RS232 support is enabled and supported at compile time). (0: normal, 1: Swiftlink, 2: Turbo232)

`Acia1Irq` Integer specifying which interrupt to use (x64, x64sc, xscpu64, xvic and x128 only, and only if RS232 support is enabled and supported at compile time). (0 = none, 1 = NMI, 2 = IRQ)

`RsUserUP9600`
: Boolean specifying if the user-port RS232 interface is of the UP9600 type. (C64 only).

`RsUserRTSInv`
: Boolean specifying if the user-port RS232 Request to Send (RTS) flow control line is inverted. (C64, C128 and VIC20).

`RsUserCTSInv`
: Boolean specifying if the user-port RS232 Clear to Send (CTS) flow control line is inverted. (C64, C128 and VIC20).

`RsUserDSRInv`
: Boolean specifying if the user-port RS232 Data Set Ready (DSR) control line is inverted. (C64, C128 and VIC20).

`RsUserDCDInv`
: Boolean specifying if the user-port RS232 Data Carrier Detect (DCD) control line is inverted. (C64, C128 and VIC20).

`RsUserDTRInv`
: Boolean specifying if the user-port RS232 Data Terminal Ready (DTR) control line is inverted. (C64, C128 and VIC20).

`RsUserRIInv`
: Boolean specifying if the user-port RS232 Ring Indicator (RI) control line is inverted. (C64, C128 and VIC20).

`RsUserBaud`
: Integer specifying the baudrate of the user-port RS232 interface (C64, C128 and VIC20). This must match the baudrate the emulated user port is being used with, and needs to be set up by the user because automatically detecting it can not be done easily (like when using ACIA)

`RsUserDev`
: Integer (ranging from 0 to 3, for device 1-4) specifying what RS232 device the user-port interface is using (C64, C128 and VIC20).

The following resources are only available if RS232 device support or RS232 network support is available at compile time.

`RsDevice1Baud`
`RsDevice2Baud`
`RsDevice3Baud`
`RsDevice4Baud`

Integers specifying the RS232 baudrate devices if the device file points to a special device (like `/dev/ttyS0` or `com1`, ie the serial device is a physical RS232 port of the host computer; see Section 6.13 [RS232 settings], page 101) (all emulators except vsid).

6.13.2 RS232 command-line options

`-rsdev1 <Name>`
`-rsdev2 <Name>`
`-rsdev3 <Name>`
`-rsdev4 <Name>`

Specify `<Name>` as RS232 devices 1, 2, 3 and 4, respectively (`RsDevice1`, `RsDevice2` `RsDevice3` and `RsDevice4`).

The windows driver understands various options passed in the so called "mode string" after a colon after the ports name: `COMx[:] [baud=b] [parity=p] [data=d] [stop=s] [to=on|off] [xon=on|off] [odsr=on|off] [octs=on|off] [dtr=on|off|hs] [rts=on|off|hs|tg] [idsr=on|off]`

`-rsdev1ip232`
`+rsdev1ip232`
`-rsdev2ip232`
`+rsdev2ip232`
`-rsdev3ip232`
`+rsdev3ip232`
`-rsdev4ip232`
`+rsdev4ip232`

Enable/Disable whether the respective RS232 devices (see Section 6.13 [RS232 settings], page 101) use the IP232 protocol supported by tcpser. This only works with Socket connections.

`-acia1`
`+acia1` Enable/Disable the $DE** ACIA RS232 interface emulation (`Acia1Enable=1`, `Acia1Enable=0`) (x64, x64sc, xscpu64, x128 and xvic only, and only if RS232 support is enabled and supported at compile time).

`-myaciadev <0-3>`

Specify RS232 device the ACIA should work on (all emulators except x64dtv and vsid, and only if RS232 support is enabled and supported at compile time)

`-acia1base <Base address>`

Set the base address of the ACIA cartridge (`Acia1Base`) (x64, x64sc, xscpu, x128 and xvic only, and only if RS232 support is enabled and supported at compile time). (xvic: $9800/$9C00, x128: $D700/$DE00/$DF00, x64, x64sc, xscpu: $DE00/$DF00)

`-acia1mode <mode>`
: Set the ACIA mode (`Acia1Mode`) (x64, x64sc, xscpu64, xvic and x128 only, and only if RS232 support is enabled and supported at compile time). (0: Normal, 1: Swiftlink, 2: Turbo232)

`-acia1irq <interrupt>`
: Set the ACIA interrupt (`Acia1Irq`) (x64, x64sc, xscpu64, xvic and x128 only, and only if RS232 support is enabled and supported at compile time). (0: None, 1: NMI, 2: IRQ)

`-rsuserup9600`
`+rsuserup9600`
: Enable or disable the userport RS232 UP9600 emulation (`RsUserUP9600`; C64 only)

`-rsuserrtsinv`
`+rsuserrtsinv`
: Enable or disable inversion of the userport RS232 emulation Request To Send (RTS) flow control line.

`-rsuserctsinv`
`+rsuserctsinv`
: Enable or disable inversion of the userport RS232 emulation Clear To Send (CTS) flow control line.

`-rsuserdsrinv`
`+rsuserdsrinv`
: Enable or disable inversion of the userport RS232 emulation Data Set Ready (DSR) control line.

`-rsuserdcdinv`
`+rsuserdcdinv`
: Enable or disable inversion of the userport RS232 emulation Data Carrier Detect (DCD) control line.

`-rsuserdtrinv`
`+rsuserdtrinv`
: Enable or disable inversion of the userport RS232 emulation Data Terminal Ready (DTR) control line.

`-rsuserriinv`
`+rsuserriinv`
: Enable or disable inversion of the userport RS232 emulation Ring Indicator (RI) control line.

`-rsuserbaud <baud>`
: Set the baud rate of the RS232 userport emulation. This must match the baudrate the emulated userport is being used at.

`-rsuserdev <0-3>`
: Specify device for the userport RS232 emulation (`RsUserDev`; C64, C128 and VIC20).

The following command-line options are only available if RS232 device support or RS232 network support is available at compile time.

`-rsdev1baud <baudrate>`
`-rsdev2baud <baudrate>`
`-rsdev3baud <baudrate>`
`-rsdev4baud <baudrate>`

If the serial device is a physical RS232 port, specify `<baudrate>` as baudrate, see Section 6.13 [RS232 settings], page 101; `RsDevice1Baud`, `RsDevice2Baud` `RsDevice3Baud` and `RsDevice4Baud`) (all emulators except vsid).

6.13.3 RS232 usage example

Here we give you a simple example how to set up an emulated C64 using the modem connected to your PC. The following list shows each step.

`Attach your modem to your PC at a serial port.`

Normally you should set it up to use the modem as "/dev/modem".

`start VICE`
`Setup VICE to use your modem as "serial device 1"`

Go to the RS232 settings menu and change "Serial 1 device" to "/dev/modem" (or the device where you attached your modem to) Then go to the RS232 settings menu and change "Serial 1 baudrate" to the baudrate your modem should run at. Watch out, e.g. on Linux there is an additional multiplier to multiply with the baudrate (so e.g. 19200 gives 115200 or so baud) See the "setserial" manpage on Linux for example. However, most modems should be able to autodetect the speed to the computer as well.

`Select the RS232 emulation your programs use`

If you want to use the Userport emulation, go to the RS232 settings and change "Userport RS232 Device" to "Serial 1". If you want ACIA emulation (swiftlink or what's it called?) then change "ACIA $DE** device" to "Serial 1".

`Enable the emulation`

Go to the RS232 settings and select either "ACIA $DE** emulation" or Userport 300/1200 baud or CIA 9600 baud emulation.

`Load your program and start it.`

If it is able to detect an RS232 cartridge like swiftlink or so, try to detect the ACIA emulation if enabled. Otherwise just set the baudrate to either 300, 1200, 2400 or 9600 according to what you enabled in the VICE menu for the userport.

6.14 Ethernet emulation

To use the ethernet emulation features, VICE must be configured with –enable-ethernet before compiling, and the system headers for libpcap, libnet and/or tuntap must be present.

In Windows you will have to install the npcap driver, which is available from `https://npcap.com/#download` (select winpcap compatibility mode when installing).

It should be clear to you that it may pose a security problem using these libraries in a network. Using libpcap can result in disclosure of any information that is available on your

network which is not secured by other means! Furthermore, libnet might allow even further attacks on your infrastructure!

Installing libpcap and libnet, you acknowledge that you're well aware of these security problems and that the VICE team cannot be made responsible for any problems and/or damage which may arise from the use of these libraries! If you don't acknowledge this, don't install the tools and/or use VICE networking.

6.14.1 Ethernet usage example

- Go to Preferences -> Peripherial Devices -> Ethernet and select the driver and network interface you want to use. If this option is not present at all, then VICE was compiled without ethernet support or the respective libraries could not be found on your system (see note above). If the option exists but is not enabled, you may not have the required permissions to use the raw ethernet device - you can try to run VICE as root/Administrator (the usual precautions apply), on *nix platforms VICE also supports the "capabilities" mechanism.
- Go to Preferences -> Cartridges - Ethernet Cartridge and select "Enable ethernet cartridge". Most likely you want to enable "RR-Net compatibility mode", too. (If this option is not set, the ethernet emulation runs in "The Final Ethernet" mode). The cartridge I/O base should be at its default value of $de00.
- Run the RR-Net enabled C64 program.
- If the program can not access the network, check your router setup. The raw ethernet emulation works exactly like a "real" ethernet device in the network, eg the emulated ethernet device gets its own MAC adress, which you may have to allow to access the internet at your router.

6.14.2 Ethernet resources

The following are the generic ethernet resources, some more may exist for the individual emulators:

`ETHERNET_INTERFACE`
: String specifying the device name of the ethernet device to use for the emulation.

`ETHERNET_DRIVER`
: String specifying the low-level ethernet driver for Ethernet Cartridge emulation (tuntap, pcap).

`ETHERNET_DISABLED`
: Boolean that specified whether ethernet emulation has been disabled because it is not available in the current configuration.

6.14.3 Ethernet command line options

`-ethernetioif <name>`
: Set the system ethernet interface for Ethernet Cartridge emulation (`ETHERNET_INTERFACE`).

`-ethernetiodriver <name>`
: Set the low-level ethernet driver for Ethernet Cartridge emulation (`ETHERNET_DRIVER`). (tuntap, pcap)

6.15 Tape port devices

6.15.1 Tape port resources

`TapePort1Device`
: Set the device attached to the (first) tape port (see table below):

`TapePort2Device`
: Set the device attached to the second tape port (xpet only, see table below):

0	None	x64, x64sc, x128, xvic, xplus4, xpet, xcbm2
1	Datasette	x64, x64sc, x128, xvic, xplus4, xpet, xcbm2
2	Tape RTC (PCF8583)	x64, x64sc, x128, xvic, xplus4, xpet, xcbm2
3	DTL BASIC dongle	x64, x64sc, x128
4	Sense dongle	x64, x64sc, x128, xvic, xplus4, xpet, xcbm2
6	tapecart	x64, x64sc, x128
7	WordCraft dongle	xpet
8	PetSpeed dongle	xpet (port 1 only)

`CPClockF83Save`
: Boolean specifying whether the CP Clock F83 (PCF8583 RTC) data is saved when changed.

`TapecartUpdateTCRT`
: Boolean, if true write back the tapecart memory contents back to the .tcrt file when detaching the image or quitting the emulator.

`TapecartOptimizeTCRT`
: Boolean, when set to true the .tcrt image will be optimized by leaving out blank space at the end when saving.

`TapecartLogLevel`
: Integer that specifies the tapecart emulation log level. At the default level of 0, only errors are logged. Level 1 additionally logs mode changes and command bytes and level 2 adds details of command parameters.

`TapecartTCRTFilename`
: String specifying the file name of the current tapecart image.

6.15.2 Tape port command line options

`-tapeport1device <device>`
: Set the device attached to the (first) tape port: (see table for `TapePort1Device` above).

`-tapeport2device <device>`
: Set the device attached to the second tape port: (see table for `TapePort2Device` above).

`-cpclockf83save`
`+cpclockf83save`
: Enable/Disable saving of the CP Clock F83 (PCF8583 RTC) data when changed (`CPClockF83Save=1`, `CPClockF83Save=0`).

`-tcrt <name>`
: Specify tapecart .tcrt image filename (`TapecartTCRTFilename`)

`-tapecartupdatetcrt`
`+tapecartupdatetcrt`
: Specify if the attached .tcrt image should be updated when the emulated tapecart is written to. (`TapecartUpdateTCRT=1`, `TapecartUpdateTCRT=0`)

`-tapecartoptimizetcrt`
`+tapecartoptimizetcrt`
: Specify if writing to a .tcrt file should optimize its size by leaving out blank space at the end. (`TapecartOptimizeTCRT=1`, `TapecartOptimizeTCRT=0`)

`-tapecartloglevel <number>`
: Specify the tapecart log level. The default level of 0 only logs errors. Level 1 additionally logs mode changes and command bytes and level 2 adds details of command parameters.

6.16 Userport devices

6.16.1 Userport resources

`UserportDevice`
: Set the device attached to the userport (see table below):

0	None	x64, x64sc, x128, xscpu64, x64dtv, xvic, xplus4, xpet, xcbm2
1	Userport printer	x64, x64sc, x128, xscpu64, xvic, xpet, xcbm2
2	Userport RS232/Modem	x64, x64sc, x128, xscpu64, xvic
3	CGA userport joy adapter	x64, x64sc, x128, xscpu64, xvic, xpet, xcbm2
4	PET userport joy adapter	x64, x64sc, x128, xscpu64, xvic, xpet, xcbm2
5	Hummer userport joy adapter	x64dtv
6	OEM userport joy adapter	x64, x64sc, x128, xscpu64, xvic, xpet, xcbm2
7	HIT userport joy adapter	x64, x64sc, x128, xscpu64
8	KingSoft userport joy adapter	x64, x64sc, x128, xscpu64
9	StarByte userport joy adapter	x64, x64sc, x128, xscpu64
10	Synergy userport joy adapter	xplus4
11	Wheel of joystick 8-joy userport joy adapter	x64, x64sc, x128, xscpu64, xvic, xplus4, xpet, xcbm2
12	Userport DAC	x64, x64sc, x128, xscpu64, xvic, xpet, xcbm2
13	Userport DigiMAX	x64, x64sc, x128, xscpu64, xcbm2
14	Userport 4bit sampler	x64, x64sc, x128, xscpu64, xcbm2
15	Userport 8bit stereo sampler	x64, x64sc, x128, xscpu64, xcbm2
16	Userport RTC (RTC58321A)	x64, x64sc, x128, xscpu64, xvic, xpet, xcbm2
17	Userport RTC (DS1307)	x64, x64sc, x128, xscpu64, xvic, xpet, xcbm2
18	Userport Petscii SNES pad	x64, x64sc, x128, xscpu64, xvic, xpet, xcbm2
19	Userport SuperPad64	x64, x64sc, x128, xscpu64, xcbm2
20	Userport Diag 586220 Harness	x64, x64sc, x128, xscpu64
21	Userport parallel drive cable	x64, x64sc, x128, xscpu64, xplus4
22	Userport I/O Simulation	x64, x64sc, x128, xscpu64, x64dtv, xvic, xplus4, xpet, xcbm2

23 Userport WiC64 Simulation	x64, x64sc, x128, xscpu64, xvic
24 Joystick Adapter (Spaceballs)	x64, x64sc, x128, xscpu64, xvic
25 Userport Stupid Pet Tricks joystick adapter	x64, x64sc, x128, xscpu64, xcbm2, xpet, xvic
26 PET Userport diagnostic pin	xpet
27 Userport PS/2 Mouse	x64dtv
28 Userport FunMP3	x64, x64sc, x128, xscpu64, xpet, xvic
29 V8 userport joy adapter	x64, x64sc, x128, xscpu64, x64dtv, xpet, xvic

`UserportRTC58321aSave`
: Boolean specifying whether the userport RTC data is saved when changed or not (xpet, cbm2, x64, x64sc, xscpu64 and x128 only).

`UserportRTCDS1307Save`
: Boolean specifying whether the userport RTC data is saved when changed or not (xpet, cbm2, x64, x64sc, xscpu64 and x128 only).

`WIC64DefaultServer`
: String specifying the default server URL used by the wic64 emulation (vic20, x64, x64sc, xscpu64 and x128 only).

`WIC64Timezone`
: Integer specifying the default timezone used by the wic64 emulation (vic20, x64, x64sc, xscpu64 and x128 only).

`WIC64SecToken`
: String specifying the security token used by the wic64 emulation for identifying the user at the WiC64 portal. (vic20, x64, x64sc, xscpu64 and x128 only).

`WIC64MACAddress`
: String specifying the pseudo MAC address used by the wic64 emulation for identifying the user at the WiC64 portal (vic20, x64, x64sc, xscpu64 and x128 only).

`WIC64IPAddress`
: String specifying the IP address used by the wic64 emulation (if DHCP is disabled) (vic20, x64, x64sc, xscpu64 and x128 only).

`WIC64DHCP`
: Boolean specifying whether the WiC64 local IP address should be choosen automatically (vic20, x64, x64sc, xscpu64 and x128 only).

`WIC64Logenabled`
: Integer, 1: user selected WiC64 tracing, 0: no tracing (vic20, x64, x64sc, xscpu64 and x128 only).

`WIC64Resetuser`
: Integer, 1: user credentials shall be reseted if WiC64 is reseted, 0: user credentials are preseved (vic20, x64, x64sc, xscpu64 and x128 only).

`WIC64HexdumpLines`
: Integer number of lines to hexdump, 0: unlimited (vic20, x64, x64sc, xscpu64 and x128 only).

`WIC64RemoteTimeout`
: Integer, 1-255: Timeout to wait for URLs in seconds (vic20, x64, x64sc, xscpu64 and x128 only).

`WIC64LogLevel`
: Integer, WiC64 trace level, 0: no trace, 1: standard, 2: verbose, 3: debug (vic20, x64, x64sc, xscpu64 and x128 only).

`FunMP3Dir`
: String specifying the path to the directory where mp3's are lookup up for playing (vic20, x64, x64sc, xscpu64, x128 and xpet only).

6.16.2 Userport command line options

`-userportdevice <device>`
: Set the device attached to the userport (see table for `UserportDevice` above).

`-userportrtc58321asave`
`+userportrtc58321asave`
: Disable/enable saving of the userport RTC data when changed (`UserportRTC58321aSave=1`, `UserportRTC58321aSave=0`) (xpet, cbm2, x64, x64sc, xscpu64 and x128 only).

`-userportrtcds1307save`
`+userportrtcds1307save`
: Disable/enable saving of the userport RTC data when changed (`UserportRTCDS1307Save=1`, `UserportRTCDS1307Save=0`) (xpet, cbm2, x64, x64sc, xscpu64 and x128 only).

`-wic64server <URL>`
: String specifying the default server URL used by the wic64 emulation, (vic20, x64, x64sc, xscpu64 and x128 only).

`-wic64timezone <0..31>`
: Integer to specify default timezone index, e.g. 2: European Central Time", (vic20, x64, x64sc, xscpu64 and x128 only).

`-wic64reset`
: Resets wic64 related resources to factory defaults (including user credentials) (vic20, x64, x64sc, xscpu64 and x128 only).

`-wic64trace`
`+wic64trace`
: Enable/disable WIC64 tracing on the console (vic20, x64, x64sc, xscpu64 and x128 only).

`-wic64tracelevel <0..3>`
: Integer to specify WIC64 trace level (0: off, 1: commands, >2: debug), implicitly turns on/off WiC64 tracing. (vic20, x64, x64sc, xscpu64 and x128 only).

`-wic64hexdumplines <value>`
: Integer to specify number of lines to hexdump, 0: unlimited, (vic20, x64, x64sc, xscpu64 and x128 only).

`-wic64remotetimeout <value>`
: Timeout to wait for URLs in seconds, 1 - 255 (vic20, x64, x64sc, xscpu64 and x128 only).

`-wic64ipaddress <IP>`
: Specify WiC64 IP (when DHCP is disabled) (vic20, x64, x64sc, xscpu64 and x128 only).

`-wic64macaddress <MAC>`
: Specify WiC64 MAC (vic20, x64, x64sc, xscpu64 and x128 only).

`-wic64token <token>`
: Specify WiC64 security token (vic20, x64, x64sc, xscpu64 and x128 only).

`-wic64dhcp`
`+wic64dhcp`
: Enable/disable automatically setting the local IP address (default: enabled) (vic20, x64, x64sc, xscpu64 and x128 only).

`-funmp3dir <path>`
: Specify path to search for mp3 files used by the FunMP3 module (vic20, x64, x64sc, xscpu64, x128, xpet only).

6.17 Monitor settings

This section lists command-line options specific to the built-in monitor.

6.17.1 Monitor resources

`KeepMonitorOpen`
: Boolean, if true the monitor window may stay open when the emulation is running, eg to look at trace-point output. (Not all ports/UIs support this, in that case this setting has no effect.)

`RefreshOnBreak`
: Boolean, if true, the emulated display(s) will update as the monitor is entered and also after each command executed via the monitor. Not entirely accurate yet, as updates become visible one scanline at a time. In addtion, the image can be not quite right around the raster line being updated.

`MonitorServer`
: Boolean specifying whether the remote monitor server is enabled.

`MonitorServerAddress`
: String specifying the address the remote monitor server listens to (ip4://127.0.0.1:6510)

`BinaryMonitorServer`
: Boolean specifying whether the binary monitor server is enabled.

`BinaryMonitorServerAddress`
: String specifying the address the binary monitor server listens to (ip4://127.0.0.1:6502)

`NativeMonitor`
: Boolean specifying whether the native monitor is enabled. When enabled, the monitor will not run in the interface of the emulator, but instead work in the spawning terminal.

`MonitorLogEnabled`
: Boolean specifying whether the output of the monitor will be logged to a file.

`MonitorLogFileName`
: String specifying the logfile name for the monitor.

`MonitorChisLines`
: Integer specifying the number of lines to keep in the cpu history. (only when enabled in configure)

`MonitorScrollbackLines`
: Integer specifying the number of lines to keep in the monitor scrollback buffer (-1 for no limit).

`MonitorFont`
: String specifying the font to use in the Gtk3 UI's VTE monitor window. Should be in the form accepted by Pango, for example "Liberation Mono 11".

`MonitorFG`
: String specifying the foreground color to use in the Gtk3 UI's VTE monitor window. Should be in form accepted by Pango: either a CSS color name (for example 'limegreen'), three/six digit hex string ("#rgb"/"#rrggbb") or three decimal values in the range 0-255 ("rbg(red,green,blue"), for example "rgb(27,180,33)").

`MonitorBG`
: String specifying the background color to use in the Gtk3 UI's VTE monitor window. Should be in form accepted by Pango: either a CSS color name (for example 'limegreen'), three/six digit hex string ("#rgb"/"#rrggbb") or three decimal values in the range 0-255 ("rbg(red,green,blue"), for example "rgb(27,180,33)").

`MonitorXPos`
: Integer specifying the Gtk3 VTE-monitor window's X position.

`MonitorYPos`
: Integer specifying the Gtk3 VTE-monitor window's Y position.

`MonitorWidth`
: Integer specifying the Gtk3 VTE-monitor window's width.

`MonitorHeight`
: Integer specifying the Gtk3 VTE-monitor window's height.

6.17.2 Monitor command-line options

`-moncommands <Name>`
: Execute the commands from the file <Name> in the monitor after starting up. All monitor commands are supported, including playback of other command

files via pb. By default, the monitor opens and starts executing the commands just before the kernal reset routine starts. The kernal startup sequence may interfere with what you are trying to achieve, for example by clearing any basic listing you may have loaded. In this case, use the -initbreak option to control when the monitor will open and execute your script. You can choose an address, or use -initbreak ready to attempt to detect when the kernel is ready and execute the script then.

-initbreak <address>
-initbreak reset
-initbreak ready
Set an initial breakpoint for the monitor. Addresses with prefix "0x" are hexadecimal. -initbreak reset will break just before the reset routine starts. -initbreak ready will attempt to break when the kernal is 'ready'.

-keepmonopen
+keepmonopen
Enable/disable keeping the monitor window open (KeepMonitorOpen=1, KeepMonitorOpen=0).

-refreshonbreak
+refreshonbreak
Enable/Disable refresh display when entering monitor and after each monitor command. (RefreshOnBreak=1, RefreshOnBreak=0).

-remotemonitor
+remotemonitor
Enable/Disable remote monitor

-remotemonitoraddress <name>
The local address the remote monitor should bind to

-binarymonitor
+binarymonitor
Enable/Disable binary monitor

-binarymonitoraddress <name>
The local address the binary monitor should bind to

-nativemonitor
+nativemonitor
Enable/Disable native monitor. (NativeMonitor=1, NativeMonitor=0).

-monlog
+monlog Enable/Disable logging monitor output to a file. (MonitorLogEnabled=1, MonitorLogEnabled=0).

-monlogname <name>
Specify logfile name for the monitor. (MonitorLogFileName).

-monchislines <value>
Set number of lines to keep in the cpu history. (only when enabled in configure) (MonitorChisLines).

`-monscrollbacklines <value>`
Set number of lines to keep in the monitor scrollback buffer (-1 for no limit). (`MonitorScrollbackLines`).

`-monitorfont <font-description>`
Set the monitor font for the Gtk3 UI's VTE-monitor. (`MonitorFont`).

`-monitorfg <colour-description>`
Set the monitor foreground colour for the Gtk3 UI's VTE-monitor. (`MonitorFG`).

`-monitorbg <colour-description>`
Set the monitor background colour for the Gtk3 UI's VTE-monitor. (`MonitorFG`).

`-monitorxpos <X-position>`
Set window X position for the Gtk3 UI's VTE-monitor. (`MonitorXPos`).

`-monitorxpos <Y-position>`
Set window Y position for the Gtk3 UI's VTE-monitor. (`MonitorYPos`).

`-monitorwidth <width>`
Set window width for the Gtk3 UI's VTE-monitor. (`MonitorWidth`).

`-monitorheight <height>`
Set window height for the Gtk3 UI's VTE-monitor. (`MonitorHeight`).

6.18 RAM init pattern settings

The initial content of uninitialized RAM depends on many factors, the inner workings of the RAM chip, the motherboard, etc. Generally, many RAM chips are build in a way that - however - at powerup about half the bits turn out 1, and about the other half turns out 0. As a consequence, and depending on what kind of RAM chips have been used in what kind of way, the typical patterns can be "seen" by the CPU right after powerup.

VICE tries to model (some of) the real patterns accurately. Please be aware of the fact that there is - technically buggy - software out there that will only work with a certain RAM init pattern.

How generating the pattern works is easily described in three simple steps:

1. A base pattern is created using blocks of `RAMInitStartValue`, which is inverted every `RAMInitValueInvert` bytes. This pattern can be offset ("rotated") by `RAMInitValueOffset` bytes. This step creates the typical 00 00 ff ff ff ff 00 00 etc patterns.

2. Every `RAMInitPatternInvert` bytes the base pattern is inverted ("xored") by `RAMInitPatternInvertValue`. This step creates the inverted pattern every $4000 or so bytes that is typical for some boards/ICs. It may also create the 0x99, 0x66 based patterns that show up on other setups.

3. Every `RAMInitRepeatRandom` bytes `RAMInitStartRandom` bytes are randomly inverted ("xored"). Additionally every bit is randomly inverted with a `RAMInitRandomChance` : 10000 chance. This step creates the typical random bytes that have been observed with some boards/ICs.

6.18.1 RAM init pattern resources

`RAMInitStartValue`
: Integer specifying the first value of the base pattern (all emulators except vsid). (0..255)

`RAMInitValueInvert`
: Integer specifying the number of bytes until the base pattern is inverted (all emulators except vsid).

`RAMInitValueOffset`
: Integer specifying the number of bytes to offset the base pattern (all emulators except vsid).

`RAMInitPatternInvert`
: Integer specifying the length of the memory block when inverting the base pattern (all emulators except vsid).

`RAMInitPatternInvertValue`
: Integer specifying the value to use for inverting the base pattern with (all emulators except vsid).

`RAMInitStartRandom`
: Integer specifying the number of random bytes in the random pattern (all emulators except vsid).

`RAMInitRepeatRandom`
: Integer specifying the number of bytes after which to repeat the random pattern (all emulators except vsid).

`RAMInitRandomChance`
: Integer specifying the chance of any bit to randomly toggle (0-4096) (all emulators except vsid).

6.18.2 RAM init pattern command-line options

`-raminitstartvalue <value>`
: Set the first value of the base pattern (`RAMInitStartValue`) (all emulators except vsid). (0..255)

`-raminitvalueinvert <num of bytes>`
: Set the number of bytes until the base pattern is inverted (`RAMInitValueInvert`) (all emulators except vsid).

`-raminitvalueoffset <num of bytes>`
: Offset the first pattern by <num> of bytes (`RAMInitValueOffset`) (all emulators except vsid).

`-raminitpatterninvert <num of bytes>`
: Set the length of the memory block when inverting the base pattern (`RAMInitPatternInvert`) (all emulators except vsid).

`-raminitpatterninvertvalue <value>`
: Set the value to invert the base pattern with (`RAMInitPatternInvertValue`) (all emulators except vsid).

`-raminitstartrandom <num of bytes>`
: Set the number of random bytes in the random pattern (`RAMInitStartRandom`) (all emulators except vsid).

`-raminitrepeatrandom <num of bytes>`
: Set the number of bytes after which to repeat the random pattern (`RAMInitRepeatRandom`) (all emulators except vsid).

`-raminitrandomchance <value>`
: Set the chance of any bit to randomly toggle (0-4096) (`RAMInitRandomChance`) (all emulators except vsid).

6.19 Debug settings

6.19.1 Debug resources

`DebugCartEnable`
: Boolean specifying whether the debug "cartridge" used for the test suite is enabled.

`LogLevelANE`
: Integer specifying the log mode (0=none 1=only unstable 2=all) (x64sc, xvic).

`LogLevelLXA`
: Integer specifying the log mode (0=none 1=only unstable 2=all) (x64sc, xvic).

The following are only available when the emulators were compiled in DEBUG mode:

`TraceMode`
: Integer specifying the trace mode (0=normal 1=small 2=history)

`AutoPlaybackFrames`
: Amount of automatic playback frames

`MainCPU_TRACE`
: Trace the main CPU / Do not trace the main CPU

`Drive0CPU_TRACE`
: Trace the drive 0 CPU / Do not trace the drive 0 CPU

`Drive1CPU_TRACE`
: Trace the drive 1 CPU / Do not trace the drive 1 CPU

`Drive2CPU_TRACE`
: Trace the drive 2 CPU / Do not trace the drive 2 CPU

`Drive3CPU_TRACE`
: Trace the drive 3 CPU / Do not trace the drive 3 CPU

`IEC_TRACE`
: Trace IEC bus activity / Do not trace IEC bus activity

`IEEE_TRACE`
: Trace IEEE bus activity / Do not trace IEEE bus activity

6.19.2 Debug command-line options

`-core`
`+core` Enable/disable generation of core dumps (`DoCoreDump=1`, `DoCoreDump=0`) (all emulators except vsid).

`-debug`
`+debug` Disable/enable calling of the exception handler (`DoCoreDump=1`, `DoCoreDump=0`) (all emulators except vsid).

`-debugcart`
`+debugcart`
Enable/disable the debug "cartridge" used for the test suite.

`-aneloglevel <mode>`
Set the log mode (0=none 1=only unstable 2=all) (`LogLevelANE`) (x64sc, xvic).

`-lxaloglevel <mode>`
Set the log mode (0=none 1=only unstable 2=all) (`LogLevelLXA`) (x64sc, xvic).

The following are only available when the emulators were compiled in DEBUG mode:

`-trace_maincpu`
`+trace_maincpu`
Trace the main CPU / Do not trace the main CPU (`MainCPU_TRACE=1`, `MainCPU_TRACE=0`)

`-trace_drive0`
`+trace_drive0`
Trace the drive 0 CPU / Do not trace the drive 0 CPU (`Drive0CPU_TRACE=1`, `Drive0CPU_TRACE=0`)

`-trace_drive1`
`+trace_drive1`
Trace the drive 1 CPU / Do not trace the drive 1 CPU (`Drive1CPU_TRACE=1`, `Drive1CPU_TRACE=0`)

`-trace_drive2`
`+trace_drive2`
Trace the drive 2 CPU / Do not trace the drive 2 CPU (`Drive2CPU_TRACE=1`, `Drive2CPU_TRACE=0`)

`-trace_drive3`
`+trace_drive3`
Trace the drive 3 CPU / Do not trace the drive 3 CPU (`Drive3CPU_TRACE=1`, `Drive3CPU_TRACE=0`)

`-trace_iec`
`+trace_iec`
Trace IEC bus activity / Do not trace IEC bus activity (`IEC_TRACE=1`, `IEC_TRACE=0`)

`-trace_ieee`
`+trace_ieee`
: Trace IEEE bus activity / Do not trace IEEE bus activity (`IEEE_TRACE=1`, `IEEE_TRACE=0`)

`-trace_mode <value>`
: Trace mode (0=normal 1=small 2=history)

`-autoplaybackframes <frames>`
: Set the amount of automatic playback frames

6.20 Network Play settings

Note network play code is still experimental and unstable in some corners and can be a bit tricky to setup and made work:

- First make sure both client and server can actually use the port you want to use. This might require poking holes into your firewall. On the server side, this often requires configuring port forwarding.
- Choose a port you want to use. Usually the default (6502) is fine.
- Setup the server address, usually you can leave it blank - if that does not work you might have to use the actual IP of your machine. Start the server and leave the settings before you continue.
- On the client setup the remote address of the server (use 127.0.0.1 to connect to another VICE instance running on the same machine) and connect to the server.

The experimental nature of this feature also means that you might require both involved parties to use the exact same version of VICE.

6.20.1 Network Play resources

`NetworkServerName`
: String specifying the name of the remote server. The client specifies here to what remote machine it wants to connect. To connect to another instance of VICE running on the same machine use 127.0.0.1.

`NetworkServerBindAddress`
: String specifying the IP of the remote server. The server specifies here at what IP it listens. This can be blank in common setups.

`NetworkServerPort`
: Integer specifying the port used for network play. This must be a number > 1023 (non privileged) and must be the same on both server and client.

`NetworkControl`
: Integer containing a bitfield that specifies which resources are controlled by either the server or the client:

Bit	Resource	controlled by
0	Keyboard	Server
1	Joystick port 1	Server
2	Joystick port 2	Server

3	Devices	Server
4	Resources	Server
5	Keyboard	Client
6	Joystick port 1	Client
7	Joystcik port 2	Client
8	Devices	Client
9	Resources	Client

6.20.2 Network Play command-line options

`-netplayserver <name>`
: Set the name of the remote server (`NetworkServerName`).

`-netplaybind <ip>`
: Set the IP of the remote server (`NetworkServerBindAddress`).

`-netplayport <port>`
: Set the port used for network play (`NetworkServerPort`).

`-netplayctrl <flag>`
: Specify what resources are controlled by the server or the client (see above) (`NetworkControl`).

7 Machine-specific features

7.1 C64/128-specific commands and settings

This section lists the settings and commands that are C64/128 specific and thus are not present in the other emulators.

7.1.1 Using cartridges

Generally a cartridge can be enabled by attaching its respective cartridge image, or using the respective menu option for cartridges that do not require an image.

Cartridge images are like disk images, but contain the contents of cartridge ROM and/or RAM images instead of disk images. Two kinds of cartridge images are supported:

- `.crt` images, as used by the CCS64 emulator by Per Håkan Sundell
- raw `.bin` images, with or without load address

Use cartconv to convert .bin images to .crt format.

To attach cartridges, use the "Attach a cartridge image" submenu. When using `.crt` images, this will work for every cartridge which is supported. For raw `.bin` images you will have to select the correct type of cartridge first, or use command line options.

When you have successfully attached a cartridge image, you should then reset the machine to make sure the cartridge initializes itself. (Or enable the "reset on cartridge change" option).

Of course, it is also possible to detach a currently attached cartridge image ("Detach cartridge image").

If you are using a freezer cart like an Action Replay cartridge, you can emulate the cartridge's freeze button with the "Cartridge freeze" command.

The cartridge system is organized in "Slots" to allow more than one cartridge connected at a time, like it can be done using an expansion port expander on a real C64 (see below).

The imaginary expansion port expander is organized in 4 slots, the cartridges are associated with them like this:

7.1.1.1 Slot 0

All carts that have a passthrough connector go here. Once a "Slot 0" cartridge is enabled all further cartridges are connected to its respective passthrough port.

Only one cartridge of this type can be active at a time.

"Slot 0" carts have individual "enable" switches, enabling means enabling permanently.

The following cartridges are emulated in this slot:

- CMD RAMlink
- IEEE-488 Interface (`http://www.funet.fi/pub/cbm/schematics/cartridges/c64/ieee-488/eprom.bin`)
- IEEE Flash 64! interface
- Magic Voice
- MMC64

7.1.1.2 Slot 1

Mostly RAM based cartridges which for one reason or the other might make sense to be enabled together with one of the "Main Slot" cartridges go here.

Only one cartridge of this type can be active at a time.

"Slot 1" carts have individual "enable" switches, enabling means enabling permanently

The following cartridges are emulated in this slot:

- Double Quick Brown Box (DQBB)
- Expert Cartridge
- ISEPIC
- RamCart

7.1.1.3 Main Slot

All other cartridges which are not pure i/o extensions go here.

Only one cartridge of this type can be active at a time.

Cartridges in the "Main Slot" must be explicitly set as default to enable them permanently.

The following cartridges are emulated in this slot:

- generic 4KiB, 8KiB and 16KiB game- and ultimax cartridges
- Action Replay V5
- Action Replay MK2
- Action Replay MK3
- Action Replay MK4
- Atomic Power
- BIS-Plus
- Blackbox V3
- Blackbox V4
- Blackbox V8
- Blackbox V9
- BMP Data Turbo 2000
- C64 Games System
- Capture
- Comal 80
- Dela EP64
- Dela EP7x8
- Dela EP256
- Diashow-Maker
- Dinamic
- Drean
- Easy Calc
- EasyFlash

- Epyx FastLoad
- EXOS
- The Final Cartridge
- The Final Cartridge III
- Final Cartridge Plus
- Formel 64
- Freeze Frame
- Freeze Frame MK2
- Freeze Machine
- Fun Play / Powerplay
- Game Killer
- GMod2
- GMod3
- Hyper-BASIC
- IDE64 (`http://www.ide64.org`)
- KCS Power Cartridge
- Kingsoft
- Lt. Kernal Host Adaptor
- MACH 5
- Magic Desk
- Magic Desk Plus
- Magic Desk 16
- Magic Formel
- MAX Basic
- Mikro Assembler
- MMC Replay
- Ocean
- Pagefox
- Prophet64
- Protovision "Megabyter"
- Profi-DOS
- REX 256KiB EPROM Cart
- REX RAM-Floppy
- REX Utility
- Retro Replay
- RGCD
- RR-Net MK3
- ROSS
- SD-BOX

- Silverrock 128
- Simons' BASIC
- Snapshot 64
- Stardos
- Structured BASIC
- Super Explode V5.0
- Super Games
- Super Snapshot V4
- Super Snapshot V5
- Turtle Graphics II
- Universal Cartridge
- Warp Speed
- Westermann Learning
- Zaxxon
- ZIPP-CODE 48

7.1.1.4 I/O Slot

All carts that are pure I/O extensions go here.

Any number of "I/O Slot" Carts may be active at a time.

"I/O Slot" carts have individual "enable" switches, enabling means enabling permanently.

The following cartridges are emulated in this slot:

- ACIA (Swiftlink, Turbo232)
- DigiMAX
- DS12C887 RTC
- Ethernet (The Final Ethernet, RR-Net)
- GEO-RAM
- MIDI (Passport/Syntech, Datel/Siel/JMS/C-Lab, Maplin, Namesoft, Sequential)
- RAM Expansion Module (REU)
- SFX Sound Expander
- SFX Sound Sampler

7.1.1.5 Expected behaviour

When the emulator is run without arguments, all settings from the config file should be applied and arguments override settings from the config file.

When saving the settings to the config file it is expected that on the next run of the emulator all settings will be in the same state as they were when saved.

There is an exception to this rule: the cartridge in the "Main Slot" must be explicitly set as default before it gets saved to the config file.

`+cart` should disable ALL cartridges, including eventually activated REU, Swiftlink and all similar expansionport devices.

`-cartXYZ` options should generally attach AND activate a cart of type XYZ. As a consequence, attaching carts this way which are NOT in the "Main Slot" will also enable the cart permanently.

7.1.1.6 Common problems

If attaching a cartridge does not work as expected, this may be because of various reasons:

- Not seldomly the CRT type is incorrectly set in `.crt` files found "in the wild". Make sure this is not the case (if in doubt use `cartconv` to verify and/or fix).
- You may have unintentionally enabled more than one cartridge at once, for example by saving the settings with REU enabled, and then later attaching a game cartridge from the command-line. The cartridge system will allow certain combinations, but (as on the real thing) not all do (can) actually work. To make sure this is not the case, either detach all cartridges from the menus, or use `+cart` on the command-line.
- The cartridge image might be broken. Try one from a different source. If you are sure the dump is ok (for example because you dumped it yourself) then make sure it is in proper linear order (on some cartridges, for example "capture", address and/or data lines at the eprom are shuffled around so a dump made with an eprom burner can not be used as is).
- Last not least you might have encountered a bug in the emulation. If you suspect this is the case, and you can still reproduce the bug after checking the things above, please file a bug report including the following information:
  - attach your vicerc and a reference to the cartridge binaries
  - if you can, comment in the respective DEBUGXYZ macros prominently defined at the top of these files: src/c64/cart/c64cart.c src/c64/cart/c64cartmem.c src/c64/c64io.c src/c64/c64export.c and then recompile. this will add debug output that might make it much easier to locate certain problems.

7.1.1.7 IEEE-488 interface

To be able to use an IEEE drive, you need to enable an IEEE interface for the emulator. You can use the Commodore IEEE-488 interface, or the Skyles Electric Works IEEE Flash! 64 interface.

To use the Commodore interface, follow these steps:

Download the IEEE 488 ROM image from the CBM archives (formerly known as FUNET) Attach that image with File/Attach cartridge image/IEEE488 interface image.

Make sure you have a one-drive system only (that is, go to Settings/Peripheral Setting, uncheck "use IEC device" for all devices, go to Settings/Drive Settings and select "Floppy type" as "none" for all drives other than drive 8.

After this, all drives can be selected in x64 and x128.

To use the IEEE Flash! 64 interface, follow these steps:

Download the IEEE Flash! 64 ROM image (uploaded to the CBM archives but not yet in place). Set the path to the ROM image using the UI under Cartridges/IEEE Flash! 64 settings, the commandline argument -ieeeflash64image, or the IEEEFlash64Image resource setting. Then you can Enable the interface and choose which devices to route to the IEEE bus using the other IEEE Flash! 64 settings. With this interface, you can mix and match IEC and IEEE drives.

7.1.1.8 The Final Cartridge 3

The Final Cartridge 3 detects whether a mouse is connected when it starts and disables mouse support if it doesnt detect one. So to make mouse emulation work you must either enable it on the command line, or reset the cartridge after enabling it from the user interface.

7.1.1.9 CMD RAMLink

The CMD RAMLink can operate standalone using its own RAMCard or with an REU, GEORAM, or RAMDrive (only one) which is inserted into the RAM port. The RAMLink is essentially a builtin high capacity RAMDOS with battery backup support. This "backup" support can be emulated through the use of images. Initially it will "format" the memory so each has their own native parition on drive 16. Paritions and the device number can be changed with the CMD "RAM-tools". The latest RAMLink firmware (2.01) can not handle REU sizes greater than 8 MiB, or GEORAM sizes beyond 2 MiB, as the firmware will hang on size detection.

The RAMLink also features a parallel port (similar to IEEE-488 interface) which allows faster transfers to the CMD HD series drives. The "cabling" is automatically performed by VICE and the RAMLink firmware will detect the cable presence and use it automatically. However, some programs may have compatibility issues (notably the CMD HD-tools with older HD boot roms). When connecting an empty image, or an image without a HD-DOS signature, the cable will be automatically disconnected. Although documented in the RAMLink manual, the JiffyDOS command "@P0" to disable, or "@P1" to enable parallel transfers will hang as they try to communicate the HD boot rom and fail. To resolve this, use the "Drive#ParallelCable" or command line switch "-parallel#" and set it to 0, to disable transfers; any other value will enable them.

The RAMLink features an "Enable/Disable" switch that can be changed at anytime using the GUI options. Prior to disabling, the JiffyDOS "@Q" command should be issued to remove the JiffyDOS system vectors. See the manual for how to reactivate JiffyDOS. It is not recommended to change the REU, GEORAM, or RAMCard size as the CMD RAMDOS was not designed to detect this. Any changes should be followed by a machine reset.

The RAMLink also has a "Normal/Direct" switch which can also be changed at anytime. Normal, the default, only allows RAMLink access to the RAM port (REU, GEORAM, and RAMDrive); their registers are hidden otherwise. Direct exposes their registers all the time. This can lead to potential data corruption of the data on the RAM expansion devices as RAMLink can not guarantee its access is exclusive.

RAMLink is also compatible with the CMD SuperCPU 64, however it requires the official SuperCPU ROM (2.04) along with the RAMLink ROM to properly detect and utilize all the RAMLink features.

7.1.2 C64 cartridge settings

7.1.2.1 C64 cartridge resources

`IOCollisionHandling`

Integer specifying the way the I/O collisions should be handled. (0: error message and detach all involved carts, 1: error message and detach last attached involved carts, 2: warning in log and 'AND' the valid return values)

`CartridgeReset`
: Boolean specifying whether the machine should be reset when a cartridge is changed.

`CartridgeType`
: Integer specifying the type of the default cartridge.

  The following cartridge types are valid:

  - - 6: Ultimax
  - - 3: Generic 8KiB
  - - 2: Generic 16KiB
  - - 1: None
  - 0: CRT
  - 1: Action Replay V5
  - 2: KCS Power Cartridge
  - 3: The Final Cartridge III
  - 4: Simons' BASIC
  - 5: Ocean
  - 6: Expert Cartridge
  - 7: Fun Play
  - 8: Super Games
  - 9: Atomic Power / Nordic Power
  - 10: Epyx FastLoad
  - 11: Westermann Learning
  - 12: REX Utility
  - 13: The Final Cartridge
  - 14: Magic Formel
  - 15: C64 Games System
  - 16: Warp Speed
  - 17: Dinamic
  - 18: Zaxxon
  - 19: Magic Desk
  - 20: Super Snapshot V5
  - 21: Comal 80
  - 22: Structured BASIC
  - 23: ROSS
  - 24: Dela EP64
  - 25: Dela EP7x8
  - 26: Dela EP256
  - 27: REX 256KiB EPROM Cart
  - 28: Mikro Assembler

- 29: Final Cartridge Plus
- 30: Action Replay MK4
- 31: Stardos
- 32: EasyFlash
- 33: EasyFlash Xbank
- 34: Capture
- 35: Action Replay MK3
- 36: Retro Replay
- 37: MMC64
- 38: MMC Replay
- 39: IDE64
- 40: Super Snapshot V4
- 41: IEEE-488 Interface
- 42: Game Killer
- 43: Prophet64
- 44: EXOS
- 45: Freeze Frame
- 46: Freeze Machine
- 47: Snapshot 64
- 48: Super Explode V5.0
- 49: Magic Voice
- 50: Action Replay MK2
- 51: MACH 5
- 52: Diashow-Maker
- 53: Pagefox
- 54: Kingsoft
- 55: Silverrock 128KiB Cartridge
- 56: Formel 64
- 57: RGCD
- 58: RR-Net MK3
- 59: EasyCalc
- 60: GMod2
- 61: MAX Basic
- 62: GMod3
- 63: ZIPP-CODE 48
- 64: Blackbox V8
- 65: Blackbox V3
- 66: Blackbox V4
- 67: REX RAM-Floppy

- 68: BIS-Plus
- 69: SD-BOX
- 70: MultiMAX
- 71: Blackbox V9
- 72: Lt. Kernal Host Adaptor
- 73: RAMLink
- 74: Drean
- 75: IEEE Flash! 64
- 76: Turtle Graphics II
- 77: Freeze Frame MK2
- 78: Partner 64
- 79: Hyper-BASIC
- 80: Universal Cartridge 1
- 81: Universal Cartridge 1.5
- 82: Universal Cartridge 2
- 83: BMP Data Turbo
- 84: Profi-DOS
- 85: Magic Desk 16
- 86: Protovision "Megabyter"
- 87: Magic Desk Plus

`CartridgeFile`
: String specifying the filename of the image for the cartridge emulated in the "main" slot.

`Comal80Revision`
: Integer specifying the Comal 80 hardware revision. (0: Grey, 1: Commodore/Black)

`DQBB`
: Boolean specifying whether the Double Quick Brown Box should be emulated or not.

`DQBBSize`
: Integer specifying the RAM size (16, 32, 64, 128, 256kiB).

`DQBBMode`
: Boolean specifying the position of the slide/mode switch (0: C128, 1: C64).

`DQBBfilename`
: String specifying the filename of the DQBB RAM image.

`DQBBImageWrite`
: Boolean, if true write back the DQBB image file automatically, incase the RAM contents changed, when detaching or quitting the emulator.

`EasyFlashJumper`
: Boolean specifying whether the Easy Flash jumper is set.

`EasyFlashWriteCRT`
: Boolean, if true write back the Easy Flash image file automatically, incase the contents changed, when detaching or quitting the emulator.

`EasyFlashOptimizeCRT`
: Boolean, if true omit empty (filled with $ff) banks from the .crt image when writing.

`ExpertCartridgeEnabled`
: Boolean specifying whether the Expert Cartridge should be emulated or not.

`Expertfilename`
: String specifying the filename of the Expert Cartridge RAM image.

`ExpertImageWrite`
: Boolean, if true write back the Expert Cartridge image file automatically, incase the RAM contents changed, when detaching or quitting the emulator.

`ExpertCartridgeMode`
: Integer specifying the state of the expert cartridge switch. (0: off, 1: prg, 2: on)

`GMod2EEPROMImage`
: String that specifies the name of the raw GMod2 EEPROM image.

`GMod2FlashWrite`
: Boolean that specifies wether writes to GMod2 EEPROM image are enabled.

`GMod2EEPROMRW`
: Boolean that specifies wether the GMod2 ROM is saved at exit

`GMod3FlashWrite`
: Boolean that specifies wether changes to GMod3 EEPROM image are written back to the cartridge image.

`IDE64version`
: Integer specifying whether the emulated card version is V3.4, V4.1 or V4.2. This is automatically detected most of the time for .crt cartridge images.

`IDE64Image1`
`IDE64Image2`
`IDE64Image3`
`IDE64Image4`
: Strings specifying the full path to the four harddisk images. If a file is non-existing the drive is not emulated. Some older IDEDOS versions only support the first two harddisks.

`IDE64Cylinders1`
`IDE64Cylinders2`
`IDE64Cylinders3`
`IDE64Cylinders4`
: Integers specifying the number of cylinders for the four harddisk images. (1..65535)

`IDE64Heads1`
`IDE64Heads2`
`IDE64Heads3`
`IDE64Heads4`
: Integers specifying the number of heads for the four harddisk images. (1..16)

`IDE64Sectors1`
`IDE64Sectors2`
`IDE64Sectors3`
`IDE64Sectors4`
: Integers specifying the number of sectors for the four harddisk images. (1..63)

`IDE64AutodetectSize1`
`IDE64AutodetectSize2`
`IDE64AutodetectSize3`
`IDE64AutodetectSize4`
: Booleans specifying whether the disk geometry should be auto detected based on the disk image for the respective harddisk, or the cylinder/head/sector resources above should be used.

`IDE64USBServerAddress`
: String specifying the address the IDE64 USB server listens to (ip4://127.0.0.1:64245)

`IDE64USBServer`
: Boolean specifying whether the IDE64 USB server is enabled.

`IDE64RTCSave`
: Boolean specifying whether the IDE64 RTC data should be saved when changed or not.

`IDE64ClockPort`
: Integer that specifies the enabled IDE64 Clockport device. (0: None, 2: RRNet, 4: MP3@64)

`SBDIGIMAX`
: Boolean that specifies whether the Short Bus DigiMAX expansion is enabled.

`SBDIGIMAXbase`
: Integer specifying the Base address of the Short Bus DigiMAX expansion. (0xDE40/0xDE48)

`SBETFE` Boolean specifying whether the Short Bus ETFE expansion is enabled

`SBETFEbase`
: Integer specifying the Base address of the Short Bus ETFE expansion. ($de00, $de10, $df00)

`IEEE488` Boolean specifying whether the IEEE488 interface should be emulated or not.

`IEEE488Image`
: String specifying the filename of the IEEE488 ROM image.

`IEEEFlash64`
: Boolean specifying whether the IEEE Flash! 64 interface should be emulated or not.

`IEEEFlash64Image`
: String specifying the filename of the IEEE Flash! 64 KERNAL ROM image.

`IEEEFlash64Dev8`
`IEEEFlash64Dev910`
`IEEEFlash64Dev4`
: Booleans indicating whether to route devices 8, 9 and 10, and 4 respectively to the IEEE bus (1) or IEC bus (0).

`IsepicCartridgeEnabled`
: Boolean specifying whether ISEPIC should be emulated or not.

`Isepicfilename`
: String specifying the filename of the ISEPIC RAM image.

`IsepicSwitch`
: Boolean specifying the status of the ISEPIC switch. (0: off, 1: on)

`IsepicImageWrite`
: Boolean, if true write back the ISEPIC image file automatically, incase the RAM contents changed, when detaching or quitting the emulator.

`LTKimage0`
`LTKimage1`
`LTKimage2`
`LTKimage3`
`LTKimage4`
`LTKimage5`
`LTKimage6`
: Strings specifying the full path to up to seven harddisk images. If a file is non-existing the drive is not emulated. The first drive is necessary.

`LTKio` Integer specifying the Base address of the I/O interface. (1: $de00, 2: $df00) Default is $df00.

`LTKport` Integer specifying the port number in multiplexed multi-computer/single-disk configurations. (0 to 15) Default is 0, the master.

`LTKserial`
: String specifying the serial number. It must be in the form `"00000000"` with all numeric digits. Default is 87000000. LTK DOS install disks are locked to their respective LTK host adapor boot ROM. This setting will override the boot ROM value. The floppy disk serial number can be changed with a sector editor on track 18, sector 18.

`MagicDeskPlusRevision`
: Integer specifying the Magic Desk Plus hardware revision. (0: SRAM + 32KiB EEPROM, 1: SRAM + 8KiB EEPROM, 2: 32KiB EEPROM, 3: 8KiB EEPROM or 4: SRAM only)

`MagicDeskPlusSRAMImage`
: String specifying the filename of the optional 128KiB Magic Desk Plus SRAM image. Without an image, SRAM is still emulated, but its contents are temporary.

`MagicDeskPlusSRAMWrite`
: Boolean specifying whether the Magic Desk Plus SRAM image is saved when the cartridge is detached or the emulator exits.

`MagicDeskPlusEEPROMImage`
: String specifying the filename of the optional 8KiB or 32KiB Magic Desk Plus EEPROM image. Without an image, an 8KiB EEPROM is emulated temporarily.

`MagicDeskPlusEEPROMWrite`
: Boolean specifying whether the Magic Desk Plus EEPROM image is saved when the cartridge is detached or the emulator exits.

`MagicVoiceCartridgeEnabled`
: Boolean specifying whether the Magic Voice should be emulated or not.

`MagicVoiceImage`
: String specifying the filename of the Magic Voice ROM image.

`MegabyterWriteCRT`
: Boolean, if true write back the Megabyter image file automatically, incase the contents changed, when detaching or quitting the emulator.

`MegabyterOptimizeCRT`
: Boolean, if true omit empty (filled with $ff) banks from the .crt image when writing.

`MMC64` Boolean specifying whether the MMC64 should be emulated or not.

`MMC64BIOSfilename`
: String specifying the filename of the MMC64 Flash ROM image.

`MMC64_bios_write`
: Boolean, if true write back the MMC64 Flash ROM image file automatically, incase the contents changed, when detaching or quitting the emulator.

`MMC64_flashjumper`
: Boolean that specifies whether the MMC64 flash jumper is set.

`MMC64_revision`
: Integer specifying the MMC64 hardware revision. (0: Revision A, 1: Revision B)

`MMC64imagefilename`
: String specifying the filename of the SD-Card image used by the MMC64 emulation.

`MMC64_RO` Boolean, if true the SD-Card image is mounted read-only.

`MMC64_sd_type`
: Integer that specifies the reported type for the emulated SD-Card. (0: Auto, 1: MMC, 2: SD, 3: SDHC)

`MMC64ClockPort`
: Integer that specifies the clockport device used. (0: None, 1: ETH64-II, 2: RRNet, 3: Silver Surfer, 4: MP3@64, 5: Catweasel MkIII SID)

`MMCRCardImage`
: String specifying the filename of the SD-Card image used by the MMCR emulation.

`MMCREEPROMImage`
: String specifying the filename of the MMCR EEPROM image.

`MMCRRescueMode`
: Boolean specifying if the rescue mode (both buttons pressed during powerup) of the MMCR is active.

`MMCRImageWrite`
: Boolean, if true write back the MMCR Flash ROM image file automatically, incase the contents changed, when detaching or quitting the emulator.

`MMCRCardRW`
: Boolean specifying if the SD-Card image used by the MMCR emulation is writeable.

`MMCRSDType`
: Integer that specifies the reported type for the emulated SD-Card. (0: Auto, 1: MMC, 2: SD, 3: SDHC)

`MMCREEPROMRW`
: Boolean specifying if the MMCR EEPROM image is writeable.

`MMCRClockPort`
: Integer that specifies the clockport device. (0: None, 1: ETH64-II, 2: RRNet, 3: Silver Surfer, 4: MP3@64, 5: Catweasel MkIII SID)

`RAMCART` Boolean specifying whether the RAMCart should be emulated or not.

`RAMCARTfilename`
: String specifying the filename of the RAMCart RAM image.

`RAMCARTImageWrite`
: Boolean, if true write back the RAMCart image file automatically, incase the RAM contents changed, when detaching or quitting the emulator.

`RAMCART_RO`
: Boolean, if true the RAMCart contents are read only.

`RAMCARTsize`
: Integer specifying the size of the RAMCart in KiB. (64, 128)

`RAMLINK` Boolean specifying whether the RAMLink module should be emulated or not. This option emulates the behavior of the enable/disable switch on the unit.

`RAMLINKBIOSfilename`
: String specifying the filename of the RAMLINK ROM image.

`RAMLINKfilename`
: String specifying the filename of the RAMCard image.

`RAMLINKImageWrite`
: Boolean, if true write back the RAMCard image file automatically, in case the RAM contents changed, when detaching or quitting the emulator.

`RAMLINKsize`
: Integer specifying the size of the RAMCard in MiB. (0..16; default is 0)

`RAMLINKmode`
: Integer specifying the mode of the RAMLink. "1", the default, (Normal) only allows RAMLink access to the RAM port (REU, GEORAM, and RAMDrive); their registers are hidden otherwise. "0" (Direct) exposes their registers all the time. This option emulates the behavior of the Normal/Direct switch on the unit.

`RAMLINKRTCSave`
: Boolean specifying whether the RTC data should be saved when changed or not.

`RRFfilename`
: String that specifies the filename for the REX RAM-Floppy RAM image

`RRFImageWrite`
: Boolean, if true write back the REX RAM-Floppy RAM image file automatically, incase the contents changed, when detaching or quitting the emulator.

`RGCDrevision`
: Integer specifying the RGCD-cartridge hardware variant. (0: RGCD 64k, 1: Hucky's EPROM Cartridge)

`RRrevision`
: Integer specifying the RR hardware revision. (0: Retro Replay, 1: Nordic Replay)

`RRFlashJumper`
: Boolean specifying whether the RR flash jumper is set or not.

`RRBankJumper`
: Boolean specifying whether the RR bank jumper is set or not.

`RRBiosWrite`
: Boolean, if true write back the RR Flash ROM image file automatically, incase the contents changed, when detaching or quitting the emulator.

`RRClockPort`
: Integer that specifies the clockport device. (0: None, 1: ETH64-II, 2: RRNet, 3: Silver Surfer, 4: MP3@64, 5: Catweasel MkIII SID)

`RRNETMK3_flashjumper`
: Boolean specifying whether the RRNETMK3 Flash Jumper is set.

`RRNETMK3_bios_write`
: Boolean specifying whether to save the RRNETMK3 bios when changed.

`SSRamExpansion`
: Boolean, if true enable the 32KiB addon RAM of the Supersnapshot V5

7.1.2.2 C64 cartridge command-line options

`-iocollision <method>`
: Select the way the I/O collisions should be handled (`IOCollisionHandling`). (0: error message and detach all involved carts, 1: error message and detach last attached involved carts, 2: warning in log and 'AND' the valid return values

`+cart`
: Disable all cartridges (which would eventually be enabled in the config file).

`-cartreset`
`+cartreset`
: Reset/Do not reset machine if a cartridge is attached or detached (`CartridgeReset=1`, `CartridgeReset=0`).

`-cart8 <name>`
: Attach generic 8KiB cartridge image.

`-cart16 <name>`
: Attach generic 16KiB cartridge image.

`-cartultimax <name>`
: Attach generic 16KiB Ultimax cartridge image.

`-cartcrt <name>`
: Attach CRT cartridge image.

`-cartap <name>`
: Attach raw 32KiB Atomic Power cartridge image.

`-cartar2 <name>`
: Attach raw 16KiB Action Replay MK2 cartridge image.

`-cartar3 <name>`
: Attach raw 16KiB Action Replay MK3 cartridge image.

`-cartar4 <name>`
: Attach raw 32KiB Action Replay MK4 cartridge image.

`-cartar5 <name>`
: Attach raw 32KiB Action Replay cartridge image.

`-cartbis <name>`
: Attach raw 8KiB BIS-Plus cartridge image.

`-cartbb3 <name>`
: Attach raw 8KiB Blackbox V3 cartridge image.

`-cartbb4 <name>`
: Attach raw 16KiB Blackbox V4 cartridge image.

`-cartbb8 <name>`
: Attach raw 32/64KiB Blackbox V8 cartridge image.

`-cartbb9 <name>`
: Attach raw 32KiB Blackbox V9 cartridge image.

`-cartbdt <name>`
: Attach raw 16KiB BMP Data Turbo 2000 cartridge image.

`-cartcap <name>`
: Attach raw 8KiB Capture cartridge image.

`-cartcomal <name>`
: Attach raw 64KiB Comal 80 cartridge image.

`-comal80rev <Revision>`
: Set the Comal 80 hardware revision (`Comal80Revision`). (0: Grey, 1: Commodore/Black)

`-cartdep256 <name>`
: Attach raw Dela EP256 cartridge image.

`-cartdep64 <name>`
: Attach raw Dela EP64 cartridge image.

`-cartdep7x8 <name>`
: Attach raw Dela EP7x8 cartridge image.

`-cartdin <name>`
: Attach raw 128KiB Dinamic cartridge image.

`-cartdsm <name>`
: Attach raw 8KiB Diashow-Maker cartridge image.

`-cartdqbb <name>`
: Attach raw 16KiB Double Quick Brown Box cartridge image.

`-dqbb`
`+dqbb`
: Enable/disable Double Quick Brown Box (`DQBB=1`, `DQBB=0`).

`-dqbbsize <size>`
: Specify the RAM size (16, 32, 64, 128, 256kiB) (`DQBBsize`).

`-dqbbmode <mode>`
: Specify the position of the slide/mode switch (`DQBBmode=0`: C128, `DQBBmode=1`: C64).

`-dqbbimage <name>`
: Specify Double Quick Brown Box filename (`DQBBfilename`).

`-dqbbimagerw`
`+dqbbimagerw`
: Allow/disallow writing to DQBB image (`DQBBImageWrite=1`, `DQBBImageWrite=0`).

`-carteasy <name>`
: Attach raw EasyFlash cartridge image.

`-carteasycalc <name>`
: Attach raw 24KiB Easy Calc Result cartridge image

`-easyflashjumper`
`+easyflashjumper`
: Enable/disable EasyFlash jumper (`EasyFlashJumper=1`, `EasyFlashJumper=0`).

`-easyflashcrtwrite`
`+easyflashcrtwrite`
: Allow/Disallow writing to EasyFlash .crt image (`EasyFlashWriteCRT=1`, `EasyFlashWriteCRT=0`).

`-easyflashcrtoptimize`
`+easyflashcrtoptimize`
: Allow/Disallow EasyFlash .crt image optimizing (omitting of empty banks) on write (`EasyFlashOptimizeCRT=1`, `EasyFlashOptimizeCRT=0`).

`-cartepyx <name>`
: Attach raw 8KiB Epyx FastLoad cartridge image.

`-cartexos <name>`
: Attach raw 8KiB EXOS cartridge image.

`-cartexpert <name>`
: Attach raw 8KiB Expert Cartridge image.

`-expert`
`+expert`
: Enable/Disable the Expert Cartridge (`ExpertCartridgeEnabled=1`, `ExpertCartridgeEnabled=0`).

`-expertimagename <name>`
: Set Expert Cartridge image name (`Expertfilename`).

`-expertimagerw`
`+expertimagerw`
: Allow/Disallow writing to Expert Cartridge image (`ExpertImageWrite=1`, `ExpertImageWrite=0`).

`-expertmode <mode>`
: Set Expert Cartridge mode (`ExpertCartridgeMode`). (0: off, 1: prg, 2: on)

`-cartf64 <Name>`
: Attach raw 32KiB Formel 64 image.

`-cartfc1 <name>`
: Attach raw 16KiB Final Cartridge image.

`-cartfc3 <name>`
: Attach raw 64KiB Final Cartridge III image.

`-cartfcplus <name>`
: Attach raw 32KiB Final Cartridge Plus image.

`-cartff <name>`
: Attach raw 8KiB Freeze Frame image.

`-cartff2 <name>`
: Attach raw 16KiB Freeze Frame MK2 image.

`-cartfm <name>`
: Attach raw 32KiB Freeze Machine image.

`-cartfp <name>`
: Attach raw 128KiB Fun Play/Power Play cartridge image.

`-cartgmod2 <name>`
: Attach raw GMod2 cartridge image.

`-gmod2eepromimage <name>`
: Attach raw GMod2 EEPROM image (`GMod2EEPROMImage`).

`-gmod2eepromrw`
`+gmod2eepromrw`
: Enable/Disable writes to GMod2 EEPROM image (`GMod2EEPROMRW=1`, `GMod2EEPROMRW=0`).

`-gmod2flashwrite`
`+gmod2flashwrite`
: Enable/Disable saving of the GMod2 ROM at exit (`GMod2FlashWrite=1`, `GMod2FlashWrite=0`).

`-cartgmod3 <name>`
: Attach raw GMod3 cartridge image.

`-gmod3flashwrite`
`+gmod3flashwrite`
: Enable/Disable saving of the GMod3 ROM at exit (`GMod3FlashWrite=1`, `GMod3FlashWrite=0`).

`-cartgk <name>`
: Attach raw 8KiB Game Killer cartridge image.

`-cartgs <name>`
: Attach raw 512KiB Game System cartridge image.

`-carthyper <name>`
: Attach raw 64KiB Hyper-BASIC cartridge image.

`-cartdrean <name>`
: Attach raw 32KiB Drean (eg H.E.R.O. bootleg) cartridge image

`-cartide64 <name>`
: Attach raw 64KiB or 128KiB IDE64 cartridge image.

`-IDE64image1 <name>`
`-IDE64image2 <name>`
`-IDE64image3 <name>`
`-IDE64image4 <name>`
: Specify path to the image files for IDE64 harddisks (`IDE64Image1`, `IDE64Image2`, `IDE64Image3`, `IDE64Image4`).

`-IDE64cyl1 <value>`
`-IDE64cyl2 <value>`
`-IDE64cyl3 <value>`
`-IDE64cyl4 <value>`
: Set number of cylinders for the IDE64 harddisk emulation (`IDE64Cylinders1`, `IDE64Cylinders2`, `IDE64Cylinders3`, `IDE64Cylinders1`). (1..65535)

`-IDE64hds1 <value>`
`-IDE64hds2 <value>`
`-IDE64hds3 <value>`
`-IDE64hds4 <value>`
Set number of heads for the IDE64 harddisk emulation (`IDE64Heads1`, `IDE64Heads2`, `IDE64Heads3`, `IDE64Heads4`). (1..16)

`-IDE64sec1 <value>`
`-IDE64sec2 <value>`
`-IDE64sec3 <value>`
`-IDE64sec4 <value>`
Set number of sectors for the IDE64 harddisk emulation (`IDE64Sectors1`, `IDE64Sectors2`, `IDE64Sectors3`, `IDE64Sectors4`). (1..63)

`-IDE64autosize1`
`+IDE64autosize1`
Autodetect geometry of formatted image or do not autodetect and use specified geometry (`IDE64AutodetectSize1=1`, `IDE64AutodetectSize1=0`).

`-IDE64autosize2`
`+IDE64autosize2`
Autodetect geometry of formatted image or do not autodetect and use specified geometry (`IDE64AutodetectSize2=1`, `IDE64AutodetectSize2=0`).

`-IDE64autosize3`
`+IDE64autosize3`
Autodetect geometry of formatted image or do not autodetect and use specified geometry (`IDE64AutodetectSize3=1`, `IDE64AutodetectSize3=0`).

`-IDE64autosize4`
`+IDE64autosize4`
Autodetect geometry of formatted image or do not autodetect and use specified geometry (`IDE64AutodetectSize4=1`, `IDE64AutodetectSize4=0`).

`-IDE64version <value>`
Select IDE64 version V3 (0), V4.1 (1) or V4.2 (2). (`IDE64version`). (0..2)

`-IDE64USB`
`+IDE64USB`
Enable/Disable IDE64 USB server

`-IDE64USBAddress <name>`
The local address the IDE64 USB server should bind to

`-IDE64rtcsave`
`+IDE64rtcsave`
Enable/disable saving of IDE64 RTC data when changed (`IDE64RTCSave=1`, `IDE64RTCSave=0`).

`-ide64clockportdevice`
Enable IDE64 Clockport device (0: None, 2: RRNet, 4: MP3@64) (`IDE64ClockPort`).

`-sbdigimax`
`+sbdigimax`
: Enable/Disable the Short Bus DigiMAX expansion (`SBDIGIMAX`).

`-sbdigimaxbase`
: Set Base address of the Short Bus DigiMAX expansion (0xDE40/0xDE48) (`SBDIGIMAXbase`).

`-sbetfe`
`+sbetfe`
: Enable/Disable the Short Bus ETFE expansion (`SBETFE`).

`-sbetfebase`
: Set Base address of the Short Bus ETFE expansion (56832: $de00, 56848: $de10, 57088: $df00) (`SBETFEbase`).

`-cartieee <name>`
: Attach CBM IEEE-488 cartridge image.

`-ieee488`
`+ieee488`
: Enable/disable emulation of the IEEE488 interface. Remember to attach a suitable ROM image before enabling the module. (`IEEE488=1`, `IEEE488=0`).

`-ieee488image <name>`
: Set IEEE488 interface ROM image name (`IEEE488Image`).

`-ieeeflash64`
`+ieeeflash64`
: Enable/disable IEEE Flash! 64 interface. Remember to attach a suitable ROM image before enabling the module. (`IEEEFlash64`).

`-ieeeflash64image`
: Set IEEE Flash! 64 ROM image name. (`IEEEFlash64Image`).

`-ieeeflash64dev8`
`+ieeeflash64dev8`
: Route/do not route device 8 to IEEE bus of IEEE Flash! 64 interface (`IEEEFlash64Dev8`).

`-ieeeflash64dev910`
`+ieeeflash64dev910`
: Route/do not route devices 9 and 10 to IEEE bus of IEEE Flash! 64 interface (`IEEEFlash64Dev910`).

`-ieeeflash64dev4`
`+ieeeflash64dev4`
: Route/do not route device 4 to IEEE bus of IEEE Flash! 64 interface (`IEEEFlash64Dev4`).

`-cartieeeflash64 <Name>`
: Attach raw 8KiB IEEE Flash! 64 cartridge image

`-isepic`
`+isepic`
: Enable/disable the ISEPIC cart (`IsepicCartridgeEnabled=1`, `IsepicCartridgeEnabled=0`).

`-isepicswitch`
`+isepicswitch`
: Enable/disable the ISEPIC switch (`IsepicSwitch=1`, `IsepicSwitch=0`).

`-cartisepic <name>`
: Attach raw 2KiB ISEPIC cartridge image.

`-isepicimagename <name>`
: Set ISEPIC image name (`Isepicfilename`).

`-isepicimagerw`
`+isepicimagerw`
: Allow/disallow writing to ISEPIC image (`IsepicImageWrite=1`, `IsepicImageWrite=0`).

`-cartkcs <name>`
: Attach raw 16KiB KCS Power cartridge image.

`-cartks <name>`
: Attach raw 24KiB Kingsoft cartridge image.

`-cartltk <name>`
: Attach raw Lt. Kernal Host Adaptor cartridge image.

`-ltkimage0 <name>`
`-ltkimage1 <name>`
`-ltkimage2 <name>`
`-ltkimage3 <name>`
`-ltkimage4 <name>`
`-ltkimage5 <name>`
`-ltkimage6 <name>`
: Strings specifying the full path to up to seven harddisk images. If a file is non-existing the drive is not emulated. The first drive is necessary. (`ltkimage0`, `ltkimage1`, `ltkimage2`, `ltkimage3`, `ltkimage4`, `ltkimage5`, `ltkimage6`).

`-ltkio <value>`
: Integer specifying the Base address of the I/O interface. (1: $de00, 2: $df00) Default is $df00.

`-ltkport <value>`
: Integer specifying the port number in multiplexed multi-computer/single-disk configurations. (0..15) Default is 0, the master.

`-ltkserial <name>`
: String specifying the serial number. It must be in the form "00000000" with all numeric digits. Default is 87000000. LTK DOS install disks are locked to their respective LTK host adapor boot ROM. This setting will override the boot ROM value. The floppy disk serial number can be changed with a sector editor on track 18, sector 18.

`-cartmach5 <name>`
: Attach raw 8KiB MACH 5 cartridge image.

`-cartmb <name>`
: Attach raw Megabyter cartridge image.

`-cartmd <name>`
: Attach raw 32/64/128KiB Magic Desk cartridge image.

`-cartmd16 <name>`
: Attach raw up to 2048KiB Magic Desk 16K cartridge image.

`-cartmf <name>`
: Attach raw Magic Formel cartridge image.

`-cartmax <name>`
: Attach raw MAX Basic cartridge image.

`-cartmm <name>`
: Attach raw MultiMAX cartridge image.

`-cartmikro <name>`
: Attach raw 8KiB Mikro Assembler cartridge image.

`-megabytercrtwrite`
`+megabytercrtwrite`
: Allow/Disallow writing to Megabyter .crt image (`MegabyterWriteCRT=1`, `MegabyterWriteCRT=0`).

`-megabytercrtoptimize`
`+megabytercrtoptimize`
: Allow/Disallow Megabyter .crt image optimizing (omitting of empty banks) on write (`MegabyterOptimizeCRT=1`, `MegabyterOptimizeCRT=0`).

`-mmc64`
`+mmc64`
: Enable/disable the MMC64 expansion (`MMC64=1`, `MMC64=0`).

`-cartmmc64 <name>`
: Attach raw 8KiB MMC64 cartridge image.

`-mmc64bios <name>`
: Specify name of MMC64 BIOS image (`MMC64BIOSfilename`).

`-mmc64image <name>`
: Specify name of MMC64 image (`MMC64imagefilename`).

`-mmc64readonly`
: Set the MMC64 card to read-only (`MMC64_RO=1`).

`-mmc64readwrite`
: Set the MMC64 card to read/write (`MMC64_RO=0`).

`-mmc64flash`
`+mmc64flash`
: Enable/Disable the MMC64 flash jumper (`MMC64_flashjumper=1`, `MMC64_flashjumper=0`).

`-mmc64bioswrite`
: Save the MMC64 bios when changed (`MMC64_bios_write`=1).

`-mmc64biosreadonly`
: Do not save the MMC64 bios when changed (`MMC64_bios_write`=0).

`-mmc64rev <revision>`
: Specify MMC64 revision (`MMC64_revision`). (0: Revision A, 1: Revision B)

`-mmc64sdtype <type>`
: Specify MMC64 SD type (`MMC64_sd_type`). (0: Auto, 1: MMC, 2: SD, 3: SDHC)

`-mmc64clockportdevice <device>`
: Set MMC64 clockport device (`MMC64ClockPort`) (0: None, 1: ETH64-II, 2: RRNet, 3: Silver Surfer, 4: MP3@64, 5: Catweasel MkIII SID)

`-cartmmcr <name>`
: Attach raw 512KiB MMC Replay cartridge image.

`-mmcrrescue`
`+mmcrrescue`
: Enable/disable MMC Replay rescue mode (`MMCRRescueMode=1`, `MMCRRescueMode=0`).

`-mmcrimagerw`
`+mmcrimagerw`
: Allow/disallow writing to MMC Replay image (`MMCRImageWrite=1`, `MMCRImageWrite=0`).

`-mmcrsdtype <type>`
: Specify MMC Replay SD type (`MMCRSDType`). (0: Auto, 1: MMC, 2: SD, 3: SDHC)

`-mmcrcardimage <filename>`
: Specify MMC Replay card image filename (`MMCRCardImage`).

`-mmcrcardrw`
`+mmcrcardrw`
: Allow/disallow writes to MMC Replay card image (`MMCRCardRW=1`, `MMCRCardRW=0`).

`-mmcreepromimage <filename>`
: Specify MMC Replay EEPROM image filename (`MMCREEPROMImage`).

`-mmcreepromrw`
`+mmcreepromrw`
: Allow/disallow writes to MMC Replay EEPROM image (`MMCREEPROMRW=1`, `MMCREEPROMRW=0`).

`-mmcrclockportdevice <id>`
: Set MMC Replay clockport device (`MMCRClockPort`). (0: None, 1: ETH64-II, 2: RRNet, 3: Silver Surfer, 4: MP3@64, 5: Catweasel MkIII SID)

`-cartmdp <name>`
: Attach raw 256/512/1024KiB Magic Desk Plus cartridge image. The revision selected by `MagicDeskPlusRevision` is used.

`-magicdeskplusrevision <revision>`
: Specify Magic Desk Plus revision (`MagicDeskPlusRevision`). (0: SRAM + 32KiB EEPROM, 1: SRAM + 8KiB EEPROM, 2: 32KiB EEPROM, 3: 8KiB EEPROM or 4: SRAM only)

`-magicdeskplussramimage <filename>`
: Specify Magic Desk Plus SRAM image filename (`MagicDeskPlusSRAMImage`).

`-magicdeskplussramwrite`
`+magicdeskplussramwrite`
: Allow/disallow writes to Magic Desk Plus SRAM image (`MagicDeskPlusSRAMWrite=1`, `MagicDeskPlusSRAMWrite=0`).

`-magicdeskpluseepromimage <filename>`
: Specify Magic Desk Plus EEPROM image filename (`MagicDeskPlusEEPROMImage`).

`-magicdeskpluseepromwrite`
`+magicdeskpluseepromwrite`
: Allow/disallow writes to Magic Desk Plus EEPROM image (`MagicDeskPlusEEPROMWrite=1`, `MagicDeskPlusEEPROMWrite=0`).

`-cartmv <name>`
: Attach raw 16KiB Magic Voice cartridge image.

`-magicvoiceimage <name>`
: Specify Magic Voice cartridge ROM image filename (`MagicVoiceImage`).

`-magicvoice`
`+magicvoice`
: Enable/disable Magic Voice cartridge. Remember to attach a suitable Magic Voice ROM image before enabling the module. (`MagicVoiceCartridgeEnabled=1`, `MagicVoiceCartridgeEnabled=0`).

`-cartocean <name>`
: Attach raw Ocean cartridge image.

`-cartp64 <name>`
: Attach raw 256KiB Prophet 64 cartridge image.

`-cartpd <name>`
: Attach raw 16KiB Profi-DOS cartridge image

`-cartpf <name>`
: Attach raw 64KiB Pagefox cartridge image.

`-cartpartner64 <name>`
: Attach raw 16KiB Partner 64 cartridge image.

`-cartramcart <name>`
: Attach raw RamCart cartridge image.

`-ramcart`
`+ramcart` Enable/disable the RAMCART expansion (`RAMCART=1`, `RAMCART=0`).

`-ramcartsize <size in KiB>`
: Size of the RAMCART expansion (`RAMCARTsize`). (64, 128)

`-ramcartimage <name>`
Specify name of RAMCART image (`RAMCARTfilename`).

`-ramcartimagerw`
`+ramcartimagerw`
Allow/disallow writing to RAMCart image (`RAMCARTImageWrite=1`, `RAMCARTImageWrite=0`).

`-ramcartro`
Set the RamCart switch to read-only (`RAMCART_RO=1`).

`-ramcartrw`
Set the RamCart switch to read-only (`RAMCART_RO=0`).

`-ramlink`
`+ramlink` Enable/disable emulation of the RAMLink module. This option emulates the behavior of the enable/disable switch on the unit. Remember to attach a suitable BIOS image before enabling the module. (`RAMLINK=1`, `RAMLINK=0`).

`-cartramlink <name>`
Attach the raw 64 KiB ROM image.

`-ramlinkbios <name>`
Specify name of RAMLink BIOS image (`RAMLINKBIOSfilename`).

`-ramlinkimage <name>`
Specify name of RAMLink RAM image (`RAMLINKfilename`).

`-ramlinkimagerw`
`+ramlinkimagerw`
Allow/disallow writing to RAMLink RAM image on detach (`RAMLINKImageWrite=1`, `RAMLINKImageWrite=0`).

`-ramlinksize <size in MiB>`
Size of the RAMLink RAM (`RAMLINKsize`). (0..16; default is 0)

`-ramlinkmode <value>`
Sets the RAM port mode of the unit. `"1"`, the default, (Normal) only allows RAMLink access to the RAM port (REU, GEORAM, and RAMDrive); their registers are hidden otherwise. `"0"` (Direct) exposes their registers all the time. This option emulates the behavior of the Normal/Direct switch on the unit. (`RAMLINKmode`). (1:normal (default), 0:direct)

`-ramlinkrtcsave`
`+ramlinkrtcsave`
Enable/disable saving of the RTC data when changed (`RAMLINKRTCSave=1`, `RAMLINKRTCSave=0`).

`-cartrep256 <name>`
Attach raw REX EP256 cartridge image.

`-cartrgcd <Name>`
Attach raw 64KiB RGCD cartridge image.

`-rgcdrev <Revision>`
: Set the RGCD-cartridge hardware variant. (`RGCDrevision`). (0: RGCD 64k, 1: Hucky's EPROM Cartridge)

`-cartross <name>`
: Attach raw 16/32KiB ROSS cartridge image.

`-cartrrnet <name>`
: Attach raw 8KiB RR-Net MK3 cartridge image.

`-cartrr <name>`
: Attach raw 64KiB Retro Replay cartridge image.

`-rrbioswrite`
`+rrbioswrite`
: Enable/disable saving of the RR ROM at exit (`RRBiosWrite=1`, `RRBiosWrite=0`).

`-rrbankjumper`
`+rrbankjumper`
: Set/unset RR Bank Jumper (`RRBankJumper=1`, `RRBankJumper=0`).

`-rrflashjumper`
`+rrflashjumper`
: Set/unset RR Flash Jumper (`RRFlashJumper=1`, `RRFlashJumper=0`).

`-rrrev <Revision>`
: Set the RR revision (`RRrevision`). (0: Retro Replay, 1: Nordic Replay)

`-rrclockportdevice <device>`
: Set the RR clockport device (`RRClockPort`) (0: None, 1: ETH64-II, 2: RRNet, 3: Silver Surfer, 4: MP3@64, 5: Catweasel MkIII SID)

`-rrnetmk3flash`
`+rrnetmk3flash`
: SetRemove the RRNETMK3 Flash Jumper (`RRNETMK3_flashjumper=1`, `RRNETMK3_flashjumper=0`).

`-rrnetmk3bioswrite`
`+rrnetmk3bioswrite`
: Save/Do not save the RRNETMK3 bios when changed (`RRNETMK3_bios_write=1`, `RRNETMK3_bios_write=0`).

`-cartrrf <name>`
: Attach raw 8KiB REX RAM-Floppy cartridge image.

`-rexramfloppyimage <name>`
: Set filename for the REX RAM-Floppy RAM image (`RRFfilename`).

`-rexramfloppyimagerw`
`+rexramfloppyimagerw`
: Save/Do not save the REX RAM-Floppy RAM when changed (`RRFImageWrite=1`, `RRFImageWrite=0`).

`-cartru <name>`
: Attach raw 8KiB REX Utility cartridge image.

`-cartsdbox <name>`
: Attach raw 128KiB SD-BOX cartridge image.

`-carts64 <name>`
: Attach raw 4KiB Snapshot 64 cartridge image.

`-cartsb <name>`
: Attach raw Structured Basic cartridge image.

`-cartse5 <name>`
: Attach raw 16KiB Super Explode V5 cartridge image.

`-cartsg <name>`
: Attach raw 64KiB Super Games cartridge image.

`-cartsilver <Name>`
: Attach raw Silverrock 128 cartridge image.

`-cartsimon <name>`
: Attach raw 16KiB Simons' Basic cartridge image.

`-cartss4 <name>`
: Attach raw 32KiB Super Snapshot V4 cartridge image.

`-cartss5 <name>`
: Attach raw 64KiB or 128KiB Super Snapshot V5 cartridge image.

`-ssramexpansion`
`+ssramexpansion`
: Enable/disable 32KiB addon RAM. (`SSRamExpansion=1`, `SSRamExpansion=0`).

`-cartstar <name>`
: Attach raw 16KiB Stardos cartridge image.

`-cartturtle <name>`
: Attach raw 16KiB Turtle Graphics II cartridge image.

`-cartuc1 <name>`
: Attach raw 128KiB Universal Cartridge 1 image.

`-cartuc15 <name>`
: Attach raw 512KiB Universal Cartridge 1.5 image.

`-cartuc2 <name>`
: Attach raw 512KiB Universal Cartridge 2 image.

`-cartwl <name>`
: Attach raw 16KiB Westermann Learning cartridge image.

`-cartws <name>`
: Attach raw 8KiB Warp Speed cartridge image.

`-cartzaxxon <name>`
: Attach raw 16KiB Zaxxon cartridge image.

`-cartzipp <name>`
: Attach raw 8KiB ZIPP-CODE 48 cartridge image.

7.1.3 CIA settings

7.1.3.1 CIA resources

`CIA1Model`
: Integer specifying CIA1 model (all emulators except x64dtv, xpet, xplus4, xvic). (0: old 6526, 1: new 6526A)

`CIA2Model`
: Integer specifying CIA2 model (all emulators except x64dtv, xcbm2, xcbm5x0, xpet, xplus4, xvic). (0: old 6526, 1: new 6526A)

7.1.3.2 CIA command-line options

`-ciamodel <model>`
: Set both CIA models (`CIA1Model`, `CIA2Model`) (all emulators except x64dtv, xcbm2, xcbm5x0, xpet, xplus4, xvic). (0: old 6526, 1: new 6526A)

`-cia1model <model>`
: Set CIA1 model (`CIA1Model`) (all emulators except x64dtv, xpet, xplus4, xvic). (0: old 6526, 1: new 6526A)

`-cia2model <model>`
: Set CIA2 model (`CIA2Model`) (all emulators except x64dtv, xcbm2, xcbm5x0, xpet, xplus4, xvic). (0: old 6526, 1: new 6526A)

7.1.4 VIC-II settings

These settings control the emulation of the VIC-II (MOS6569) video chip used in both the C64 and the C128.

- "Sprite-sprite collisions" and "Sprite-background collisions", if enabled, cause the hardware detection of sprite-to-sprite and sprite-to-background collisions of the VIC-II to be emulated. This feature is used by many games, and disabling either of the two detection systems can sometimes make you invincible (although there is also a chance that also enemies become invincible then).
- "Color set" can be used to dynamically change the palette file being used by choosing one of the available predefined color sets, for example:
  - `pepto-pal.vpl` ("Pepto PAL"), a Palette calculated by Philip "Pepto" Timmermann (`http://www.pepto.de/projects/colorvic/`). (This is the default)
  - `vice.vpl` ("VICE"), the old default VICE palette.
  - `c64s.vpl` ("C64S"), palette taken from the shareware C64S emulator by Miha Peternel.
  - `ccs64.vpl` ("CCS64"), palette taken from the shareware CCS64 emulator by Per Håkan Sundell.
  - `frodo.vpl` ("Frodo"), palette taken from the free Frodo emulator by Christian Bauer (`https://frodo.cebix.net/`).
  - `pc64.vpl` ("PC64"), palette taken from the free PC64 emulator by Wolfgang Lorenz.
  - `godot.vpl` ("GoDot"), palette as suggested by the authors of the C64 graphics package GoDot (`https://www.godot64.de`).

7.1.4.1 VIC-II resources

`VICIIModel`
: Integer that specifies VIC-II model (x64sc, xscpu64 only). (6569, 6569r1, 8565, 6567, 8562, 6567r56a, 6572)

`VICIICheckSsColl`
: Boolean specifying whether the sprite-sprite hardware collision detection must be emulated.

`VICIICheckSbColl`
: Boolean specifying whether the sprite-background hardware collision detection must be emulated.

`VICIIVSPBug`
: Boolean specifying whether the "VSP Bug" must be emulated (x64sc, xscpu64 only).

`VICIIVideoCache`
: Boolean specifying whether the video cache is turned on.

`VICIIDoubleSize`
: Boolean specifying whether double-size mode is turned on.

`VICIIDoubleScan`
: Boolean specifying whether double-scan mode is turned on.

`VICIINewLuminances`
: Boolean specifying whether to use new (9 steps) luminances.

`VICIIPaletteFile`
: String specifying the name of the palette file being used. The `.vpl` extension is optional.

`VICIIExternalPalette`
: Boolean specifying whether to use external palette file or not.

`VICIIColorSaturation`
: Integer specifying saturation of internal calculated palette. (0..2000)

`VICIIColorContrast`
: Integer specifying contrast of internal calculated palette. (0..2000)

`VICIIColorBrightness`
: Integer specifying brightness of internal calculated palette. (0..2000)

`VICIIColorGamma`
: Integer specifying gamma of internal calculated palette. (0..4000)

`VICIIColorTint`
: Integer specifying tint of internal calculated palette. (0..2000)

`VICIIPALScanLineShade`
: Integer specifying amount of scan line shading for the CRT emulation. (0..1000)

`VICIIPALBlur`
: Integer specifying amount of horizontal blur for the CRT emulation. (0..1000)

`VICIIPALOddLinePhase`
: Integer specifying phase for color carrier in odd lines. (0..2000)

`VICIIPALOddLineOffset`
: Integer specifying phase offset for color carrier in odd lines. (0..2000)

`VICIIPALDelaylineType`
: Integer specifying the type of delay line used in the CRT (0: normal, 1: U only (1084 style))

`VICIIAudioLeak`
: Boolean specifying whether to enable/disable video to audio leak emulation.

`VICIIFilter`
: Integer specifying rendering filter. (0: None, 1: CRT emulation, 2: Scale2x)

`VICIIBorderMode`
: Integer specifying border display mode. (0: normal, 1: full, 2: debug, 3: none)

`VICIIVSync`
: Boolean specifying whether to enable vsync to prevent screen tearing. (0: vsync off, 1: vsync on)

`VICIIShowStatusbar`
: Boolean specifying whether to show the status bar or not (x64, x64sc, x64dtv, xscpu64, x128 and xcbm5x0).

`VICIIFullscreen`
: Boolean specifying whether to use fullscreen mode or not (all emulators except xcbm2, xpet, xplus4, xvic and vsid).

The following resources are only present if hardware scaling support (OpenGL or Direct-X) is present and used at compile time.

`VICIIAspectRatio`
: String/float specifying the aspect ratio. (0.5-2.0)

`VICIIAspectMode`
: Integer specifying the OpenGL aspect mode. (0: off, 1: custom, 2: true)

`VICIIGLFilter`
: Integer specifying the OpenGL filtering mode. (0: nearest neighbour, 1: bilinear, 2: bicubic)

`VICIIFlipX`
: Boolean to enable/disable OpenGL horizontal screen flip.

`VICIIFlipY`
: Boolean to enable/disable OpenGL vertical screen flip.

`VICIIRotate`
: Boolean to enable/disable OpenGL 90 degree clockwise rotation.

7.1.4.2 VIC-II command-line options

`-VICIIcheckss`
`+VICIIcheckss`
Enable/disable emulation of hardware sprite-sprite collision detection (`VICIICheckSsColl=1`, `VICIICheckSsColl=0`).

`-VICIIchecksb`
`+VICIIchecksb`
Enable/disable emulation of hardware sprite-background collision detection (`VICIICheckSbColl=1`, `VICIICheckSbColl=0`).

`-VICIIvspbug`
`+VICIIvspbug`
Enable/disable emulation of the "VSP bug" (`VICIIVSPBug=1`, `VICIIVSPBug=0`) (x64sc, xscpu64 only).

`-VICIIvcache`
`+VICIIvcache`
Enable/disable the video cache (`VICIIVideoCache=1`, `VICIIVideoCache=0`).

`-VICIIdsize`
`+VICIIdsize`
Enable/disable the double size mode (`VICIIDoubleSize=1`, `VICIIDoubleSize=0`).

`-VICIIdscan`
`+VICIIdscan`
Enable/disable the double scan mode (`VICIIDoubleScan=1`, `VICIIDoubleScan=0`).

`-VICIIfilter <Mode>`
Select rendering filter (`VICIIFilter`). (0: None, 1: CRT emulation, 2: Scale2x)

`-VICIIintpal`
Use an internal calculated palette (`VICIIExternalPalette=0`).

`-VICIIextpal`
Use an external palette (file) (`VICIIExternalPalette=1`).

`-VICIIpalette <Name>`
Specify the name of the palette file (`VICIIPaletteFile`).

`-VICIIborders <mode>`
Set VIC-II border display mode (`VICIIBorderMode`). (0: normal, 1: full, 2: debug, 3: none)

`-VICIImodel <model>`
Set VIC-II model (`VICIIModel`) (x64sc and xscpu64 only). (6569, 6569r1, 8565, 6567, 8562, 6567r56a, 6572)

`-VICIInewluminance`
`+VICIInewluminance`
Enable/disable new luminances (`VICIINewLuminances=1`, `VICIINewLuminances=0`).

`-VICIIsaturation <0-2000>`
Set saturation of internal calculated palette (`VICIIColorSaturation`).

`-VICIIcontrast <0-2000>`
Set contrast of internal calculated palette (`VICIIColorContrast`).

`-VICIIbrightness <0-2000>`
Set brightness of internal calculated palette (`VICIIColorBrightness`).

`-VICIIgamma <0-4000>`
Set gamma of internal calculated palette (`VICIIColorGamma`).

`-VICIItint <0-2000>`
Set tint of internal calculated palette (`VICIIColorTint`).

`-VICIIoddlinesphase <0-2000>`
Set phase for color carrier in odd lines (`VICIIPALOddLinePhase`).

`-VICIIoddlinesoffset <0-2000>`
Set phase offset for color carrier in odd lines (`VICIIPALOddLineOffset`).

`-VICIIcrtblur <0-1000>`
Amount of horizontal blur for the CRT emulation (`VICIIPALBlur`).

`-VICIIcrtscanlineshade <0-1000>`
Amount of scan line shading for the CRT emulation (`VICIIPALScanLineShade`).

`-VICIIcrtscanlineshade <type>`
Type of delay line used in the CRT (0: normal, 1: U only (1084 style)) (`VICIIPALDelaylineType`).

`-VICIIaudioleak`
`+VICIIaudioleak`
Enable/disable video to audio leak emulation (`VICIIAudioLeak=1`, `VICIIAudioLeak=0`).

`-VICIIshowstatusbar`
`+VICIIshowstatusbar`
Show/hide status bar (`VICIIShowStatusbar=1`, `VICIIShowStatusbar=0`).

`-VICIIvsync`
Enable vsync to prevent screen tearing (`VICIIVSync=1`).

`+VICIIvsync`
Disable vsync to allow screen tearing (`VICIIVSync=0`).

`-VICIIfull`
`+VICIIfull`
Enable/Disable fullscreen (`VICIIFullscreen=1`, `VICIIFullscreen=0`) (all emulators except xcbm2, xpet, xplus4, xvic and vsid).

The following command-line options are only present if hardware scaling support (OpenGL or Direct-X) is present and used at compile time.

`-VICIIaspectmode <mode>`
Set the aspect mode (`VICIIAspectMode`). (0: off, 1: custom, 2: true)

`-VICIIaspect <aspect ratio>`
: Set the aspect ratio (`VICIIAspectRatio`). (0.5-2.0)

`-VICIIglfilter <mode>`
: Set OpenGL (or Direct-X) filtering mode (0 = nearest, 1 = linear, 2 = bicubic) (`VICIIglfilter`).

`-VICIIflipx`
`+VICIIflipx`
: Enable/disable OpenGL horizontal screen flip (`VICIIFlipX=1`, `VICIIFlipX=0`).

`-VICIIflipy`
`+VICIIflipy`
: Enable/disable OpenGL vertical screen flip (`VICIIFlipY=1`, `VICIIFlipY=0`).

`-VICIIrotate`
`+VICIIrotate`
: Enable/disable OpenGL 90 degree rotation (`VICIIRotate=1`, `VICIIRotate=0`).

7.1.5 SID settings

These settings control the emulation of the SID (MOS6581 or MOS8580) audio chip.

- "Second SID" maps a second SID chip into the address space for stereo sound. This emulates e.g. the "SID Symphony Stereo Cartridge" from Dr. Evil Laboratories. The second SID can be used with software such as "Stereo SID Player" by Mark Dickenson or "The Enhanced Sidplayer" by Craig Chamberlain.
- "Second SID base address" sets the start address for the second SID chip. Software normally uses $DE00 or $DF00, since $DE00-$DEFF and $DF00-$DFFF can be mapped through the cartridge port of the C64. The default start address is $DE00.
- "Emulate filters" causes the built-in programmable filters of the SID chip to be emulated. A lot of C64 music requires them to be emulated properly, but their emulation requires some additional processor power.
- "ChipModel" specifies the model of the SID chip being emulated: there are two slightly different generations of SID chips: MOS6581 ones and MOS8580 ones. Additionally there is "8580D", which refers to MOS8580 with added "Digifix" modification, which adds a DC offset to audio-in, which makes classic samples audible also on the 8580.
- "Use reSID emulation" specifies whether the more accurate (and resource hungry) reSID emulation is turned on or off.
- "reSID sampling method" selects the method for conversion of the SID output signal to a sampling rate appropriate for playback by standard digital sound equipment. Possible settings are:
  - "Fast" simply clocks the SID chip at the output sampling frequency, picking the nearest sample. This yields acceptable sound quality, but sampling noise is noticeable in some cases, especially with SID combined waveforms. The sound emulation is still cycle exact.
  - "Interpolating" clocks the SID chip each cycle, and calculates each sample with linear interpolation. The sampling noise is now strongly attenuated by the SID external filter (as long as "Emulate filters" is selected), and the linear interpolation further improves the sound quality.

- "Resampling" clocks the SID chip each cycle, and uses the theoretically correct method for sample generation. This delivers CD quality sound, but is extremely CPU intensive, and is thus most useful for non-interactive sound generation. Unless you have a very fast machine, that is.

- "reSID resampling passband" specifies the percentage of the total bandwidth allocated to the resampling filter passband. The work rate of the resampling filter is inversely proportional to the remaining transition band percentage. This implies that e.g. with the transition band starting at ~ 20kHz, it is faster to generate 48kHz than 44.1kHz samples. For CD quality sound generation at 44.1kHz the passband percentage should be set to 90 (i.e. the transition band starting at almost 20kHz).

7.1.5.1 SID resources

`SidStereo`

Integer specifying the amount of emulated extra SIDs. (0: off, 1: 1 extra sid, 2: 2 extra sids, 3: three extra sids, 4: four extra sids, 5: five extra sids, 6: six extra sids, 7: seven extra sids, 8: eight extra sids, 9: nine extra sids)

`Sid2AddressStart`

Integer specifying the base address of the second SID (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`Sid3AddressStart`

Integer specifying the base address of the third SID (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`Sid4AddressStart`

Integer specifying the base address of the fourth SID (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0,

0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`Sid5AddressStart`

Integer specifying the base address of the fifth SID (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`Sid6AddressStart`

Integer specifying the base address of the sixth SID (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`Sid7AddressStart`

Integer specifying the base address of the seventh SID (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760,

0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`Sid8AddressStart`

Integer specifying the base address of the eigth SID (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`Sid9AddressStart`

Integer specifying the base address of the ninth SID (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`Sid10AddressStart`

Integer specifying the base address of the tenth SID (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`SidFilters`

Boolean specifying whether the built-in SID filters must be emulated.

`SidModel` Integer specifying what model of the SID must be emulated. (0: 6581, 1: 8580, 2: 8580D, 3: DTVSID)

`SidEngine`
Integer specifying what SID engine will be used. (0: FastSID, 1: ReSID, 2: Catweasel MKIII, 3: HardSID, 4: ParSID Port 1, 5: ParSID Port 2, 6: ParSID Port 3, 7: USBSID-Pico, 8: ReSIDfp)

`SidResidSampling`
Integer specifying the sampling method (`0`: Fast, `1`: Interpolation, `2`: Resampling, `3`: Fast Resampling)

`SidResidPassband`
Integer specifying the resampling filter passband in percentage of the total bandwidth (`0 - 90`) for 6581.

`SidResidGain`
Integer that specifies reSID gain in percent [97] (90..100) for 6581.

`SidResidFilterBias`
Integer that specifies reSID filter bias for 6581, which can be used to adjust DAC bias in millivolts. [0] (-5000..5000, 1000 equals 1mV)

`SidResid8580Passband`
Integer specifying the resampling filter passband in percentage of the total bandwidth (`0 - 90`) for 8580.

`SidResid8580Gain`
Integer that specifies reSID gain in percent [97] (90..100) for 8580.

`SidResid8580FilterBias`
Integer that specifies reSID filter bias for 8580, which can be used to adjust DAC bias in millivolts. [0] (-5000..5000, 1000 equals 1mV)

`SidResidEnableRawOutput`
Boolean specifying whether raw debug output is enabled. When enabled, the raw - not downsampled - audio data is written to a file called resid.raw in the current working directory.

`SidResid8580Gain`
Integer that specifies reSID gain in percent [97] (90..100) for 8580.

`SidResid6581FilterCurve`
Integer that specifies the reSIDfp filter curve parameter [500] (0..1000) for the 6581.

`SidResid6581FilterRange`
Integer that specifies the reSIDfp filter range parameter [500] (0..1000) for the 6581.

`SidResid8580FilterCurve`
Integer that specifies the reSIDfp filter curve parameter [500] (0..1000) for the 8580.

`SidResidCombinedWaveformStrength`
: Integer that specifies the reSIDfp strength of combined waveforms [1] (0..2).

`SidResid6581OldCaps`
: Boolean specifying whether to use the old 2200pf capacitors for 6581.

`SidUSBSIDReadMode`
: Boolean specifying whether to enable USBSID-Pico read mode. When enabled, this mode allows for reading from SID registers that provides a means to configure an FPGASID that is seated on USBSID-Pico. This mode is not suited for regular audio use as it disables cycle exact writing & digiplay. Default is (`SidUSBSIDReadMode=0`) for off, (`SidUSBSIDReadMode=1`) for on.

`SidUSBSIDAudioMode`
: Boolean to enable USBSID-Pico PCB (v1.3) stereo audio mode. When enabled the audio of SID socket 1 will be output over the left audio channel and the audio of SID socket 2 over the right channel of USBSID-Pico's audio out. Default is (`SidUSBSIDAudioMode=0`) for Mono, (`SidUSBSIDAudioMode=1`) for Stereo.

`SidUSBSIDDiffSize`
: Integer specifying the minimal difference (must be a number divisable by 8) in bytes to use between the head and tail of the drivers ringbuffer, this allows for some audio quality tuning on slower systems. In most cases the default value of 64 bytes (also the size of a USB packet) is the right setting for best audio quality. (`SidUSBSIDDiffSize=32-256`).

`SidUSBSIDBufferSize`
: Integer specifying the size in bytes to use as ringbuffer to store temporary data when sending SID writes to USBSID-Pico (must be a number divisable by 8), this allows for some audio quality tuning on slower systems. In most cases the default value of 8192 bytes is the right setting for best audio quality. (`SidUSBSIDBufferSize=512-16384`).

7.1.5.2 SID command-line options

`-sidextra`
: Specify the amount of extra SID chips to emulate (`SidStereo`). (0: off, 1: 1 extra sid, 2: 2 extra sids, 3: 3 extra sids, 4: 4 extra sids, 5: 5 extra sids, 6: 6 extra sids, 7: 7 extra sids, 8: 8 extra sids, 9: 9 extra sids)

`-sid2address <Base address>`
: Specifies the start address for the second SID chip (`Sid2AddressStart`) (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20,

0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`-sid3address ADDRESS`

Specifies the start address for the third SID chip (`Sid3AddressStart`) (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`-sid4address ADDRESS`

Specifies the start address for the fourth SID chip (`Sid4AddressStart`) (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`-sid5address ADDRESS`

Specifies the start address for the fifth SID chip (`Sid5AddressStart`) (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`-sid6address ADDRESS`

Specifies the start address for the sixth SID chip (`Sid6AddressStart`) (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760,

0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`-sid7address ADDRESS`
: Specifies the start address for the seventh SID chip (`Sid7AddressStart`) (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`-sid8address ADDRESS`
: Specifies the start address for the eight SID chip (`Sid8AddressStart`) (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`-sid9address ADDRESS`
: Specifies the start address for the ninth SID chip (`Sid9AddressStart`) (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20,

0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`-sid810ddress ADDRESS`
: Specifies the start address for the tenth SID chip (`Sid10AddressStart`) (x64, x64sc, xscpu64, x128 and vsid only). (x128: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0) (x64, x64sc, xscpu64, vsid: 0xD420, 0xD440, 0xD460, 0xD480, 0xD4A0, 0xD4C0, 0xD4E0, 0xD500, 0xD520, 0xD540, 0xD560, 0xD580, 0xD5A0, 0xD5C0, 0xD5E0, 0xD600, 0xD620, 0xD640, 0xD660, 0xD680, 0xD6A0, 0xD6C0, 0xD6E0, 0xD700, 0xD720, 0xD740, 0xD760, 0xD780, 0xD7A0, 0xD7C0, 0xD7E0, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`-sidenginemodel <engine and model>`
: Specify engine and model for the emulated SID chip (`SidEngine`, `SidModel`). (FastSID 6581: 0/fast/fastold/fast6581, FastSID 8580: 1/fastnew/fast8580, ReSID 6581: 256/resid/residold/resid6581, ReSID 8580: 257/residnew/resid8580, ReSID 8580 + digiboost: 258/residdigital/residd/residnewd/resid8580d, DTVSID: 260/dtv/c64dtv/dtvsid, Catweasel MKIII: 512/catweaselmkiii/catweasel3/catweasel/cwmkiii/cw3/cw, HardSID: 768/hardsid/hard/hs, ParSID Port 1: 1024/parsid/parsid1/par1/lpt1, ParSID Port 2: 1280/parsid2/par2/lpt2, ParSID Port 3: 1536/parsid3/par3/lpt3, USBSID-Pico: 1792/usbsid/usbs/us, ReSIDfp 6581: 2048/residfp/residfpold/residfp6581, ReSIDfp 8580: 2049/residfpnew/residfp8580, ReSIDfp 8580 + digiboost: 2050/residfpdigital/residfpd/residfpnewd/residfp8580d)

`-sidengine <engine>`
: Specify engine for the emulated SID chip (`SidEngine`). (FastSID: 0, ReSID: 1, DTVSID: 2, HardSID: 3, ParSID Port 1: 4, ParSID Port 2: 5, ParSID Port 3: 6, USBSID-Pico: 7, ReSIDfp: 8)

`-sidmodel <model>`
: Specify model for the emulated SID chip (`SidModel`). (6581: 0, 8580: 1, ReSID 8580 + digiboost: 2, DTVSID: 3)

`-sidfilters`
`+sidfilters`
: Enable/disable emulation of the built-in SID filters (`SidFilters=1`, `SidFilters=0`).

`-residsamp METHOD`
: Specifies the sampling method; fast (`SidResidSampling=0`), interpolating (`SidResidSampling=1`), resampling (`SidResidSampling=2`), fast resampling (`SidResidSampling=3`).

`-residpass PERCENTAGE`
: Specifies the resampling filter passband in percentage of the total bandwidth (`SidResidPassband=0-90`) for 6581.

`-residgain PERCENTAGE`
: Specifies reSID gain in percent (90 - 100) for 6581.

`-residfilterbias <number>`
: reSID filter bias setting for 6581, which can be used to adjust DAC bias in millivolts.

`-residpass PERCENTAGE`
: Specifies the resampling filter passband in percentage of the total bandwidth (`SidResid8580Passband=0-90`) for 8580.

`-residgain PERCENTAGE`
: Specifies reSID gain in percent (90 - 100) for 8580.

`-residfilterbias <number>`
: reSID filter bias setting for 8580, which can be used to adjust DAC bias in millivolts.

`-residrawoutput`
`+residrawoutput`
: Boolean specifying whether raw debug output is enabled. When enabled, the raw - not downsampled - audio data is written to a file called resid.raw in the current working directory.

`-residfp6581filtercurve <number>`
: reSIDfp filter curve setting for 6581 (0..1000).

`-residfp6581filterrange <number>`
: reSIDfp filter range setting for 6581 (0..1000).

`-residfp8580filtercurve <number>`
: reSIDfp filter curve setting for 8580 (0..1000).

`-residcombinedwaveformstrength <number>`
: reSIDfp strength of combined waveforms (0..2).

`-resid6581oldcaps`
`+resid6581oldcaps`
: Enable/disable old 6581 2200pf capacitors. (`SidResid6581OldCaps=1`, `SidResid6581OldCaps=0`).

`-usreadmode <0 or 1>`
: Enable USBSID-Pico read mode. When enabled, this mode allows for reading from SID registers that provides a means to configure an FPGASID that is seated on USBSID-Pico. This mode is not suited for regular audio use as it disables cycle exact writing & digiplay. Default is (`SidUSBSIDReadMode=0`) for off, (`SidUSBSIDReadMode=1`) for on.

`-usaudiomode <0 or 1>`
: Set USBSID-Pico PCB (v1.3) audio mode to Stereo or Mono. When enabled the audio of SID socket 1 will be output over the left audio channel and the audio

of SID socket 2 over the right channel of USBSID-Pico's audio out. Default is (`SidUSBSIDAudioMode=0`) for Mono, (`SidUSBSIDAudioMode=1`) for Stereo.

`-usdiffsize <number divisable by 8>`
Specifies the minimal difference in bytes to use between the head and tail of the drivers ringbuffer, this allows for some audio quality tuning on slower systems. In most cases the default value of 64 bytes (also the size of a USB packet) is the right setting for best audio quality. (`SidUSBSIDDiffSize=32-256`).

`-usbuffsize <number divisable by 8>`
Specifies the size in bytes to use as ringbuffer to store temporary data when sending SID writes to USBSID-Pico, this allows for some audio quality tuning on slower systems. In most cases the default value of 8192 bytes is the right setting for best audio quality. (`SidUSBSIDBufferSize=512-16384`).

7.1.6 C64 I/O extension settings

I/O extensions are (usually) cartridges which do not map into ROM space, but use only the I/O space at address range $DE00 . . . $DEFF and/or $DF00 . . . $DFFF.

Please use these extensions only when needed, as they might cause compatibility problems.

The following I/O extensions are available:

- ACIA (Swiftlink, Turbo232)
- DigiMAX
- DS12C887 RTC
- Ethernet (The Final Ethernet, RR-Net)
- GEO-RAM
- MIDI (Passport, Datel, Maplin, Namesoft, Sequential)
- REU - The "RAM Expansion Module" extension emulates a standard Commodore RAM Expansion Unit; this can be used with GEOS and other programs that are designed to take advantage of it. This currently works only in the C64 emulator.
- SFX Sound Expander
- SFX Sound Sampler

7.1.6.1 C64 I/O extension resources

`DIGIMAX` Boolean specifying whether the DigiMAX cartridge should be emulated or not.

`DIGIMAXbase`
Integer specifying the DigiMAX base address. (0xDD00: useport, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`DS12C887RTC`
Boolean specifying whether the DS12C887 RTC cartridge should be emulated or not.

`DS12C887RTCbase`
Integer specifying the DS12C887 RTC base address. x128: (0xD700, 0xDE00, 0xDF00) x64, x64sc, xscpu64: (0xD500, 0xD600, 0xD700, 0xDE00, 0xDF00)

`DS12C887RTCRunMode`
: Boolean specifying whether the DS12C887 RTC cartridge starts out running or halted. (0: halted, 1: running)

`DS12C887RTCSave`
: Boolean specifying whether the DS12C887 RTC data should be saved when changed or not.

`ETHERNETCART_ACTIVE`
: Boolean that specifies whether the CS8900 ethernet interface emulation is active.

`ETHERNETCARTMode`
: Boolean that specifies whether RR-Net compatible mapping is enabled.

`ETHERNETCARTBase`
: Integer specifying the I/O base address of the emulated ethernet cartridge,

`GEORAM`
: Boolean specifying whether the GEO-RAM cartridge should be emulated or not. (x64, x64sc, x128).

`GEORAMfilename`
: String specifying the filename of the GEORAM image. (x64, x64sc, x128).

`GEORAMImageWrite`
: Boolean, if true write back the GEO-RAM image file automatically, incase the RAM contents changed, when detaching or quitting the emulator. (x64, x64sc, x128).

`GEORAMsize`
: Integer specifying the size of the emulated GEO-RAM in KiB. (512, 1024, 2048, 4096). (x64, x64sc, x128).

`MIDIEnable`
: Boolean specifying whether the MIDI cartridge should be emulated or not (x64, x64sc, xscpu64, x128 only, and only if MIDI support is enabled and available at compile time).

`MIDIMode`
: Integer specifying the type of emulated MIDI interface (x64, x64sc, xscpu64 and x128 only, and only if MIDI support is enabled and available at compile time). (0: Sequential, 1: Passport/Syntech, 2: DATEL/Siel/JMS, 3: Namesoft, 4: Maplin)

`REU`
: Boolean specifying whether the RAM Expansion Module should be emulated or not.

`REUfilename`
: String specifying the filename of the REU image.

`REUImageWrite`
: Boolean, if true write back the REU image file automatically, in case the RAM contents changed, when detaching or quitting the emulator.

`REUsize`
: Integer specifying the size of the emulated REU in KiB. (128, 256, 512, 1024, 2048, 4096, 8192, 16384)

`SFXSoundExpander`
: Boolean specifying whether the SFX Sound Expander should be emulated or not.

`SFXSoundExpanderChip`
: Integer specifying which YM chip is emulated. (3526, 3812)

`SFXSoundSampler`
: Boolean specifying whether the SFX Sound Sampler should be emulated or not.

7.1.6.2 C64 I/O extension command-line options

`-digimax`
`+digimax` Enable/disable the DigiMAX cartridge (`DIGIMAX=1`, `DIGIMAX=0`).

`-digimaxbase <base address>`
: Base address of the DigiMAX cartridge (`DIGIMAXbase`). (0xDD00: userport, 0xDE00, 0xDE20, 0xDE40, 0xDE60, 0xDE80, 0xDEA0, 0xDEC0, 0xDEE0, 0xDF00, 0xDF20, 0xDF40, 0xDF60, 0xDF80, 0xDFA0, 0xDFC0, 0xDFE0)

`-ds12c887rtc`
`+ds12c887rtc`
: Enable/disable the DS12C887 RTC cartridge (`DS12C887RTC=1`, `DS12C887RTC=0`).

`-ds12c887rtcbase <base address>`
: Base address of the DS12C887 RTC cartridge (`DS12C887RTCbase`). x128: (0xD700, 0xDE00, 0xDF00) x64, x64sc, xscpu64: (0xD500, 0xD600, 0xD700, 0xDE00, 0xDF00)

`-ds12c887rtchalted`
: Set the DS12C887 RTC oscillator to 'halted' (`DS12C887RTCRunMode=0`).

`-ds12c887rtcrunning`
: Set the DS12C887 RTC oscillator to 'running' (`DS12C887RTCRunMode=1`).

`-ds12c887rtcsave`
`+ds12c887rtcsave`
: Enable/disable saving of the DS12C887 RTC data when changed (`DS12C887RTCSave=1`, `DS12C887RTCSave=0`).

`-miditype <0-4>`
: Set MIDI interface type (`MIDIMode`) (x64, x64sc, xscpu64 and x128 only, and only if MIDI support is enabled and available at compile time). (0: Sequential, 1: Passport/Syntech, 2: DATEL/Siel/JMS, 3: Namesoft, 4: Maplin)

`-midi`
`+midi` Enable/disable MIDI emulation (`MIDIEnable=1`, `MIDIEnable=0`) (x64, x64sc, xscpu64 and x128 only, and only if MIDI support is enabled and available at compile time). (0: Sequential, 1: Passport/Syntech, 2: DATEL/Siel/JMS, 3: Namesoft, 4: Maplin)

`-georam`
`+georam` Enable/disable the GEORAM expansion unit (`GEORAM=1`, `GEORAM=0`).

`-cartgeoram <name>`
Attach raw GEO-RAM cartridge image.

`-georamimage <name>`
Specify name of GEORAM image (`GEORAMfilename`).

`-georamimagerw`
`+georamimagerw`
Allow/disallow writing to GEORAM image (`GEORAMImageWrite=1`, `GEORAMImageWrite=0`).

`-georamsize <size in KiB>`
Size of the GEORAM expansion unit (`GEORAMsize`). (512, 1024, 2048, 4096)

`-reu`
`+reu` Enable/disable emulation of the RAM Expansion Module (`REU=1`, `REU=0`).

`-cartreu <name>`
Attach raw REU cartridge image.

`-reuimage <name>`
Specify name of REU image (`REUfilename`).

`-reuimagerw`
`+reuimagerw`
Allow/disallow writing to REU image (`REUImageWrite=1`, `REUImageWrite=0`).

`-reusize <size in KiB>`
Size of the RAM expansion unit (`REUsize`). (128, 256, 512, 1024, 2048, 4096, 8192, 16384)

`-sfxse`
`+sfxse` Enable/disable the SFX soundexpander cartridge (`SFXSoundExpander=1`, `SFXSoundExpander=0`).

`-sfxsetype <type>`
Set YM chip type (`SFXSoundExpanderChip`). (3526, 3812)

`-sfxss`
`+sfxss` Enable/disable the SFX Sound Sampler cartridge (`SFXSoundSampler=1`, `SFXSoundSampler=0`).

`-ethernetcart`
`+ethernetcart`
Disable/Enable the Ethernet Cartridge (`ETHERNETCART_ACTIVE`). (TFE/RR-Net/64NIC/FB-NET)

`-ethernetcartmode <Mode>`
Mode of Ethernet Cartridge (`ETHERNETCARTMode`). (0: TFE, 1: RR-Net)

`-ethernetcartbase <Base address>`
Base address of the Ethernet Cartridge. (`ETHERNETCARTBase`) (0xDE00, 0xDE10, 0xDE20, 0xDE30, 0xDE40, 0xDE50, 0xDE60, 0xDE70, 0xDE80, 0xDE90, 0xDEA0, 0xDEB0, 0xDEC0, 0xDED0, 0xDEE0, 0xDEF0, 0xDF00, 0xDF10, 0xDF20, 0xDF30, 0xDF40, 0xDF50, 0xDF60, 0xDF70, 0xDF80, 0xDF90, 0xDFA0, 0xDFB0, 0xDFC0, 0xDFD0, 0xDFE0, 0xDFF0)

`-tfe` Enable the Ethernet Cartridge in TFE ("The Final Ethernet") compatible mode and set default I/O address

`-rrnet` Enable the Ethernet Cartridge in RR-Net compatible mode and set default I/O address

`-burstmod <value>`
Set the kind of burst modification. This emulates the fast serial bus connection as described at `https://a1bert.kapsi.fi/Dev/burst/`, with the wire to the tape port cut (`BurstMod`). (0: None, 1: CIA-1, 2: CIA-2)

7.1.7 C64 system ROM settings

These settings can be used to control what system ROMs are loaded in the C64 emulator at startup. They cannot be changed from the menus.

7.1.7.1 C64 system ROM resources

`BasicName`
String specifying the name of the Basic ROM (default `basic`).

`ChargenName`
String specifying the name of the character generator ROM (default `chargen`).

`KernalName`
String specifying the name of the Kernal ROM (default `kernal-901227-03.bin`).

`KernalRev`
String specifying the Kernal revision. This resource can be used to control what revision of the C64 kernal is being used; Changing this resource equals loading one of the provided kernal ROM files. It will be adjusted when changing `KernalName`.

Valid values are:

`-1` Unknown kernal. This value can not be set by the user, it will be set when loading a custom kernal via changing `KernalName`.

`-2` No kernal. This is used by the MAX Machine.

`0` Kernal revision 0 (Japanese, 906145-02)

`1` Kernal revision 1 (901227-01)

`2` Kernal revision 2 (901227-02)

`3` Kernal revision 3 (901227-03)

`39` C64 GS (390852-01)

`67` SX-64 (251104-04)

`100` Commodore 4064 (also known as “PET64” or “Educator 64”) (901246-01)

7.1.7.2 C64 system ROM command-line options

`-basic <name>`
: Specify filename of the Basic ROM file (`BasicName`).

`-chargen <name>`
: Specify filename of the character generator ROM file (`ChargenName`).

`-kernal <name>`
: Specify filename of the Kernal ROM file (`KernalName`).

`-kernalrev <revision>`
: Specify Kernal revision (`KernalRev`). (0/jap: japanese 1: rev. 1, 2: rev. 2, 3: rev. 3, 39/gs: C64 GS, 67/sx: sx64, 100/4064: 4064)

7.1.8 C64 settings

7.1.8.1 C64 resources

`GlueLogic`
: Integer specifying the type of emulated glue-logic. (0: discrete, 1: custom IC)

`BurstMod` Integer specifying the kind of Burst-Mode modification. (0: None, 1: CIA-1, 2: CIA-2)

`BoardType`
: Integer specifying the type of emulated board (not available in xscpu64). (0: C64, 1: MAX)

`IECReset` Integer specifying if the IEC bus resets when the CPU resets. (0: No, 1: Yes)

`MemoryHack`
: Integer specifying what memory expansion hack is active. (0: None, 1: C64 256K, 2: PLUS60K, 3: PLUS256K)

`PLUS60Kfilename`
: String specifying the filename of the PLUS60K RAM image.

`PLUS60Kbase`
: Integer that specifies the base address of the PLUS60K RAM expansion. (0xD040, 0xD100)

`PLUS256Kfilename`
: String specifying the filename of the PLUS256K RAM image.

`C64_256Kfilename`
: String specifying the filename of the 256KiB RAM image.

`C64_256Kbase`
: Integer that specifies the base address of the 256KiB RAM expansion. (0xDE00/0xDE80/0xDF00/0xDF80)

`MachineVideoStandard`
: Integer that specifies the video standard of the emulated machine (1: PAL, 2: NTSC, 3: Old NTSC, 4: PAL-N).

`MachinePowerFrequency`
: Integer that specifies the frequency of the power grid in Hz (50, 60). This is used to determine the tick frequency for the TOD clocks.

`CPMCart` Boolean that specifies if a CP/M cartridge is attached.

7.1.8.2 C64 command-line options

`-gluelogictype <type>`
: Set glue logic type (`GlueLogic`). (0: discrete, 1: 252535-01)

`-iecreset <value>`
: Set IEC reset bahaviour (`IECReset`). (0: Do not reset with CPU reset, 1: Reset with CPU reset)

`-memoryexphack <device>`
: Set active memory expansion hack (`MemoryHack`). (0: None, 1: C64 256K, 2: PLUS60K, 3: PLUS256K)

`-plus60kimage <name>`
: Specify name of PLUS60K image (`PLUS60Kfilename`).

`-plus60kbase <base address>`
: Base address of the PLUS60K expansion (`PLUS60Kbase`). (0xD040/0xD100)

`-plus256kimage <name>`
: Specify name of PLUS256K image (`PLUS256Kfilename`).

`-256kimage <name>`
: Specify name of 256K image (`C64_256Kfilename`).

`-256kbase <base address>`
: Base address of the 256KiB expansion (`C64_256Kbase`). (0xDE00, 0xDE80, 0xDF00, 0xDF80)

`-pal` Use PAL sync factor (`MachineVideoStandard=1`).

`-ntsc` Use NTSC sync factor (`MachineVideoStandard=2`).

`-ntscold` Use old NTSC sync factor (`MachineVideoStandard=3`).

`-paln` Use PAL-N sync factor (`MachineVideoStandard=4`).

`-power50` Use 50Hz power grid frequency. (`MachinePowerFrequency=50`).

`-power60` Use 60Hz power grid frequency. (`MachinePowerFrequency=60`).

`-model <Model>`
: Set the C64 model (`MachineVideoStandard`, `MachinePowerFrequency`, `VICIIModel`, `CIA1Model`, `CIA2Model`, `GlueLogic`, `BoardType`, `IECReset`, `KernalRev`, `ChargenName`, `SidEngine`, `SidModel`) (x64 and x64sc only). (c64/c64c/c64old, ntsc/newntsc/oldntsc, drean, jap, c64gs, pet64, ultimax)

`-cpmcart`
`+cpmcart` Enable/disable the CP/M cartridge (`CPMCart=1`, `CPMCart=0`)

7.2 C128-specific commands and settings

7.2.1 C128 cartridge settings

x128 reuses the cartridge system from the C64 emulation, so all options listed there apply as well. The following only lists what is different and/or additional.

7.2.1.1 C128 cartridge resources

`CartridgeType`
: Integer specifying the type of the default cartridge.

  Additional to the C64 types, the following types are valid C128 CRT ID:

  - 1: Warpspeed 128
  - 2: Partner 128
  - 3: Comal 80
  - 4: Magic Desk 128
  - 5: Gmod2-C128

  Note: The actual value will be this ID added to the highest C64 CRT ID, so both can coexist.

`GMod128EEPROMImage`
: String that specifies the name of the raw GMod2-C128 EEPROM image.

`GMod128FlashWrite`
: Boolean that specifies wether writes to GMod2-C128 EEPROM image are enabled.

`GMod128EEPROMRW`
: Boolean that specifies wether the GMod2-C128 ROM is saved at exit

7.2.1.2 C128 cartridge command-line options

`-cartfrom <name>`
: Attach generic external function ROM cartridge image.

`-cartcomal128 <name>`
: Attach 96k Comal 80 cartridge image.

`-cartgmod128 <name>`
: Attach 512k Gmod2-C128 cartridge image.

`-cartmd128 <name>`
: Attach 16/32/64/128/256/512k/1M Magic Desk 128 cartridge image.

`-cartpartner128 <name>`
: Attach 16KiB Partner 128 cartridge image.

`-cartws128 <name>`
: Attach 16KiB Warpspeed 128 cartridge image.

`-gmod128eepromimage <filename>`
: Specify Gmod2-C128 EEPROM image filename (`GMod128EEPROMImage`).

`-gmod128eepromrw`
`+gmod128eepromrw`
: Enable/Disable writes to Gmod2-C128 EEPROM image (`GMod128EEPROMRW=1`, `GMod128EEPROMRW=0`).

`-gmod128flashwrite`
`+gmod128flashwrite`
: Enable/Disable saving of the Gmod2-C128 ROM at exit (`GMod128FlashWrite=1`, `GMod128FlashWrite=0`).

7.2.2 VDC settings

7.2.2.1 VDC resources

`VDC64KB` Boolean to enabled/disable full 64KiB video ram.

`VDCRevision`
: Integer specifying the VDC hardware revision (0: Rev 0, 1: Rev 1, 2: Rev 2).

`VDCVideoCache`
: Boolean specifying whether the video cache is turned on.

`VDCDoubleSize`
: Boolean specifying whether double-size mode is turned on.

`VDCDoubleScan`
: Boolean specifying whether double-scan mode is turned on.

`VDCStretchVertical`
: Boolean specifying whether vertical stretching is turned on.

`VDCPaletteFile`
: String specifying the name of the palette file being used. The `.vpl` extension is optional.

`VDCExternalPalette`
: Boolean specifying whether to use external palette file or not.

`VDCColorSaturation`
: Integer specifying saturation of internal calculated palette. (0..2000)

`VDCColorContrast`
: Integer specifying contrast of internal calculated palette. (0..2000)

`VDCColorBrightness`
: Integer specifying brightness of internal calculated palette. (0..2000)

`VDCColorGamma`
: Integer specifying gamma of internal calculated palette. (0..4000)

`VDCColorTint`
: Integer specifying tint of internal calculated palette. (0..2000)

`VDCPALScanLineShade`
: Integer specifying amount of scan line shading for the CRT emulation. (0..1000)

`VDCPALBlur`
: Integer specifying amount of horizontal blur for the CRT emulation. (0..1000)

`VDCAudioLeak`
: Boolean specifying whether to enable/disable video to audio leak emulation.

`VDCFilter`
: Integer specifying rendering filter (0: None, 1: CRT emulation, 2: Scale2x)

`VDCVSync`
: Boolean specifying whether to enable vsync to prevent screen tearing. (0: vsync off, 1: vsync on)

`TEDShowStatusbar`
: Boolean specifying whether to show the status bar or not (x128 only).

`VDCFullscreen`
: Boolean specifying whether to use fullscreen mode or not (x128 only).

The following resources are only present if hardware scaling support (OpenGL or Direct-X) is present and used at compile time.

`VDCAspectRatio`
: String/float specifying the aspect ratio. (0.5-2.0)

`VDCAspectMode`
: Integer specifying the OpenGL aspect mode. (0: off, 1: custom, 2: true)

`VDCGLFilter`
: Integer specifying the OpenGL filtering mode. (0: nearest neighbour, 1: bilinear, 2: bicubic)

`VDCFlipX`
: Boolean to enable/disable OpenGL horizontal screen flip.

`VDCFlipY`
: Boolean to enable/disable OpenGL vertical screen flip.

`VDCRotate`
: Boolean to enable/disable OpenGL 90 degree clockwise rotation.

7.2.2.2 VDC command-line options

`-VDCvcache`
`+VDCvcache`
: Enable/disable the video cache (`VDCVideoCache=1`, `VDCVideoCache=0`).

`-VDCdsize`
`+VDCdsize`
: Enable/disable double size (`VDCDoubleSize=1`, `VDCDoubleSize=0`).

`-VDCstretchvertical`
`+VDCstretchvertical`
: Enable/Disable vertical stretching (`VDCStretchVertical=1`, `VDCStretchVertical=0`).

`-VDCdscan`
`+VDCdscan`
: Enable/disable double scan (`VDCDoubleScan=1`, `VDCDoubleScan=0`).

`-VDCintpal`
: Use an internal calculated palette (`VDCExternalPalette=0`).

`-VDCextpal`
: Use an external palette (file) (`VDCExternalPalette=1`).

`-VDCpalette <name>`
: Specify name of file of external palette (`VDCPaletteFile`).

`-VDC16KB` Set the VDC memory size to 16KiB (`VDC64KB=0`).

`-VDC64KB` Set the VDC memory size to 64KiB (`VDC64KB=1`).

`-VDCRevision <number>`
: Set VDC revision (`VDCRevision`). (0..2)

`-VDCsaturation <0-2000>`
: Set saturation of internal calculated palette (`VDCColorSaturation`).

`-VDCcontrast <0-2000>`
: Set contrast of internal calculated palette (`VDCColorContrast`).

`-VDCbrightness <0-2000>`
: Set brightness of internal calculated palette (`VDCColorBrightness`).

`-VDCgamma <0-4000>`
: Set gamma of internal calculated palette (`VDCColorGamma`).

`-VDCtint <0-2000>`
: Set tint of internal calculated palette (`VDCColorTint`).

`-VDCcrtblur <0-1000>`
: Amount of horizontal blur for the CRT emulation (`VDCPALBlur`).

`-VDCcrtscanlineshade <0-1000>`
: Amount of scan line shading for the CRT emulation (`VDCPALScanLineShade`).

`-VDCaudioleak`
`+VDCaudioleak`
: Enable/disable video to audio leak emulation (`VDCAudioLeak=1`, `VDCAudioLeak=0`).

`-VDCfilter <Mode>`
: Select rendering filter (`VDCFilter`). (0: None, 1: CRT emulation, 2: Scale2x)

`-VDCshowstatusbar`
`+VDCshowstatusbar`
: Show/hide status bar (`VDCShowStatusbar=1`, `VDCShowStatusbar=0`).

`-VDCvsync`
: Enable vsync to prevent screen tearing (`VDCVSync=1`).

`+VDCvsync`
: Disable vsync to allow screen tearing (`VDCVSync=0`).

`-VDCfull`
`+VDCfull` Enable/Disable fullscreen (`VDCFullscreen=1`, `VDCFullscreen=0`) (x128 only).

The following command-line options are only present if hardware scaling support (OpenGL or Direct-X) is present and used at compile time.

`-VDCaspectmode <mode>`
: Set the aspect mode (`VDCAspectMode`). (0: off, 1: custom, 2: true)

`-VDCaspect <aspect ratio>`
: Set the aspect ratio (`VDCAspectRatio`). (0.5-2.0)

`-VDCglfilter <mode>`
: Set OpenGL (or Direct-X) filtering mode (0 = nearest, 1 = linear, 2 = bicubic) (`VDCglfilter`).

`-VDCflipx`
`+VDCflipx`
: Enable/disable OpenGL horizontal screen flip (`VDCFlipX=1`, `VDCFlipX=0`).

`-VDCflipy`
`+VDCflipy`
: Enable/disable OpenGL vertical screen flip (`VDCFlipY=1`, `VDCFlipY=0`).

`-VDCrotate`
`+VDCrotate`
: Enable/disable OpenGL 90 degree rotation (`VDCRotate=1`, `VDCRotate=0`).

7.2.3 C128 system ROM settings

7.2.3.1 C128 system ROM resources

`ChargenIntName`
: String specifying the filename of the international character generator ROM image.

`ChargenDEName`
: String specifying the filename of the German character generator ROM image.

`ChargenFIName`
: String specifying the filename of the Finnish character generator ROM image.

`ChargenFRName`
: String specifying the filename of the French character generator ROM image.

`ChargenITName`
: String specifying the filename of the Italian character generator ROM image.

`ChargenNOName`
: String specifying the filename of the Norwegian character generator ROM image.

`ChargenSEName`
: String specifying the filename of the Swedish character generator ROM image.

`ChargenCHName`
: String specifying the filename of the Swiss character generator ROM image.

`KernalIntName`
: String specifying the filename of the international Kernal ROM image.

`KernalDEName`
: String specifying the filename of the German Kernal ROM image.

`KernalFIName`
: String specifying the filename of the Finnish Kernal ROM image.

`KernalFRName`
: String specifying the filename of the French Kernal ROM image.

`KernalITName`
: String specifying the filename of the Italian Kernal ROM image.

`KernalNOName`
: String specifying the filename of the Norwegian Kernal ROM image.

`KernalSEName`
: String specifying the filename of the Swedish Kernal ROM image.

`KernalCHName`
: String specifying the filename of the Swiss Kernal ROM image.

`BasicLoName`
`BasicHiName`
: Strings specifying the filename of the Basic ROM images.

`Kernal64Name`
: String specifying the filename of the C64 Kernal ROM image.

`Basic64Name`
: String specifying the filename of the C64 Basic ROM image.

`InternalFunctionROM`
: Integer specifying the internal function ROM type (0: None, 1: ROM, 2: RAM, 3: RTC).

`InternalFunctionName`
: String specifying the filename of the ROM image for the internal function ROM.

`InternalFunctionROMRTCSave`
: Boolean to enable/disable the saving of the Internal Function RTC data when changed.

7.2.3.2 C128 system ROM command-line options

`-basic64 <name>`
: Specify name of C64 mode BASIC ROM image (`Basic64Name`).

`-kernal64 <name>`
: Specify name of C64 mode Kernal ROM image (`Kernal64Name`).

`-basiclo <name>`
: Specify name of BASIC ROM image (lower part) (`BasicLoName`).

`-basichi <name>`
: Specify name of BASIC ROM image (higher part) (`BasicHiName`).

`-kernal <name>`
: Specify name of international Kernal ROM image (`KernalIntName`).

`-kernalde <name>`
: Specify name of German Kernal ROM image (`KernalDEName`).

`-kernalfi <name>`
: Specify name of Finnish Kernal ROM image (`KernalFIName`).

`-kernalfr <name>`
: Specify name of French Kernal ROM image (`KernalFRName`).

`-kernalit <name>`
: Specify name of Italian Kernal ROM image (`KernalITName`).

`-kernalno <name>`
: Specify name of Norwegian Kernal ROM image (`KernalNOName`).

`-kernalse <name>`
: Specify name of Swedish Kernal ROM image (`KernalSEName`).

`-kernalch <name>`
: Specify name of Swiss Kernal ROM image (`KernalCHName`).

`-chargen <name>`
: Specify name of international character generator ROM image (`ChargenIntName`).

`-chargde <name>`
: Specify name of German character generator ROM image (`ChargenDEName`).

`-chargfi <name>`
: Specify name of Finnish character generator ROM image (`ChargenFRName`).

`-chargfr <name>`
: Specify name of French character generator ROM image (`ChargenFRName`).

`-chargit <name>`
: Specify name of Italian character generator ROM image (`ChargenFRName`).

`-chargno <name>`
: Specify name of Norwegian character generator ROM image (`ChargenNOName`).

`-chargse <name>`
: Specify name of Swedish character generator ROM image (`ChargenSEName`).

`-chargch <name>`
: Specify name of Swiss character generator ROM image (`ChargenCHName`).

`-intfunc <type>`
: Set the internal Function ROM type (`InternalFunctionROM`). (0: None, 1: ROM, 2: RAM, 3: RTC)

`-intfrom <name>`
: Specify name of internal Function ROM image (`InternalFunctionName`).

`-intfuncrtcsave`
`+intfuncrtcsave`
: Enable/disable the saving of the Internal Function RTC data when changed (`InternalFunctionROMRTCSave=1`, `InternalFunctionROMRTCSave=0`).

7.2.4 C128 settings

7.2.4.1 C128 resources

`C128ColumnKey`
: Boolean specifying the status of the 40/80 columns key. (0: Not pressed, 1: Pressed)

`Go64Mode` Boolean, if true enter C64 mode on reset.

`C128FullBanks`
: Boolean to enable/disable RAM banks 2 and 3.

`MachineType`
: Integer specifying the C128 machine type. (0: International, 1: Finnish, 2: French, 3: German, 4: Italian, 5: Norwegian, 6: Swedish)

`MachineVideoStandard`
: Integer that specifies the video standard of the emulated machine (1: PAL, 2: NTSC).

`MachinePowerFrequency`
: Integer that specifies the frequency of the power grid in Hz (50, 60). This is used to determine the tick frequency for the TOD clocks.

`C128HideVDC`
: Boolean to enable/disable the VDC display window.

`DualWindow`
: Boolean to turn on dual-window rendering, opening two windows, one for each display.

7.2.4.2 C128 command-line options

`-40col` Activate 40 column mode (`C128ColumnKey=1`).

`-80col` Activate 80 column mode (`C128ColumnKey=0`).

`-go64` Always switch to C64 mode on reset (`Go64Mode=1`.

`+go64` Always switch to C128 mode on reset (`Go64Mode=0`.

`-pal` Use PAL sync factor (`MachineVideoStandard=1`).

`-ntsc` Use NTSC sync factor (`MachineVideoStandard=2`).

`-power50` Use 50Hz power grid frequency. (`MachinePowerFrequency=50`).

`-power60` Use 60Hz power grid frequency. (`MachinePowerFrequency=60`).

`-model <Model>`
: Set the C128 model (`MachineVideoStandard`, `MachinePowerFrequency`, `CIA1Model`, `CIA2Model`, `VICIINewLuminances`, `VDCRevision`, `VDC64KB`, `SidEngine`, `SidModel`). (c128/c128dcr, pal/ntsc)

`-c128fullbanks`
`+c128fullbanks`
: Enable/disable RAM banks 2 and 3 (`C128FullBanks=1`, `C128FullBanks=0`).

`-machinetype <Type>`
: Set the C128 machine type (`MachineType`). (0: International, 1: Finnish, 2: French, 3: German, 4: Italian, 5: Norwegian, 6: Swedish)

`-hidevdcwindow`
`+hidevdcwindow`
: Hide/show the VDC display window (`C128HideVDC=1`, `C128HideVDC=0`).

`-dualwindow`
: Turn on dual-window rendering, opening two windows, one for each display. (`DualWindow`).

7.3 C64DTV-specific commands and settings

This section lists the settings and commands that are C64DTV specific and thus are not present in the other emulators.

7.3.1 C64DTV ROM image

The DTV has a 2MiB Flash chip which contains the kernal, basic and character set ROMs along with other data, such as games in the case of the original C64DTV ROM.

The image file is a dump of the flash chip. It is exactly 2MiB (2097152 bytes).

If you do not have a suitable image file, an image using the C64 kernal, basic and charset is automatically created.

If writing to the C64DTV ROM is enabled, the image file is rewritten with the current data when exiting x64dtv.

Note that x64dtv tries to load the image file from the C64DTV directory first, and if it isn't found there, x64dtv tries to load it from the current directory. If you do not have `dtvrom.bin` in your C64DTV directory and writing to DTV ROM is enabled, the `dtvrom.bin` file is created to the current directory.

NOTE: The original C64DTV ROM has somewhat distorted colors, normally you should use a patched rom.

`-ntscv2romimage <name>`
: Specify filename of the C64DTV NTSC v2 ROM image (`DTVNTSCV2FlashName`).

`-palv2romimage <name>`
: Specify filename of the C64DTV PAL v2 ROM image (`DTVPALV2FlashName`).

`-ntscv3romimage <name>`
: Specify filename of the C64DTV NTSC v3 ROM image (`DTVNTSCV3FlashName`).

`-palv3romimage <name>`
: Specify filename of the C64DTV PAL v3 ROM image (`DTVPALV3FlashName`).

`-hummerntscromimage <name>`
: Specify filename of the C64DTV NTSC Hummer ROM image (`DTVNTSCHummerFlashName`).

`-hummerpalromimage <name>`
: Specify filename of the C64DTV PAL Hummer ROM image (`DTVPALHummerFlashName`).

`-c64dtvromrw`
`+c64dtvromrw`
: Enable/disable writing to C64DTV ROM image (`c64dtvromrw=1`, `c64dtvromrw=0`).

The trueflashfs option is analogous to True drive emulation. If disabled, any file access to the flash filesystem (device 1) will go to the local file system instead.

`-trueflashfs`
`+trueflashfs`
: Enable/disable true hardware flash file system (`FlashTrueFS=1`, `FlashTrueFS=0`).

`-fsflash <name>`
: Specify the directory for the flash file system device (`FSFlashDir`).

7.3.2 DTV revision

The DTV revision 2 has a bug in the Blitter. Using revision 3 is recommended. Emulation of DTV revision 2 including Blitter bug is intended for testing DTV software.

`-dtvrev <revision>`
: Specify DTV revision (`DtvRevision`). (2: DTV2, 3: DTV3)

7.3.3 LumaFix

The PAL C64DTVs have wrong resistors in the video output circuit, which causes incorrect luminances. Several hardware solutions ("LumaFixes") have been developed to fix this flaw.

The fixed video output is emulated by selecting "New Luminances". The unmodified C64DTV video output can be emulated with "Old Luminances".

The default setting is "New Luminances".

7.3.4 Userport

The C64DTV userport emulation currently supports three devices: Hummer ADC, userport joystick and PS/2 mouse.

The Hummer ADC will be always enabled when a "Hummer" model is selected.

While using the Hummer ADC, joystick UP and DOWN are mapped to the Hummer buttons A and B respectively. LEFT and RIGHT set the ADCs output to 0 and 255. Centering the joystick results in the ADC value of 128.

The Hummer ADC and userport joystick are mutually exclusive. This means that enabling one disables the other. PS/2 mouse emulation can be used simultaneously with either Hummer ADC or userport joystick.

`-hummeradc`
`+hummeradc`
: Enable/Disable Hummer ADC (`HummerADC=1`, `HummerADC=0`).

7.3.5 Debug

Debugging information on Blitter, DMA and Flash can be enabled with command line parameters. This can be useful for DTV software development.

`-dtvblitterlog`
`+dtvblitterlog`
: Enable or disable DTV Blitter log

`-dtvdmalog`
`+dtvdmalog`
: Enable or disable DTV DMA log

`-dtvflashlog`
`+dtvflashlog`
: Enable or disable DTV Flash log

7.3.6 Monitor DTV features

Currently the registers A, Y and X are registers R0, R1 and R2 regardless of the mapping, which can be seen and modified via the registers ACM and XYM.

The monitor can access all 2MiB of RAM and 2MiB of Flash, but only 64 KiB at a time. The 64KiB bank can be selected with "bank ram00"."ram1f" for RAM and "bank rom00"."rom1f" for Flash.

The "load" command can load large files (>64KiB) correctly if the bank is set to "ramXX", where XX is the starting bank (usually "bank00").

7.3.7 DTV resources

`DtvRevision`
: Integer specifying the emulated DTV revision. (2: DTV2, 3: DTV3)

`ChargenName`
: String specifying the name of the character generator ROM (default `chargen`).

`KernalName`
: String specifying the name of the Kernal ROM (default `kernal`).

`BasicName`
: String specifying the name of the Basic ROM (default `basic`).

`DTVNTSCV2FlashName`
: String specifying the filename of the DTV NTSC v2 Flash ROM image.

`DTVPALV2FlashName`
: String specifying the filename of the DTV PAL v2 Flash ROM image.

`DTVNTSCV3FlashName`
: String specifying the filename of the DTV NTSC v3 Flash ROM image.

`DTVPALV3FlashName`
: String specifying the filename of the DTV PAL v3 Flash ROM image.

`DTVNTSCHummerFlashName`
: String specifying the filename of the DTV NTSC Hummer Flash ROM image.

`DTVPALHummerFlashName`
: String specifying the filename of the DTV PAL Hummer Flash ROM image.

`DTVFlashRevision`
: Integer specifying which of the above Flash ROM images will be used.

`c64dtvromrw`
: Boolean that specifies whether the emulated Flash ROM is writeable.

`FSFlashDir`
: String specifying the working directory for the flash file system.

`FlashTrueFS`
: Boolean, enables true hardware flash file system.

`HummerADC`
: Boolean to enable/disable the Hummer ADC emulation.

`DtvBlitterLog`
: Boolean, enables Blitter logging.

`DtvDMALog`
: Boolean, enables DMA logging.

`DtvFlashLog`
: Boolean, enables Flash ROM logging.

`MachineVideoStandard`
: Integer that specifies the video standard of the emulated machine. (1: PAL, 2: NTSC)

7.3.8 DTV command-line options

`-model <Model>`
: Set the DTV model (`MachineVideoStandard`, `DtvRevision`, `HummerADC`). (v2/v2pal/v2ntsc, v3/v3pal/v3ntsc, hummer)

`-pal`
: Use PAL sync factor (`MachineVideoStandard=1`).

`-ntsc`
: Use NTSC sync factor (`MachineVideoStandard=2`).

`-chargen <name>`
: Specify filename of the character generator ROM file (`ChargenName`).

`-kernal <name>`
: Specify filename of the Kernal ROM file (`KernalName`).

`-basic <name>`
: Specify the filename of the Basic ROM file (`BasicName`).

7.4 SCPU64-specific commands and settings

7.4.1 SCPU64 resources

`ChargenName`
: String specifying the name of the character generator ROM (default `chargen`).

`SCPU64Name`
: String specifying the name of the SCPU64 ROM (default `scpu64`).

`MachineVideoStandard`
: Integer that specifies the video standard of the emulated machine (4: PAL-N, 3: Old NTSC, 1: PAL, 2: NTSC).

`MachinePowerFrequency`
: Integer that specifies the frequency of the power grid in Hz (50, 60). This is used to determine the tick frequency for the TOD clocks.

`IECReset` Integer specifying if the IEC bus resets when the CPU resets. (0: No, 1: Yes)

`BurstMod` Integer specifying the kind of Burst-Mode modification. (0: None, 1: CIA-1, 2: CIA-2)

`SIMMSize` Integer specifying the size of the SIMM RAM. (0, 1, 4, 8, 16)

`JiffySwitch`
: Boolean to enable/disable the jiffy switch.

`SpeedSwitch`
: Boolean to enable/disable the speed switch.

`GlueLogic`
: Integer specifying the type of emulated glue-logic. (0: discrete, 1: custom IC)

7.4.2 SCPU64 command-line options

`-chargen <name>`
: Specify filename of the character generator ROM file (`ChargenName`).

`-scpu64 <Name>`
: Specify filename of the SCPU64 ROM file (`SCPU64Name`).

`-model <Model>`
: Set the C64 model (`MachineVideoStandard`, `MachinePowerFrequency`, `CIA1Model`, `CIA2Model`, `VICIINewLuminances`, `IECReset`, `ChargenName`, `SidEngine`, `SidModel`). (c64/c64c/c64old, ntsc/newntsc/oldntsc, drean, jap, c64gs)

`-pal` Use PAL sync factor (`MachineVideoStandard=1`).

`-ntsc` Use NTSC sync factor (`MachineVideoStandard=2`).

`-ntscold` Use old NTSC sync factor (`MachineVideoStandard=3`).

`-paln` Use PAL-N sync factor (`MachineVideoStandard=4`).

`-power50` Use 50Hz power grid frequency. (`MachinePowerFrequency=50`).

`-power60` Use 60Hz power grid frequency. (`MachinePowerFrequency=60`).

`-iecreset <value>`
Set IEC reset bahaviour (`IECReset`). (0: Do not reset with CPU reset, 1: Reset with CPU reset)

`-burstmod <value>`
Set the kind of burst modification. This emulates the fast serial bus connection as described at `https://a1bert.kapsi.fi/Dev/burst/`, with the wire to the tape port cut (`BurstMod`). (0: None, 1: CIA-1, 2: CIA-2)

`-simmsize <number>`
Set the size of the SIMM RAM (`SIMMSize`). (0, 1, 4, 8, 16)

`-jiffyswitch`
`+jiffyswitch`
Enable/disable the jiffy switch (`JiffySwitch=1`, `JiffySwitch=0`).

`-speedswitch`
`+speedswitch`
Enable/disable the speed switch (`SpeedSwitch=1`, `SpeedSwitch=0`).

`-gluelogictype <type>`
Set glue logic type (`GlueLogic`). (0: discrete, 1: 252535-01)

7.5 VIC20-specific commands and settings

This section lists the settings and commands that are VIC20-specific and thus are not present in the other emulators.

7.5.1 Using prg files

It's not alway easy to get VIC-20 games to work due to its different configurations. To run VIC-20 games, you can try the following:

- Try to autorun with different memory sets: `"no expansion"`, `"3KiB"` and `"Full"`
- Load the file in a hex editor and have a look at the first two bytes. If they are e.g. `00 A0` the file should probably be attached as a cartridge image at `$A000`.
- Some programs, (eg the loadable Scott Adams text adventures) have to be loaded into the expanded memory blocks, and then run with the expansion disabled via software, like this:
  - Enable the full memory expansion
  - Disable the expanded memory

    ```
    POKE44,16:POKE56,30:POKE4096,0:new
    POKE648,30
    ```
  - Press `RUN/STOP + RESTORE` (which is `ESC + F12` on the default keymap)
  - Load the program

    ```
    LOAD"4.Voodoo Castle",8,1
    ```
  - Start the program (other programs may have a different start address)

    ```
    SYS32592
    ```

7.5.2 Using cartridges

Generally a cartridge can be enabled by attaching its respective cartridge image, or using the respective menu option for cartridges that do not require an image.

Cartridge images are like disk images, but contain the contents of cartridge ROM and/or RAM images instead of disk images. Two kinds of cartridge images are supported:

- `.crt` images, as originally used by the CCS64 emulator by Per Håkan Sundell
- raw `.bin` images, without load address
- raw `.prg` images, with load address

Use cartconv to convert .bin or .prg images to .crt format.

To attach cartridges, use the "Attach a cartridge image" submenu. When using `.crt` images, this will work for every cartridge which is supported. For raw `.bin` or `.prg` images you will have to select the correct type of cartridge first, or use command line options.

When you have successfully attached a cartridge image, you should then reset the machine to make sure the cartridge initializes itself. (Or enable the "reset on cartridge change" option).

Of course, it is also possible to detach a currently attached cartridge image ("Detach cartridge image").

The cartridge system is organized in "Slots" to allow more than one cartridge connected at a time, like it can be done using an expansion port expander on a real VIC20 (see below).

The imaginary expansion port expander is organized in 4 slots, the cartridges are associated with them like this:

7.5.2.1 Slot 0

All carts that have a passthrough connector go here. Once a "Slot 0" cartridge is enabled all further cartridges are connected to its respective passthrough port.

Only one cartridge of this type can be active at a time.

"Slot 0" carts have individual "enable" switches, enabling means enabling permanently.

The following cartridges are emulated in this slot:

- Minimon

7.5.2.2 Slot 1

Mostly RAM based cartridges which for one reason or the other might make sense to be enabled together with one of the "Main Slot" cartridges go here.

Only one cartridge of this type can be active at a time.

"Slot 1" carts have individual "enable" switches, enabling means enabling permanently

Currently no cartridges use this slot

7.5.2.3 Main Slot

All other cartridges which are not pure i/o extensions go here.

Only one cartridge of this type can be active at a time.

Cartridges in the "Main Slot" must be explicitly set as default to enable them permanently.

The following cartridges are emulated in this slot:

- generic 4KiB, 8KiB and 16KiB cartridges located at $2000, $4000, $6000, $a000, $b000 (see below)
- Behrbonz
- Final Expansion
- Megacart
- Mikro Assembler
- Rabbit tape turbo
- Super Expander
- Ultimem
- VIC Flash Plugin
- Write Now

7.5.2.4 I/O Slot

All carts that are pure I/O extensions go here.

Any number of "I/O Slot" Carts may be active at a time.

"I/O Slot" carts have individual "enable" switches, enabling means enabling permanently.

The following cartridges are emulated in this slot:

- ACIA (Swiftlink, Turbo232)
- MIDI (Passport/Syntech, Datel/Siel/JMS/C-Lab, Maplin, Namesoft, Sequential)
- RAM Expansion Module (REU)
- SID Cartridge
- VIC1112 IEEE-488 Interface

On top of that, xvic emulates the "Masquerade" IO expander, which make it possible to use (some) C64 cartridges at the VIC20:

- ACIA (Swiftlink, Turbo232)
- DigiMAX
- DS12C887 RTC
- Ethernet (The Final Ethernet, RR-Net)
- GEO-RAM
- SFX Sound Expander
- SFX Sound Sampler

7.5.2.5 Expected behaviour

When the emulator is run without arguments, all settings from the config file should be applied and arguments override settings from the config file.

When saving the settings to the config file it is expected that on the next run of the emulator all settings will be in the same state as they were when saved.

There is an exception to this rule: the cartridge in the "Main Slot" must be explicitly set as default before it gets saved to the config file.

`+cart` should disable ALL cartridges, including eventually activated REU, Swiftlink and all similar expansionport devices.

`-cartXYZ` options should generally attach AND activate a cart of type XYZ. As a consequence, attaching carts this way which are NOT in the "Main Slot" will also enable the cart permanently.

7.5.2.6 Common problems

If attaching a cartridge does not work as expected, this may be because of various reasons:

- Not seldomly VIC20 related files with `.crt` extension found "in the wild" are plain binaries, and not in actual `.crt` format - if in doubt use `cartconv` to verify and/or fix).
- You may have unintentionally enabled more than one cartridge at once, for example by saving the settings with GEORAM enabled, and then later attaching a game cartridge from the command-line. The cartridge system will allow certain combinations, but (as on the real thing) not all do (can) actually work. To make sure this is not the case, either detach all cartridges from the menus, or use `+cart` on the command-line.
- The cartridge image might be broken. Try one from a different source. If you are sure the dump is ok (for example because you dumped it yourself) then make sure it is in proper linear order (on some cartridges, for example "capture", address and/or data lines at the eprom are shuffled around so a dump made with an eprom burner can not be used as is).
- Last not least you might have encountered a bug in the emulation. If you suspect this is the case, and you can still reproduce the bug after checking the things above, please file a bug report including the following information:
  - attach your vicerc and a reference to the cartridge binaries
  - if you can, comment in the respective DEBUGXYZ macros prominently defined at the top of these files: src/vic20/cart/vic20cart.c src/vic20/cart/vic20cartmem.c src/vic20/vic20io.c src/vic20/vic20export.c and then recompile. this will add debug output that might make it much easier to locate certain problems.

7.5.2.7 Using cartridge images

As with the C64 (see Section 7.1.1 [C64 cartridges], page 121), it is possible to attach several types of generic cartridge images:

- 4 or 8 KiB cartridges located at $2000;
- 4 or 8 KiB cartridges located at $4000;
- 4 or 8 KiB cartridges located at $6000;
- 4 or 8 KiB cartridges located at $A000;
- 4 KiB cartridges located at $B000.

This can all be done via the “Attach cartridge image. . .” command in the menu. It is also possible to let xvic “guess” the type of cartridge using “Smart-attach cartridge image. . .”.

Notice that several cartridges are actually made up of two pieces (and two files), that need to be loaded separately at different addresses. In that case, you have to know the addresses (which are usually specified in the file name) and use the “attach” command twice.

Also notice that surprisingly often it is required for the user to start VIC20 cartridges manually by typing a BASIC "SYS" command.

A special kind of cartridge file is where the two files mentioned above are concatenated (with removing the two byte load address of the second image) into one 16KiB image. Vice can attach such concatenated files at the start address $2000, $4000, and $6000. The second half of such an image is moved to the memory block following the first block. It is also possible to concatenate four 8KiB files, the resulting file will load into all 4 cartridge blocks.

If you encounter 16KiB images that have the second half at eg $A000 you can split the image into two halfs (i.e. one 8194 byte and one 8192 byte, because the first has the load address) and attach both files separately. Alternatively you can create a 32KiB file with padding blocks in between.

One cartridge that is currently only partially supported here is the VIC1112 IEEE488 interface. You have to load the ROM as a cartridge, but you also have to enable the IEEE488 hardware by menu.

7.5.3 VIC20 cartridge settings

7.5.3.1 VIC20 cartridge resources

`CartridgeReset`
: Boolean specifying whether the machine should be reset when a cartridge is changed.

`CartridgeType`
: Integer specifying the type of the default cartridge.

  The following cartridge types are valid:

  - - 1: None
  - - 2: Generic
  - 1: Megacart
  - 2: Behrbonz
  - 3: Vic Flash Plugin
  - 4: Ultimem
  - 5: Final Expansion
  - 6: Rabbit tape turbo
  - 7: Super Expander
  - 8: Mikro Assembler
  - 9: Write Now
  - 10: Minimon
  - - 10: SID Cartridge
  - - 11: IEEE-4888
  - - 12: IO ram cart (IO2)
  - - 13: IO ram cart (IO3)
  - 0x8000: Auto Detect

- 0x8002: 4KiB at $2000-$2FFF
- 0x8003: 8KiB at $2000-$3FFF
- 0x8004: 4KiB at $6000-$6FFF
- 0x8005: 8KiB at $6000-$7FFF
- 0x8006: 4KiB at $A000-$AFFF
- 0x8007: 8KiB at $A000-$BFFF
- 0x8008: 4KiB at $B000-$BFFF
- 0x8009: 8KiB at $4000-$5FFF
- 0x800A: 4KiB at $4000-$4FFF
- 0x8013: 16KiB at $2000-$5FFF
- 0x8015: 16KiB at $6000-$9FFF
- 0x8019: 16KiB at $4000-$7FFF

`CartridgeFile`
: String specifying the filename of the image for the current cartridge.

`IOCollisionHandling`
: Integer specifying the way the I/O collisions should be handled. (0: error message and detach all involved carts, 1: error message and detach last attached involved carts, 2: warning in log and 'AND' the valid return values)

`GenericCartridgeFile2000`
`GenericCartridgeFile4000`
`GenericCartridgeFile6000`
`GenericCartridgeFileA000`
`GenericCartridgeFileB000`
: Strings specifying the name of the respective cartridge ROM images.

`FinalExpansionWriteBack`
: Boolean, if true write back the Flash ROM image file automatically, incase the contents changed, when detaching or quitting the emulator.

`VicFlashPluginWriteBack`
: Boolean, if true write back the Flash ROM image file automatically, incase the contents changed, when detaching or quitting the emulator.

`MegaCartNvRAMfilename`
: String specifying the filename of the MegaCart NvRAM image.

`MegaCartNvRAMWriteBack`
: Boolean, if true write back the NvRAM image file automatically, incase the RAM contents changed, when detaching or quitting the emulator.

`UltiMemWriteBack`
: Boolean, if true write back the Flash ROM image file automatically, incase the contents changed, when detaching or quitting the emulator.

`IO2RAM` Boolean specifying whether the I/O-2 ($9800-$9BFF) RAM cartridge should be emulated or not.

`IO3RAM` Boolean specifying whether the I/O-3 ($9C00-$9FFF) RAM cartridge should be emulated or not.

`VFLImod` Boolean specifying whether the VFLI modification should be enabled.

`SFXSoundExpander`
Boolean specifying whether the SFX Sound Expander should be emulated or not.

`SFXSoundExpanderChip`
Integer specifying which YM chip is emulated. (3526, 3812)

`SFXSoundExpanderIOSwap`
Boolean, swap io mapping (map cart I/O to VIC20 I/O-2) or don't swap io mapping (map cart I/O to VIC20 I/O-3).

`SFXSoundSampler`
Boolean specifying whether the SFX Sound Sampler should be emulated or not.

`SFXSoundSamplerIOSwap`
Boolean, swap io mapping (map cart I/O to VIC20 I/O-2) or don't swap io mapping (map cart I/O to VIC20 I/O-3).

`GEORAMfilename`
String specifying the filename of the GEORAM image.

`GEORAM` Boolean specifying whether the GEO-RAM cartridge (using the MasC=uerade cartridge adapter) should be emulated or not.

`GEORAMsize`
Integer specifying the size of the emulated GEO-RAM in KiB. (512, 1024, 2048, 4096).

`GEORAMImageWrite`
Boolean, if true write back the GEO-RAM image file automatically, incase the RAM contents changed, when detaching or quitting the emulator.

`GEORAMIOSwap`
Boolean specifying whether the io mapping should be swapped (map cart I/O-1 to VIC20 I/O-3 and cart I/O-2 to VIC20 I/O-2) or not (map cart I/O-2 to VIC20 I/O-2 and cart I/O-2 to VIC20 I/O-3).

`SidCart` Boolean specifying whether SID-Cart emulation is enabled or not.

`SidAddress`
Integer that specifies the base address of the emulated SID chip. (0x9800, 0x9C00)

`SidClock` Integer specifying the clock rate used for the emulated SID chip (0: C64, 1: VIC20)

`DIGIMAX` Boolean specifying whether the DigiMAX cartridge (using the MasC=uerade cartridge adapter) should be emulated or not.

`DIGIMAXbase`
Integer specifying the DigiMAX base address. (0x9800, 0x9820, 0x9840, 0x9860, 0x9880, 0x98A0, 0x98C0, 0x98E0, 0x9C00, 0x9C20, 0x9C40, 0x9C60, 0x9C80, 0x9CA0, 0x9CC0, 0x9CE0)

`DS12C887RTC`
: Boolean specifying whether the DS12C887 RTC cartridge (using the MasC=uerade cartridge adapter) should be emulated or not.

`DS12C887RTCbase`
: Integer specifying the DS12C887 RTC base address. (0x9800, 0x9C00)

`DS12C887RTCRunMode`
: Boolean specifying whether the DS12C887 RTC cartridge starts out running or halted. (0: halted, 1: running)

`DS12C887RTCSave`
: Boolean specifying whether the DS12C887 RTC data should be saved when changed or not.

`IEEE488` Boolean specifying whether the IEEE488 interface should be emulated or not.

`MinimonFilename`
: String specifying the filename of the Minimon ROM image.

`MinimonEnabled`
: Boolean specifying whether Minimon emulation is enabled.

`MinimonPgmSwitch`
: Boolean specifying whether the Minimon ROM is writeable.

`MinimonIoSwitch`
: Boolean specifying whether the Minimon ROM is active in IO2/3 (default: enabled)

`MinimonImageWrite`
: Boolean specifying whether changes to the Minimon ROM will be written back to the ROM image file.

7.5.3.2 VIC20 cartridge command-line options

`-iocollision <method>`
: Select the way the I/O collisions should be handled (`IOCollisionHandling`). (0: error message and detach all involved carts, 1: error message and detach last attached involved carts, 2: warning in log and 'AND' the valid return values

`-cartreset`
`+cartreset`
: Do/don't reset machine if a cartridge is attached or detached (`CartridgeReset=1`, `CartridgeReset`).

`-cart2 <name>`
: Specify 4/8/16KiB extension ROM name at $2000

`-cart4 <name>`
: Specify 4/8/16KiB extension ROM name at $4000

`-cart6 <name>`
: Specify 4/8/16KiB extension ROM name at $6000

`-cartA <name>`
: Specify 4/8KiB extension ROM name at $A000

`-cartB <name>`
: Specify 4KiB extension ROM name at $B000

`-cartgeneric <name>`
: Specify generic extension ROM name

`-cartbb <name>`
: Specify Behr-Bonz extension ROM name

`-cartmega <name>`
: Specify Mega-Cart extension ROM name

`-mcnvramfile <name>`
: Set Mega-Cart NvRAM filename (`MegaCartNvRAMfilename`).

`-mcnvramwriteback`
`+mcnvramwriteback`
: Enable/Disable Mega-Cart NvRAM writeback (`MegaCartNvRAMWriteBack=1`, `MegaCartNvRAMWriteBack=0`).

`-cartfe <name>`
: Specify Final Expansion extension ROM name

`-fewriteback`
`+fewriteback`
: Enable/disable Final Expansion write back to ROM file (`FinalExpansionWriteBack=1`, `FinalExpansionWriteBack=0`).

`-cartfp <name>`
: Specify Vic Flash Plugin extension ROM name

`-fpwriteback`
`+fpwriteback`
: Enable/Disable Vic Flash Plugin write back to ROM file (`VicFlashPluginWriteBack=1`, `VicFlashPluginWriteBack=0`).

`-cartrabbit <name>`
: Specify Rabit Tape extension ROM name

`-cartse <name>`
: Specify Super Expander cartridge ROM name

`-cartma <name>`
: Specify Mikro Assembler cartridge ROM name

`-cartwn <name>`
: Specify Write Now cartridge ROM name

`-ultimem <name>`
: Specify Ultimem extension ROM name

`-cartmini <name>`
: Specify name of Minimon ROM image (`MinimonFilename`).

`-umwriteback`
`+umwriteback`
Enable/disable UltiMem write back to ROM file (`UltiMemWriteBack=1`, `UltiMemWriteBack=0`).

`-io2ram`
`+io2ram` Enable/disable the I/O-2 ($9800-$9BFF) RAM cartridge (`IO2RAM=1`, `IO2RAM=0`).

`-io3ram`
`+io3ram` Enable/disable the I/O-3 ($9C00-$9FFF) RAM cartridge (`IO3RAM=1`, `IO3RAM=0`).

`-ieee488`
`+ieee488` Enable/disable VIC-1112 IEEE488 interface (`IEEE488=1`, `IEEE488=0`).

`-vflimod`
`+vflimod` Enable/disable VIC-20 VFLI modification (`VFLImod=1`, `VFLImod=0`)

`-sidcart`

`+sidcart` Enable/disable SID Cartridge (`SidCart=1`, `SidCart=0`).

`-sidcartaddress <address>`
Specify address of the SID Cartridge (`SidAddress`). (0x9800, 0x9C00)

`-sidcartclock <clock>`
Specify clock of the SID Cartridge (`SidClock`). (0: C64, 1: VIC20)

`-ethernetcart`
`+ethernetcart`
Disable/Enable the Ethernet Cartridge (`ETHERNETCART_ACTIVE`). (TFE/RR-Net/64NIC/FB-NET)

`-ethernetcartmode <Mode>`
Mode of Ethernet Cartridge (`ETHERNETCARTMode`). (0: TFE, 1: RR-Net)

`-ethernetcartbase <Base address>`
Base address of the Ethernet Cartridge. (`ETHERNETCARTBase`) (0x9800, 0x9810, 0x9820, 0x9830, 0x9840, 0x9850, 0x9860, 0x9870, 0x9880, 0x9890, 0x98A0, 0x98B0, 0x98C0, 0x98D0, 0x98E0, 0x98F0, 0x9C00, 0x9C10, 0x9C20, 0x9C30, 0x9C40, 0x9C50, 0x9C60, 0x9C70, 0x9C80, 0x9C90, 0x9CA0, 0x9CB0, 0x9CC0, 0x9CD0, 0x9CE0, 0x9CF0)

`-tfe` Enable the Ethernet Cartridge in TFE ("The Final Ethernet") compatible mode and set default I/O address

`-rrnet` Enable the Ethernet Cartridge in RR-Net compatible mode and set default I/O address

`-digimax`
`+digimax` Enable/disable the DigiMAX cartridge (using the MasC=uerade cartridge adapter) (`DIGIMAX=1`, `DIGIMAX=0`).

`-digimaxbase <base address>`
: Base address of the DigiMAX cartridge (`DIGIMAXbase`). (0x9800, 0x9820, 0x9840, 0x9860, 0x9880, 0x98A0, 0x98C0, 0x98E0, 0x9C00, 0x9C20, 0x9C40, 0x9C60, 0x9C80, 0x9CA0, 0x9CC0, 0x9CE0)

`-ds12c887rtc`
`+ds12c887rtc`
: Enable/disable the DS12C887 RTC cartridge (using the MasC=uerade cartridge adapter) (`DS12C887RTC=1`, `DS12C887RTC=0`).

`-ds12c887rtcbase <base address>`
: Base address of the DS12C887 RTC cartridge (`DS12C887RTCbase`). (0x9800, 0x9C00)

`-ds12c887rtchalted`
: Set the DS12C887 RTC oscillator to 'halted' (`DS12C887RTCRunMode=0`).

`-ds12c887rtcrunning`
: Set the DS12C887 RTC oscillator to 'running' (`DS12C887RTCRunMode=1`).

`-ds12c887rtcsave`
`+ds12c887rtcsave`
: Enable/disable saving of the DS12C887 RTC data when changed (`DS12C887RTCSave=1`, `DS12C887RTCSave=0`).

`-sfxse`
`+sfxse`
: Enable/disable the SFX soundexpander cartridge (using the MasC=uerade cartridge adapter) (`SFXSoundExpander=1`, `SFXSoundExpander=0`).

`-sfxsetype <type>`
: Set YM chip type (`SFXSoundExpanderChip`). (3526, 3812)

`-sfxseioswap`
: Swap io mapping (map cart I/O to VIC20 I/O-2) (`SFXSoundExpanderIOSwap=1`).

`+sfxseioswap`
: Swap io mapping (map cart I/O to VIC20 I/O-3) (`SFXSoundExpanderIOSwap=0`).

`-sfxssioswap`
: Swap io mapping (map cart I/O to VIC20 I/O-2) (`SFXSoundSamplerIOSwap=1`).

`+sfxssioswap`
: Don't swap io mapping (map cart I/O to VIC20 I/O-3) (`SFXSoundSamplerIOSwap=0`).

`-sfxss`
`+sfxss`
: Enable/disable the SFX Sound Sampler cartridge (`SFXSoundSampler=1`, `SFXSoundSampler=0`).

`-georamioswap`
: Swap the io mapping (map cart I/O-1 to VIC20 I/O-3 and cart I/O-2 to VIC20 I/O-2) (`GEORAMIOSwap=1`).

`+georamioswap`
: Do not swap the io mapping (map cart I/O-2 to VIC20 I/O-2 and cart I/O-2 to VIC20 I/O-3) (`GEORAMIOSwap=0`).

`-georam`
`+georam` Enable/disable the GEORAM expansion unit (using the MasC=uerade cartridge adapter) (`GEORAM=1`, `GEORAM=0`).

`-georamimage <name>`
Specify name of GEORAM image (`GEORAMfilename`).

`-georamimagerw`
`+georamimagerw`
Allow/disallow writing to GEORAM image (`GEORAMImageWrite=1`, `GEORAMImageWrite=0`).

`-georamsize <size in KiB>`
Size of the GEORAM expansion unit (`GEORAMsize`). (512, 1024, 2048, 4096)

`-minimon`
`+minimon` Enable/Disable the Minimon expansion (`MinimonEnabled=1`, `MinimonEnabled=0`).

`-minimonrom <name>`
Specify name of Minimon ROM image (`MinimonFilename`).

`-minimonrw`
`-minimonro`
Save/Do not save the Minimon ROM when changed (`MinimonImageWrite=1`, `MinimonImageWrite=0`).

`-minimonpgm`
`+minimonpgm`
Set/Remove the Minimon PGM switch (`MinimonPgmSwitch=1`, `MinimonPgmSwitch=0`).

`-minimonio`
`+minimonio`
Set/Remove the Minimon IO switch (`MinimonIoSwitch=1`, `MinimonIoSwitch=0`).

7.5.4 VIC settings

7.5.4.1 VIC resources

`MachineVideoStandard`
Integer that specifies the video standard of the emulated machine (1: PAL, 2: NTSC).

`VICVideoCache`
Boolean specifying whether the video cache is turned on.

`VICDoubleSize`
Boolean specifying whether double-size mode is turned on.

`VICDoubleScan`
Boolean specifying whether double-scan mode is turned on.

`VICPaletteFile`
: String specifying the name of the palette file being used. The `.vpl` extension is optional.

`VICExternalPalette`
: Boolean specifying whether to use external palette file or not.

`VICColorSaturation`
: Integer specifying saturation of internal calculated palette. (0..2000)

`VICColorContrast`
: Integer specifying contrast of internal calculated palette, (0..2000)

`VICColorBrightness`
: Integer specifying brightness of internal calculated palette. (0..2000)

`VICColorGamma`
: Integer specifying gamma of internal calculated palette. (0..4000)

`VICColorTint`
: Integer specifying tint of internal calculated palette. (0..2000)

`VICPALScanLineShade`
: Integer specifying amount of scan line shading for the CRT emulation. (0..1000)

`VICPALBlur`
: Integer specifying amount of horizontal blur for the CRT emulation. (0..1000)

`VICPALOddLinePhase`
: Integer specifying phase for color carrier in odd lines. (0..2000)

`VICPALOddLineOffset`
: Integer specifying phase offset for color carrier in odd lines. (0..2000)

`VICPALDelaylineType`
: Integer specifying the type of delay line used in the CRT (0: normal, 1: U only (1084 style))

`VICAudioLeak`
: Boolean specifying whether to enable/disable video to audio leak emulation.

`VICFilter`
: Integer specifying the rendering filter. (0: None, 1: CRT emulation, 2: Scale2x)

`VICBorderMode`
: Integer specifying border display mode (0: normal, 1: full, 2: debug, 3: none)

`VICVSync` Boolean specifying whether to enable vsync to prevent screen tearing. (0: vsync off, 1: vsync on)

`VICShowStatusbar`
: Boolean specifying whether to show the status bar or not (xvic only).

`VICFullscreen`
: Boolean specifying whether to use fullscreen mode or not (xvic only).

The following resources are only present if hardware scaling support (OpenGL or Direct-X) is present and used at compile time.

`VICAspectRatio`
String/float specifying the aspect ratio. (0.5-2.0)

`VICAspectMode`
Integer specifying the OpenGL aspect mode. (0: off, 1: custom, 2: true)

`VICGLFilter`
Integer specifying the OpenGL filtering mode. (0: nearest neighbour, 1: bilinear, 2: bicubic)

`VICFlipX` Boolean to enable/disable OpenGL horizontal screen flip.

`VICFlipY` Boolean to enable/disable OpenGL vertical screen flip.

`VICRotate`
Boolean to enable/disable OpenGL 90 degree clockwise rotation.

7.5.4.2 VIC command-line options

`-model <model>`
Specify the VIC20 model you want to emulate (`MachineVideoStandard`, `RamBlock0`, `RamBlock1`, `RamBlock2`, `RamBlock3` and `RamBlock5`). (vic20/vic20pal/vic20ntsc, vic21)

`-pal` Use PAL sync factor (`MachineVideoStandard=1`).

`-ntsc` Use NTSC sync factor (`MachineVideoStandard=2`).

`-VICvcache`
`+VICvcache`
Enable/disable the video cache (`VICVideoCache=1`, `VICVideoCache=0`).

`-VICdsize`
`+VICdsize`
Enable/disable the double size mode (`VICDoubleSize=1`, `VICDoubleSize=0`).

`-VICdscan`
`+VICdscan`
Enable/disable the double scan mode (`VICDoubleScan=1`, `VICDoubleScan=0`).

`-VICfilter <Mode>`
Select rendering filter (`VICFilter`). (0: None, 1: CRT emulation, 2: Scale2x)

`-VICpalette NAME`
Specify the name of the palette file (`VICPaletteFile`).

`-VICintpal`
Use an internal calculated palette (`VICExternalPalette=0`).

`-VICextpal`
Use an external palette (file) (`VICExternalPalette=1`).

`-VICborders <mode>`
Set VIC border display mode (`VICBorderMode`). (0: normal, 1: full, 2: debug, 3: none)

`-VICsaturation <0-2000>`
: Set saturation of internal calculated palette (`VICColorSaturation`).

`-VICcontrast <0-2000>`
: Set contrast of internal calculated palette (`VICColorContrast`).

`-VICbrightness <0-2000>`
: Set brightness of internal calculated palette (`VICColorBrightness`).

`-VICgamma <0-4000>`
: Set gamma of internal calculated palette (`VICColorGamma`).

`-VICtint <0-2000>`
: Set tint of internal calculated palette (`VICColorTint`).

`-VICoddlinesphase <0-2000>`
: Set phase for color carrier in odd lines (`VICPALOddLinePhase`).

`-VICoddlinesoffset <0-2000>`
: Set phase offset for color carrier in odd lines. (`VICPALOddLineOffset`).

`-VICcrtblur <0-1000>`
: Amount of horizontal blur for the CRT emulation (`VICPALBlur`).

`-VICcrtscanlineshade <0-1000>`
: Amount of scan line shading for the CRT emulation (`VICPALScanLineShade`).

`-VICcrtscanlineshade <type>`
: Type of delay line used in the CRT (0: normal, 1: U only (1084 style)) (`VICPALDelaylineType`).

`-VICaudioleak`
`+VICaudioleak`
: Enable/disable video to audio leak emulation (`VICAudioLeak=1`, `VICAudioLeak=0`).

`-VICshowstatusbar`
`+VICshowstatusbar`
: Show/hide status bar (`VICShowStatusbar=1`, `VICShowStatusbar=0`).

`-VICvsync`
: Enable vsync to prevent screen tearing (`VICVSync=1`).

`+VICvsync`
: Disable vsync to allow screen tearing (`VICVSync=0`).

`-VICfull`
`+VICfull` Enable/Disable fullscreen (`VICFullscreen=1`, `VICFullscreen=0`) (xvic only).

The following command-line options are only present if hardware scaling support (OpenGL or Direct-X) is present and used at compile time.

`-VICaspectmode <mode>`
: Set the aspect mode (`VICAspectMode`). (0: off, 1: custom, 2: true)

`-VICaspect <aspect ratio>`
: Set the aspect ratio (`VICAspectRatio`). (0.5-2.0)

`-VICglfilter <mode>`
: Set OpenGL (or Direct-X) filtering mode (0 = nearest, 1 = linear, 2 = bicubic) (`VICglfilter`).

`-VICflipx`
`+VICflipx`
: Enable/disable OpenGL horizontal screen flip (`VICFlipX=1`, `VICFlipX=0`).

`-VICflipy`
`+VICflipy`
: Enable/disable OpenGL vertical screen flip (`VICFlipY=1`, `VICFlipY=0`).

`-VICrotate`
`+VICrotate`
: Enable/disable OpenGL 90 degree rotation (`VICRotate=1`, `VICRotate=0`).

7.5.5 Changing memory configuration

It is possible to change the VIC20 memory configuration in two ways: by enabling and/or disabling certain individual memory blocks, or by choosing one among a few typical memory configurations. The former can be done by modifying resource values directly or from the right-button menu; the latter can only be done from the menu.

There are 5 RAM expansion blocks in the VIC20, numbered 0, 1, 2, 3 and 5:

- block 0 (3 KiB at $0400-$0FFF);
- block 1 (8 KiB at $2000-$3FFF);
- block 2 (8 KiB at $4000-$5FFF);
- block 3 (8 KiB at $6000-$7FFF);
- block 5 (8 KiB at $A000-$BFFF).

These blocks are called *expansion blocks* because they are not present a stock ("unexpanded") machine. Each of them is associated to a boolean `RamBlockX` resource (where `X` is the block number) that specifies whether the block is enabled or not.

There are also some common memory configurations you can select:

- no RAM expansion blocks at all;
- all RAM expansion blocks enabled;
- 3KiB expansion (only block 0 is enabled);
- 8KiB expansion (only block 1 is enabled);
- 16KiB expansion (only blocks 1 and 2 are enabled);
- 24KiB expansion (only blocks 1, 2 and 3 are enabled).

7.5.5.1 VIC20 memory configuration resources

`RAMBlock0`
`RAMBlock1`
`RAMBlock2`
`RAMBlock3`
`RAMBlock5`
: Booleans specifying whether RAM blocks 0, 1, 2, 3 and 5 must be enabled.

7.5.5.2 VIC20 memory configuration command-line options

`-memory <config>`
: Specify memory configuration. It must be a comma-separated list of options, each of which can be one the following:

  - `none` (no extension);
  - `all` (all blocks);
  - `3k` (3KiB space in block 0);
  - `8k` (first 8KiB extension block);
  - `16k` (first and second 8KiB extension blocks);
  - `24k` (first, second and 3rd extension blocks);
  - `0`, `1`, `2`, `3`, `5` (memory in respective blocks);
  - `04`, `20`, `40`, `60`, `A0` (memory at respective address.

  For example,

  ```
  xvic -memory none
  ```

  gives an unexpanded VIC20. While

  ```
  xvic -memory 60,a0
  ```

  or

  ```
  xvic -memory 3,5
  ```

  enables memory in blocks 3 and 5, which is the usual configuration for 16KiB ROM modules.

7.5.6 VIC20 system ROM settings

These settings can be used to control what system ROMs are loaded in the VIC20 emulator at startup.

7.5.6.1 VIC20 system ROM resources

`KernalName`
: String specifying the name of the Kernal ROM (default `kernal`).

`BasicName`
: String specifying the name of the Basic ROM (default `basic`).

`ChargenName`
: String specifying the name of the character generator ROM (default `chargen`).

7.5.6.2 VIC20 system ROM command-line options

`-kernal <name>`
: Specify the filename of the Kernal ROM file (`KernalName`).

`-basic <name>`
: Specify the filename of the Basic ROM file (`BasicName`).

`-chargen <name>`
: Specify the filename of the character generator ROM file (`ChargenName`).

`-cart2 NAME`
`-cart4 NAME`
`-cart6 NAME`
`-cartA NAME`
`-cartB NAME`
Specify `NAME` as the cartridge image to attach. (`CartridgeFile2000`, ..., `CartridgeFileB000`).

7.5.7 VIC20 settings

7.5.7.1 VIC20 command-line options

7.6 PLUS4-specific commands and settings

7.6.1 Using cartridge images

As with the C64 (see Section 7.1.1 [C64 cartridges], page 121), it is possible to attach several types of cartridge images:

7.6.2 PLUS4 cartridge settings

7.6.2.1 PLUS4 cartridge resources

`CartridgeReset`
Boolean specifying whether the machine should be reset when a cartridge is changed.

`CartridgeType`
Integer specifying the type of the default cartridge.

The following cartridge types are valid:

- - 1: None
- - 2: Generic
- 1: c264 magic cart
- 2: Plus4 multi cart
- 3: 1MB Cartridge
- 4: "Speedy" freezer cartridge

`CartridgeFile`
String specifying the filename of the image for the current cartridge.

`c1loName` String specifying the filename of cartridge 1 Lo ROM.

`c1hiName` String specifying the filename of cartridge 1 High ROM.

7.6.2.2 PLUS4 cartridge command-line options

`-cartreset`
`+cartreset`
Do/don't reset machine if a cartridge is attached or detached (`CartridgeReset=1`, `CartridgeReset`).

`-c1lo <name>`
: Specify name of Cartridge 1 low ROM image (`c1loName`).

`-c1hi <name>`
: Specify name of Cartridge 1 high ROM image (`c1HiName`).

`-cart <Name>`
`+cart`
: Smart-attach cartridge image / Start without cartridge

`-cartjacint <name>`
: Attach a 1MiB "1MB Cartridge" image

`-cartmagic <name>`
: Attach a 128kiB/256kiB/512kiB/1MiB/2MiB "c264 magic cart" image

`-cartmulti <name>`
: Attach a 1MiB/2MiB "Plus4 multi cart" image. The binary should contain all c1lo banks first, then all c1hi banks.

`-cartspeedy <name>`
: Attach a 8kiB "Speedy" image

7.6.3 TED settings

7.6.3.1 TED resources

`TEDVideoCache`
: Boolean specifying whether the video cache is turned on.

`TEDDoubleSize`
: Boolean specifying whether double-size mode is turned on.

`TEDDoubleScan`
: Boolean specifying whether double-scan mode is turned on.

`TEDPaletteFile`
: String specifying the name of the palette file being used. The `.vpl` extension is optional.

`TEDExternalPalette`
: Boolean specifying whether to use external palette file or not.

`TEDColorSaturation`
: Integer specifying saturation of internal calculated palette. (0..2000)

`TEDColorContrast`
: Integer specifying contrast of internal calculated palette. (0..2000)

`TEDColorBrightness`
: Integer specifying brightness of internal calculated palette. (0..2000)

`TEDColorGamma`
: Integer specifying gamma of internal calculated palette. (0..4000)

`TEDColorTint`
: Integer specifying tint of internal calculated palette. (0..2000)

`TEDPALScanLineShade`
: Integer specifying amount of scan line shading for the CRT emulation. (0..1000)

`TEDPALBlur`
: Integer specifying amount of horizontal blur for the CRT emulation. (0..1000)

`TEDPALOddLinePhase`
: Integer specifying phase for color carrier in odd lines. (0..2000)

`TEDPALOddLineOffset`
: Integer specifying phase offset for color carrier in odd lines. (0..2000)

`TEDPALDelaylineType`
: Integer specifying the type of delay line used in the CRT (0: normal, 1: U only (1084 style))

`TEDAudioLeak`
: Boolean specifying whether to enable/disable video to audio leak emulation.

`TEDFilter`
: Integer specifying rendering filter. (0: None, 1: CRT emulation, 2: Scale2x)

`TEDBorderMode`
: Integer specifying border display mode. (0: normal, 1: full, 2: debug, 3: none)

`TEDVSync` Boolean specifying whether to enable vsync to prevent screen tearing. (0: vsync off, 1: vsync on)

`TEDShowStatusbar`
: Boolean specifying whether to show the status bar or not (xplus4 only).

`TEDFullscreen`
: Boolean specifying whether to use fullscreen mode or not (xplus4 only).

The following resources are only present if hardware scaling support (OpenGL or Direct-X) is present and used at compile time.

`TEDAspectRatio`
: String/float specifying the aspect ratio. (0.5-2.0)

`TEDAspectMode`
: Integer specifying the OpenGL aspect mode. (0: off, 1: custom, 2: true)

`TEDGLFilter`
: Integer specifying the OpenGL filtering mode. (0: nearest neighbour, 1: bilinear, 2: bicubic)

`TEDFlipX` Boolean to enable/disable OpenGL horizontal screen flip.

`TEDFlipY` Boolean to enable/disable OpenGL vertical screen flip.

`TEDRotate`
: Boolean to enable/disable OpenGL 90 degree clockwise rotation.

7.6.3.2 TED command-line options

`-TEDvcache`
`+TEDvcache`
: Enable/disable the video cache (`TEDVideoCache=1`, `TEDVideoCache=0`).

`-TEDdsize`
`+TEDdsize`
: Enable/disable double size (`TEDDoubleSize=1`, `TEDDoubleSize=0`).

`-TEDdscan`
`+TEDdscan`
: Enable/disable double scan (`TEDDoubleScan=1`, `TEDDoubleScan=0`).

`-TEDfilter <Mode>`
: Select rendering filter (`TEDFilter`). (0: None, 1: CRT emulation, 2: Scale2x)

`-TEDintpal`
: Use an internal calculated palette (`TEDExternalPalette=0`).

`-TEDextpal`
: Use an external palette (file) (`TEDExternalPalette=1`).

`-TEDpalette <name>`
: Specify name of the external palette file (`TEDPaletteFile`).

`-TEDborders <mode>`
: Set TED border display mode (`TEDBorderMode`). (0: normal, 1: full, 2: debug, 3: none)

`-TEDsaturation <0-2000>`
: Set saturation of internal calculated palette (`TEDColorSaturation`).

`-TEDcontrast <0-2000>`
: Set contrast of internal calculated palette (`TEDColorContrast`).

`-TEDbrightness <0-2000>`
: Set brightness of internal calculated palette (`TEDColorBrightness`).

`-TEDgamma <0-4000>`
: Set gamma of internal calculated palette (`TEDColorGamma`).

`-TEDtint <0-2000>`
: Set tint of internal calculated palette (`TEDColorTint`).

`-TEDoddlinesphase <0-2000>`
: Set phase for color carrier in odd lines (`TEDPALOddLinePhase`).

`-TEDoddlinesoffset <0-2000>`
: Set phase offset for color carrier in odd lines (`TEDPALOddLineOffset`).

`-TEDcrtblur <0-1000>`
: Amount of horizontal blur for the CRT emulation (`TEDPALBlur`).

`-TEDcrtscanlineshade <0-1000>`
: Amount of scan line shading for the CRT emulation (`TEDPALScanLineShade`).

`-TEDcrtscanlineshade <type>`
: Type of delay line used in the CRT (0: normal, 1: U only (1084 style)) (`TEDPALDelaylineType`).

`-TEDaudioleak`
`+TEDaudioleak`
: Enable/disable video to audio leak emulation (`TEDAudioLeak=1`, `TEDAudioLeak=0`).

`-TEDshowstatusbar`
`+TEDshowstatusbar`
: Show/hide status bar (`TEDShowStatusbar=1`, `TEDShowStatusbar=0`).

`-TEDvsync`
: Enable vsync to prevent screen tearing (`TEDVSync=1`).

`+TEDvsync`
: Disable vsync to allow screen tearing (`TEDVSync=0`).

`-TEDfull`
`+TEDfull`
: Enable/Disable fullscreen (`TEDFullscreen=1`, `TEDFullscreen=0`) (xplus4 only).

The following command-line options are only present if hardware scaling support (OpenGL or Direct-X) is present and used at compile time.

`-TEDaspectmode <mode>`
: Set the aspect mode (`TEDAspectMode`). (0: off, 1: custom, 2: true)

`-TEDaspect <aspect ratio>`
: Set the aspect ratio (`TEDAspectRatio`). (0.5-2.0)

`-TEDglfilter <mode>`
: Set OpenGL (or Direct-X) filtering mode (0 = nearest, 1 = linear, 2 = bicubic) (`TEDglfilter`).

`-TEDflipx`
`+TEDflipx`
: Enable/disable OpenGL horizontal screen flip (`TEDFlipX=1`, `TEDFlipX=0`).

`-TEDflipy`
`+TEDflipy`
: Enable/disable OpenGL vertical screen flip (`TEDFlipY=1`, `TEDFlipY=0`).

`-TEDrotate`
`+TEDrotate`
: Enable/disable OpenGL 90 degree rotation (`TEDRotate=1`, `TEDRotate=0`).

7.6.4 PLUS4 I/O extension settings

7.6.4.1 PLUS4 I/O extension resources

`DIGIBLASTER`
: Boolean to enable/disable the Digiblaster emulation.

`SpeechEnabled`
: Boolean to enable/disable the emulation of the V364 speech chip.

`SidCart` Boolean specifying whether SID-Cart emulation is enabled or not.

`SidAddress`
: Integer that specifies the base address of the emulated SID chip. (0xFD40, 0xFE80)

`SidClock` Integer specifying the clock rate used for the emulated SID chip (0: C64, 1: Plus4)

`SIDCartJoy`
: Boolean to enable/disable the emulation of the SID-Cart control port.

7.6.4.2 PLUS4 I/O extension command-line options

`-digiblaster`
`+digiblaster`
: Enable/disable the digiblaster add-on (`DIGIBLASTER=1`, `DIGIBLASTER=0`).

`-sidcart`

`+sidcart` Enable/disable SID Cartridge (`SidCart=1`, `SidCart=0`).

`-sidcartjoy`
`+sidcartjoy`
: Enable/disable SID cartridge control port (`SIDCartJoy=1`, `SIDCartJoy=0`).

`-sidcartaddress <address>`
: Specify address of the SID Cartridge (`SidAddress`). (0xFD40, 0xFE80)

`-sidcartclock <clock>`
: Specify clock of the SID Cartridge (`SidClock`). (0: C64, 1: Plus4)

`-speech`
`+speech` Enable/Disable the v364 speech add-on (`SpeechEnabled=1`, `SpeechEnabled=0`).

7.6.5 PLUS4 system ROM settings

7.6.5.1 PLUS4 system ROM resources

`KernalName`
: String specifying the name of the Kernal ROM (default `kernal`).

`BasicName`
: String specifying the name of the Basic ROM (default `basic`).

`FunctionLowName`
: String specifying the filename of the Lo Function ROM.

`FunctionHighName`
: String specifying the filename of the High Function ROM.

`c2loName` String specifying the filename of cartridge 2 Lo ROM.

`c2hiName` String specifying the filename of cartridge 2 High ROM.

7.6.5.2 PLUS4 system ROM command-line options

`-kernal <Name>`
: Specify name of the Kernal ROM image (`KernalName`).

`-basic <Name>`
: Specify name of the BASIC ROM image (`BasicName`).

`-functionlo <name>`
: Specify name of Function low ROM image (`FunctionLowName`).

`-functionhi <name>`
: Specify name of Function high ROM image (`FunctionHighName`).

`-c2lo <name>`
: Specify name of Cartridge 2 low ROM image (`c2loName`).

`-c2hi <name>`
: Specify name of Cartridge 2 high ROM image (`c2HiName`).

7.6.6 PLUS4 settings

7.6.6.1 PLUS4 resources

`MachineVideoStandard`
: Integer that specifies the video standard of the emulated machine (1: PAL, 2: NTSC).

`RamSize` Integer specifying the size of the memory in KiB. (16, 32, 64)

`MemoryHack`
: Integer specifying what memory expansion hack is active. (0: None, 1: C256K, 2: H256K, 3: H1024K)

`Acia1Enable`
: Boolean specifying whether the ACIA should be emulated or not.

7.6.6.2 PLUS4 command-line options

`-model <model>`
: Specify the PLUS4 model you want to emulate (`MachineVideoStandard`, `RamSize`, `KernalName`, `BasicName`, `FunctionLowName`, `FunctionHighName`, `c2loName`, `Acia1Enable`, `SpeechEnabled`). (c16/c16pal/c16ntsc, plus4/plus4pal/plus4ntsc, v364/cv364, c232)

`-pal` Use PAL sync factor (`MachineVideoStandard=1`).

`-ntsc` Use NTSC sync factor (`MachineVideoStandard=2`).

`-ramsize <ramsize>`
: Specify size of RAM installed in KiB (`RamSize`). (16, 32, 64)

`-memoryexphack <device>`
: Set active memory expansion hack (`MemoryHack`). (0: None, 1: C256K, 2: H256K, 3: H1024K)

`-acia`
`+acia` Enable/Disable the ACIA emulation

7.7 PET-specific commands and settings

This section lists the settings and commands that are PET-specific and thus are not present in the other emulators.

7.7.1 Changing PET model settings

With `xpet`, it is possible to change at runtime the characteristics of the emulated PET so that it matches (or not) the ones of a certain PET model, and it is also possible to select from a common set of PET models so that all the features are selected accordingly.

The former is done by changing the following resources (via resource file, command line options or right-menu items):

`RamSize` Integer specifying the size of the memory in KiB. 96KiB denotes an 8096, 128KiB an 8296. (4, 8, 16, 32, 96, 128)

`IOSize` Integer specifying the size of the I/O area in Bytes for the 8296 model. (256, 2048)

`VideoSize`
Integer specifying the number of columns on the screen. (0: Automatic from ROM, 40: 40 columns, 80: 80 columns)

`Ram9` Boolean specifying if the 8296 can map RAM into the address range $9*** or not.

`RamA` Boolean specifying if the 8296 can map RAM into the address range $A*** or not.

`SuperPET` Boolean that enables/disables the SuperPET (MicroMainFrame 9000) I/O and 6809 CPU, and disables/enables the 8x96 mappings.

`Basic1` Boolean to enable/disable the patching of a version 1 kernal ROM to make the IEEE488 interface work.

`EoiBlank` Boolean to enable/disable the "blank screen on EOI" feature of the oldest PET 2001.

`Screen2001`
Boolean specifying if the screen memory should have "mirrors" like on the oldest PET 2001: $8000-$83FF is visible 3 more times, up to $8FFF. Otherwise, mirrors (if any) only go up to $87FF.

`DiagPin` Boolean to enable/disable the diagnostic pin on the PET userport (see below).

`ChargenName`
String specifying the name of the character generator ROM (default `chargen`).

`KernalName`
String specifying the name of the Kernal ROM (default `kernal4`).

`BasicName`
String specifying the name of the Basic ROM (default `basic4`).

`EditorName`
String specifying the filename of the editor ROM file. This file contains an overlay for the editor ROM at $E000-$E7FF if necessary.

`RomModule9Name`
: String specifying the filename for the $9*** Expansion ROM file. This file contains an expansion ROM image of 4KiB.

`RomModuleAName`
: String specifying the filename for the $A*** Expansion ROM file. This file contains an expansion ROM image of 4KiB.

`RomModuleBName`
: String specifying the filename for the $B*** Expansion ROM file. This file contains an expansion ROM image of 4KiB. This file overlays the lowest 4KiB of a BASIC 4 ROM.

Choosing a common PET model is done from the setting dialog, by choosing an item from the "Model" selection. Available models are:

- PET 2001-8N
- PET 3008
- PET 3016
- PET 3032
- PET 3032B
- PET 4016
- PET 4032
- PET 4032B
- PET 8032
- PET 8096
- PET 8296
- SuperPET

Notice that this will **reset the emulated machine**.

It is also possible to select the PET model at startup, with the `-model` command-line option: for example, '`xpet -model 3032`' will emulate a PET 3032 while '`xpet -model 8296`' will emulate a PET 8296.

7.7.2 CRTC Settings

7.7.2.1 CRTC resources

`Crtc`
: Boolean specifying whether to enable/disable CRTC 6545 emulation (all models from 40xx and above).

`CrtcVideoCache`
: Boolean specifying whether the video cache is turned on.

`CrtcDoubleSize`
: Boolean specifying whether double-size mode is turned on.

`CrtcDoubleScan`
: Boolean specifying whether double-scan mode is turned on.

`CrtcStretchVertical`
: Boolean specifying whether vertical stretching is turned on.

`CrtcPaletteFile`
: String specifying the name of the palette file being used. The `.vpl` extension is optional.

`CrtcExternalPalette`
: Boolean specifying whether to use external palette file or not.

`CrtcColorSaturation`
: Integer specifying saturation of internal calculated palette. (0..2000)

`CrtcColorContrast`
: Integer specifying contrast of internal calculated palette. (0..2000)

`CrtcColorBrightness`
: Integer specifying brightness of internal calculated palette. (0..2000)

`CrtcColorGamma`
: Integer specifying gamma of internal calculated palette. (0..4000)

`CrtcColorTint`
: Integer specifying tint of internal calculated palette. (0..2000)

`CrtcPALScanLineShade`
: Integer specifying amount of scan line shading for the CRT emulation. (0..1000)

`CrtcPALBlur`
: Integer specifying amount of horizontal blur for the CRT emulation. (0..1000)

`CrtcAudioLeak`
: Boolean specifying whether to enable/disable video to audio leak emulation.

`CrtcFilter`
: Integer specifying rendering filter. (0: None, 1: CRT emulation, 2: Scale2x)

`CrtcVSync`
: Boolean specifying whether to enable vsync to prevent screen tearing. (0: vsync off, 1: vsync on)

`CrtcShowStatusbar`
: Boolean specifying whether to show the status bar or not (xcbm2 and xpet only).

`CrtcFullscreen`
: Boolean specifying whether to use fullscreen mode or not (xcbm2 and xpet only).

The following resources are only present if hardware scaling support (OpenGL or Direct-X) is present and used at compile time.

`CrtcAspectRatio`
: String/float specifying the aspect ratio. (0.5-2.0)

`CrtcAspectMode`
: Integer specifying the OpenGL aspect mode. (0: off, 1: custom, 2: true)

`CrtcGLFilter`
: Integer specifying the OpenGL filtering mode. (0: nearest neighbour, 1: bilinear, 2: bicubic)

`CrtcFlipX`
: Boolean to enable/disable OpenGL horizontal screen flip.

`CrtcFlipY`
: Boolean to enable/disable OpenGL vertical screen flip.

`CrtcRotate`
: Boolean to enable/disable OpenGL 90 degree clockwise rotation.

7.7.2.2 CRTC command-line options

`-crtc`
`+crtc`
: Enable/disable the 6545 CRTC emulation (`Crtc=1`, `Crtc=0`).

`-Crtcvcache`
`+Crtcvcache`
: Enable/disable the video cache (`CrtcVideoCache=1`, `CrtcVideoCache=0`).

`-Crtcdsize`
`+Crtcdsize`
: Enable/disable double size (`CrtcDoubleSize=1`, `CrtcDoubleSize=0`).

`-CRTCstretchvertical`
`+CRTCstretchvertical`
: Enable/disable vertical stretching (`CrtcStretchVertical=1`, `CrtcStretchVertical=0`).

`-Crtcdscan`
`+Crtcdscan`
: Enable/disable double scan (`CrtcDoubleScan=1`, `CrtcDoubleScan=0`).

`-Crtcfilter <Mode>`
: Select rendering filter (`CrtcFilter`). (0: None, 2: CRT emulation, 3: Scale2x)

`-Crtcintpal`
: Use an internal calculated palette (`CrtcExternalPalette=0`).

`-Crtcextpal`
: Use an external palette (file) (`CrtcExternalPalette=1`).

`-Crtcpalette <name>`
: Specify the name of the palette file (`CrtcPaletteFile`).

`-Crtcsaturation <0-2000>`
: Set saturation of internal calculated palette (`CrtcColorSaturation`).

`-Crtccontrast <0-2000>`
: Set contrast of internal calculated palette (`CrtcColorContrast`).

`-Crtcbrightness <0-2000>`
: Set brightness of internal calculated palette (`CrtcColorBrightness`).

`-Crtcgamma <0-4000>`
: Set gamma of internal calculated palette (`CrtcColorGamma`).

`-Crtctint <0-2000>`
: Set tint of internal calculated palette (`CrtcColorTint`).

`-Crtccrtblur <0-1000>`
: Amount of horizontal blur for the CRT emulation (`CrtcPALBlur`).

`-Crtccrtscanlineshade <0-1000>`
: Amount of scan line shading for the CRT emulation (`CrtcPALScanLineShade`).

`-Crtcaudioleak`
`+Crtcaudioleak`
: Enable/disable video to audio leak emulation (`CrtcAudioLeak=1`, `CrtcAudioLeak=0`).

`-Crtcshowstatusbar`
`+Crtcshowstatusbar`
: Show/hide status bar (`CrtcShowStatusbar=1`, `CrtcShowStatusbar=0`).

`-Crtcvsync`
: Enable vsync to prevent screen tearing (`CrtcVSync=1`).

`+Crtcvsync`
: Disable vsync to allow screen tearing (`CrtcVSync=0`).

`-CRTCfull`
`+CRTCfull`
: Enable/Disable fullscreen (`CrtcFullscreen=1`, `CrtcFullscreen=0`) (xcbm2 and xpet only).

The following command-line options are only present if hardware scaling support (OpenGL or Direct-X) is present and used at compile time.

`-Crtcaspectmode <mode>`
: Set the aspect mode (`CrtcAspectMode`). (0: off, 1: custom, 2: true)

`-Crtcaspect <aspect ratio>`
: Set the aspect ratio (`CrtcAspectRatio`). (0.5-2.0)

`-Crtcglfilter <mode>`
: Set OpenGL (or Direct-X) filtering mode (0 = nearest, 1 = linear, 2 = bicubic) (`Crtcglfilter`).

`-Crtcflipx`
`+Crtcflipx`
: Enable/disable OpenGL horizontal screen flip (`CrtcFlipX=1`, `CrtcFlipX=0`).

`-Crtcflipy`
`+Crtcflipy`
: Enable/disable OpenGL vertical screen flip (`CrtcFlipY=1`, `CrtcFlipY=0`).

`-Crtcrotate`
`+Crtcrotate`
: Enable/disable OpenGL 90 degree rotation (`CrtcRotate=1`, `CrtcRotate=0`).

7.7.3 PET I/O extension settings

7.7.3.1 PET I/O extension resources

`PETREU` Boolean specifying whether PET REU emulation is enabled or not.

`PETREUfilename`
String specifying the filename of the PET REU image.

`PETREUsize`
Integer specifying the size of the emulated PET REU in KiB. (128, 512, 1024, 2048)

`PETDWW` Boolean specifying whether DWW emulation is enabled or not.

`PETDWWfilename`
String specifying the filename of the DWW image RAM image.

`PETHRE` Boolean specifying whether HRE emulation is enabled or not.

`PETColour`
Integer selecting the colour extension. (0: off, 1: RGBI, 2: Analog)

`PETColourBG`
Integer specifying the analog colour background on PET 4032. (0..255)

`SidCart` Boolean specifying whether SID Cartridge emulation is enabled or not.

`SidAddress`
Integer that specifies the base address of the emulated SID chip. (0x8F00, 0xE900)

`SidClock` Integer specifying the clock rate used for the emulated SID chip. (0: C64, 1: PET)

7.7.3.2 PET I/O extension command-line options

`-petreu`
`+petreu` Enable/disable the PET Memory Expansion Unit (`PETREU=1`, `PETREU=0`).

`-petreuimage <name>`
Specify name of PET Ram and Expansion Unit image (`PETREUfilename`).

`-petreuramsize <size in KiB>`
Size of the PET Ram and Expansion Unit (`PETREUsize`). (128, 512, 1024, 2048)

`-petdww`
`+petdww` Enable/disable the PET DWW hi-res board (30xx models only) (`PETDWW=1`, `PETDWW=0`).

`-petdwwimage <name>`
Specify name of PET DWW RAM image (`PETDWWfilename`).

`-pethre`
`+pethre` Enable/disable the PET HRE extension (model 8296 only) (`PETHRE=1`, `PETHRE=0`).

`-colour-rgbi`
: Enable the RGBI colour extension to PET 4032 (`PETColour=1`).

`-colour-analog`
: Enable the analog colour extension to PET 4032 (`PETColour=2`).

`-colour-analog-bg <Colour 0-255>`
: Select the analog colour background on PET 4032 (`PETColourBG`).

`-sidcart`
`+sidcart` Enable/disable the SID Cartridge (`SidCart=1`, `SidCart=0`).

`-sidcartaddress <address>`
: Specify address of the SID Cartridge (`SidAddress`). (0x8F00, 0xE900)

`-sidcartclock <clock>`
: Specify clock of the SID Cartridge (`SidClock`). (0: C64, 1: PET)

7.7.4 PET system ROM settings

7.7.4.1 PET system ROM resources

`H6809RomAName`
`H6809RomBName`
`H6809RomCName`
`H6809RomDName`
`H6809RomEName`
`H6809RomFName`
: Strings specifying the filenames of the respective H6809 ROM images, relevant for the SuperPET.

7.7.4.2 PET system ROM command-line options

`-kernal <name>`
: Specify filename of the Kernal ROM file (`KernalName`).

`-basic <name>`
: Specify filename of the BASIC ROM file (`BasicName`).

`-editor NAME`
: Specify the filename for the editor ROM file (`EditorName`).

`-chargen <name>`
: Specify filename of the character generator ROM file (`ChargenName`).

`-rom9 <name>`
: Specify the filename for the ROM image file for the $9*** cartridge area (`RomModule9Name`).

`-romA <name>`
: Specify the filename for the ROM image file for the $A*** cartridge area (`RomModuleAName`).

`-romB <name>`
: Specify the filename for the ROM image file for the $B*** cartridge area (`RomModuleBName`).

`-6809romA <Name>`
Specify 4KiB to 24KiB ROM file name at $A000 for 6809 (`H6809RomAName`).

`-6809romB <Name>`
Specify 4KiB to 20KiB ROM file name at $B000 for 6809 (`H6809RomBName`).

`-6809romC <Name>`
Specify 4KiB to 16KiB ROM file name at $C000 for 6809 (`H6809RomCName`).

`-6809romD <Name>`
Specify 4KiB to 12KiB ROM file name at $D000 for 6809 (`H6809RomDName`).

`-6809romE <Name>`
Specify 2KiB or 8KiB ROM file name at $E000 for 6809 (`H6809RomEName`).

`-6809romF <Name>`
Specify 4KiB ROM file name at $F000 for 6809 (`H6809RomFName`).

7.7.5 The PET diagnostic pin

It is possible to enable or disable emulation of the PET diagnostic pin via the `DiagPin` resource, or the "PET userport diagnostic pin" item in the right-button menu.

When the diagnostic pin is set, the Kernal does not try to initialize the BASIC, but directly jumps into the builtin machine monitor.

7.7.6 PET settings

7.7.6.1 PET resources

`MachineVideoStandard`
Integer that specifies the video standard of the emulated machine (1: PAL, 2: NTSC).

`CPUswitch`
Integer specifying the status of the SuperPET CPU switch (0: 6502, 1: 6809, 2: Prog.)

`CB2Lowpass`
Integer specifying the frequency of the low-pass filter that is applied to the CB2 sound signal. Default: 16000

7.7.6.2 PET command line options

These are the commandline options specific for the PET models.

`-pal` Use PAL sync factor (`MachineVideoStandard=1`).

`-ntsc` Use NTSC sync factor (`MachineVideoStandard=2`).

`-model <model>`
Specify the PET model you want to emulate (`Basic1`, `Basic1Chars`, `EoiBlank`, `Screen2001`, `ChargenName`, `KernalName`, `BasicName`, `EditorName`, `RomModule9Name`, `RomModuleAName`, `RomModuleBName`, `H6809RomAName`, `H6809RomBName`, `H6809RomCName`, `H6809RomDName`, `H6809RomEName`, `H6809RomFName`). (2001, 3008, 3016, 3032, 3032B, 4016, 4032, 4032B, 8032, 8096, 8296, SuperPET)

`-iosize <size>`
: Specify size of I/O in Bytes (`IOSize`). (256, 2048)

`-ramsize <size>`
: Specify size of RAM in KiB (`RamSize`). (8, 16, 32, 96, 128)

`-videosize <size>`
: Specify video size (`VideoSize`). (0: Automatic from ROM, 40: 40 columns, 80: 80 columns)

`-petram9`
`+petram9`
: Switch on/off RAM mapping at address $9000-$9FFF (`Ram9=1`, `Ram9=0`).

`-petramA`
`+petramA`
: Switch on/off RAM mapping at address $A000-$AFFF (`RamA=1`, `RamA=0`).

`-superpet`
`+superpet`
: Enable/disable SuperPET I/O and CPU emulation (`SuperPET`).

`-cpu6502`
: Set SuperPET CPU switch to '6502' (`CPUswitch=0`).

`-cpu6809`
: Set SuperPET CPU switch to '6809' (`CPUswitch=1`).

`-cpuprog`
: Set SuperPET CPU switch to 'Prog' (`CPUswitch=2`).

`-basic1`
`+basic1`
: Enable/disable patching the IEEE488 section of the PET2001 ROM when detected (`Basic1=1`, `Basic1=0`).

`-eoiblank`
`+eoiblank`
: Model-2001-only quirk: Enable/disable EOI blanking the screen. (`EoiBlank=1`, `EoiBlank=0`).

`-screen2001`
`+screen2001`
: Model-2001-only quirk: the 1 KiB of screen memory is mirrored all the way through the $8*** block. This option is incompatible with 80 columns and the Colour PET extension. (`Screen2001=1`, `Screen2001=0`).

`-diagpin`
`+diagpin`
: Enable/disable the diagnostic pin at the PET userport (`DiagPin=1`, `DiagPin=0`).

`-cb2lowpass`
: Integer specifying the frequency of the low-pass filter that is applied to the CB2 sound signal. (`CB2Lowpass`)

7.7.7 Colour PET

The Colour PET is a simple extension by Steve Gray `http://cbmsteve.ca/colourpet/index.html`. It exploits the similarities and differences between the 4032 and 8032 models, to use memory at $8800 as colour RAM.

There are two versions of the extension:

with digital colour (RGBI), which can select 16 foreground and 16 background colours for each character cell. The 8 colour bits are used as RGBI RGBI, with the most significant bits for the background, and the least significant bits for the foreground.

With analog colour, which can use 256 foreground colours for each character cell, the 8 colour bits are used as RRR GGG BB.

7.7.8 Changing screen colors

It is also possible to choose what color set is used for the emulation window. This is done by specifying a palette file name (see Section 3.4 [Palette files], page 40) in the `PaletteName` resource. The menu provides the following values:

- `green.vpl` (default, "green)"), the good old green-on-black feeling; for use on the 3000 models and up;
- `2001-blueish.vpl` ("2001-blueish"), an approximation of the slightly blue CRTs used for the 2001 models;
- `amber.vpl` ("amber"), an amber phosphor lookalike (not used by Commodore);
- `white.vpl` ("white"), simple white-on-black palette (not used by Commodore).

7.7.9 DWW high resolution graphics

The DWW, or Double-W[1], board is a high resolution graphics board for PET models 30xx. It attaches to the internal expansion connector. It would not physically fit in 20xx, 40xx or 80xx models because their connectors are physically and logically different. Apart from this, it requires address space at $EC00-$EFFF, which makes it unfit to work in 40xx and 80xx models, which have only 256 bytes of I/O space at $E800.

The DWW board consists of 8 KiB of RAM and a PIA to control various options. The RAM can be mapped either linearly at $9000-$AFFF, or bank-switched in 8 banks of 1 KiB each at $EC00-$EFFF.

It seems that in the demo programs, the BASIC versions like to POKE in 60200, where the machine language programs use $EB00.

```
60200 Port A or DDR A           $EB28
$EBx0 1  0 \
      2  1 - RAM block at $EC00 (0-7)
      4  2 /
      8  3 charrom         0 = off 1 = on
      16 4 hires           0 = on  1 = off
      32 5 extra charrom   0 = on  1 = off

60201 Control Register A: bit #3 (worth 4) controls if 60200
$EBx1 accesses the Data Direction Register A (0) or Port A (1).

60202 Port B or DDR B
$EBx2 0 = RAM is visible from $9000 - $AFFF
      1 = RAM is bank-switched in blocks of 1 K in $EC00 - $EFFF
```

[1] Dubbel-W bord, designed by Ben de Winter and Pieter Wolvekamp

```
[Control Register B is never mentioned, so putting 1 in this
 address would access the DDR, creating an output line, which
 after RESET is default 0...]
```

Typical initialisation sequence:

```
poke 60201,0        poke 60200,255          (all outputs)
poke 60201,4        poke 60200,24 or 25 (16 + 8 + 1)
```

Demo programs on disk PBE-110A, 110B, 111A, and 111B. (PBE = PET Benelux Exchange, the Dutch PET user group)

The memory mapping is a bit strange. It seems each 1 K block contains the pixeldata for 1 bit-line of each text line. This is probably so that the addressing of the RAM can borrow part of the addressing logic/signals of the text screen. (The screen addressing cycles through 0-39, then increases the line (= byte offset) which is fetched from the character ROM; for the graphics, the screen position selects the byte in a KiB and the char ROM offset selects which KiB of graphics RAM).

My notes say: to set a pixel:

```
RE = INT(Y/8): LY = Y - 8*RE    (or Y AND 7)
BY = INT(X/8): BI = X - 8*BY    (or X AND 7)
```

when memory mapped to $9000:

```
L = 36864 + 1024 * LY + 40 * RE + BY
POKE L, PEEK(L) OR 2^BI
```

when memory mapped to $EC00:

```
POKE 60200,LY + 40 (or 8?)
L = 60416 + RE * 40 + BY
POKE L, PEEK(L) OR 2^BI
```

Unfortunately there is no logical means of expanding the memory to 16 K, so even in an 80 columns PET the resolution will be the same.

7.7.10 HRE high resolution graphics

This information comes from Michal Pleban, `http://www.cbm-ii.com`.

This is a short information about the HRE (HiRes Emulator) graphics board for PET 8296 (ASSY No. 324890-01). This is a small board that is installed on the mainboard into sockets for the CRTC and character ROM. It adds the ability to display 512x256 graphics on the built-in monitor.

The board is accompanied with two extension ROMs:

- 324992-02 HiRes Emulator (at $9000)
- 324993-02 HiRes BASIC (at $A000)

The ROMs are initialized by the command `SYS 36864`.

After that, the computer recognizes additional BASIC commands like DISPLAY, PLOT and so on; these commands are identical to those of the HiRes Graphics boards based on Thomson chips. All BASIC programs written for the HRG boards should also work on the HRE (albeit much slower).

Upon initialization, the ROM code writes some routines into expansion RAM as $8800 - they are used to manipulate the bitmap data. The actual bitmap is located in expansion

RAM at addresses $A000-$DFFF, and is organized linearly (first 64 bytes form the first display line, then comes the next line and so on).

The RAM is accessed for reading by writing the value #$83 into $E888. This is a register in the CRTC memory space that is intercepted by the board and serves as a latch to drive jumpers on the 8296D mainboard. Because the ROMs are banked out this way, all video memory manipulation must happen with interrupts disabled. Normal ROM operation is restored by writing #$0F into $E888.

As usual with the 8296, the RAM can also be written even when the ROMs are mapped normally.

The board is initialized into graphics mode by modifying the following values in CRTC registers (it is done by the routine located at $8C1A):

- Register $01 (horizontal displayed) = #$20
- Register $02 (horizontal sync position) = original + #$02
- Register $06 (vertical displayed) = #$20
- Register $07 (vertical sync position) = original + #$04
- Register $0C (RAM address high) = #$02

The last value causes the board to switch into graphics mode; former values set up proper screen dimensions for 512x256 display.

7.7.10.1 The meaning of the bits in $E888

On zimmers.net is a 1-page document 324890-01_manual.pdf which reads (my translation from German):

The 8296 HIRES Graphics is an emulation of the 512*256 Commodore High Speed Graphics, which uses the built-in RAM and 6502 microprocessor.

A hardware addition is plugged into the socket of the CRTC (UC9) and the character ROM (UC5).

The software for emulating the High Speed Graphics is contained in a 4 KB EPROM ($9000, UE10), and a BASIC-extension to use the graphics is in a further 4K EPROM ($A000, UE9).

The "hidden" RAM behind the EPROM in UE9 and the BASIC ROM is used as screen memory.

On the extension board there is a 4-fold DIL switch (1 to 4), with which one can set jumpers JU3, JU4, JU7, JU6 by hardware.

By writing a latch at $E888 (decimal 59582), the jumpers JU3...JU7 are controlled by software (independently of the DIL-settings).

Bit	Value	Jumper/Signal	Note
0	1	JU4 /RAMSEL9	1)
1	2	JU3 /RAMSELA	1)
2	4	JU5 /RAMON	1)
3	8	—	
4	16	JU7	2)
5	32	JU6	2)

6	64	—	
7	128	LATCHON	3)

1. On the original motherboard the signals /RAMSELA, /RAMSEL9 and /RAMON can be controlled by PA0, PA1 and PA3 of the user port, if jumpers JU3, JU4 and JU5 are closed. On the adapter, "off" of the DIL-switch means for 1 and 2: RAMSELA and RAMSEL9 : high; for 3 and 4: Jumper J6 and J7: placed (closed)
2. high = jumper placed
3. high = Latch On

(unfortunately the document doesn't say what "Latch On" does, but I guess that if it is off, the `/RAM*` signals are set by the DIL switches.)

Here is a quick reminder of the meaning of the 8296's jumpers:

- JU1 set /RAMSELA to GND (do not use JU1/JU3 together)
- JU2 set /RAMSEL9 to GND (do not use JU2/JU4 together)
- JU3 set /RAMSELA to Userport PA0 (do not use JU1/JU3 together)
- JU4 set /RAMSEL9 to Userport PA1 (do not use JU2/JU4 together)
- JU5 set /RAMON to Userport PA2
- JU6 set J4 expansion port pin /SEL EXP to /CSA ($A*** ROM)
- JU7 set J4 expansion port pin /SEL EXP to /CS9 ($9*** ROM)
- JU8/JU9 set JU8, unset JU9: do not use video MA12 for RAM addressing; unset JU8, set JU9: use video MA12 for RAM addressing.

7.8 CBM-II-specific commands and settings

This section lists the settings and commands that are CBM-II-specific and thus are not present in the other emulators.

7.8.1 Changing CBM-II model

With `xcbm2` and `xcbm5x0`, it is possible to change at runtime the characteristics of the emulated CBM so that it matches (or not) the ones of a certain CBM model, and it is also possible to select from a common set of CBM models so that all the features are selected accordingly.

The former is done by changing the following resources (via resource file, command line options or right-menu items):

`RamSize` Size of memory in KiB. xcbm2: (128, 256, 512, 1024) xcbm5x0: (64, 128, 256, 512, 1024)

`Ram08` Boolean specifying whether the RAM should be mapped to the expansion ROM area at $0800-$0FFF or not.

`Ram1` Boolean specifying whether the RAM should be mapped to the expansion ROM area at $1000-$1FFF or not.

`Ram2` Boolean specifying whether the RAM should be mapped to the expansion ROM area at $2000-$3FFF or not.

`Ram4` Boolean specifying whether the RAM should be mapped to the expansion ROM area at $4000-$5FFF or not.

`Ram6` Boolean specifying whether the RAM should be mapped to the expansion ROM area at $6000-$7FFF or not.

`RamC` Boolean specifying whether the RAM should be mapped to the expansion ROM area at $C000-$CFFF or not.

`CartridgeReset`
Boolean specifying whether the machine should be reset when a cartridge is changed.

`Cart1Name`
String specifying the filename for the $1000-$1FFF expansion ROM file. This file contains a 4KiB ROM dump.

`Cart2Name`
String specifying the filename for the $2000-$3FFF expansion ROM file. This file contains an 8KiB ROM dump.

`Cart4Name`
String specifying the filename for the $4000-$5FFF expansion ROM file. This file contains an 8KiB ROM dump.

`Cart6Name`
String specifying the filename for the $6000-$7FFF expansion ROM file. This file contains an 8KiB ROM dump.

`ModelLine`
The CBM-II business models have two hardcoded lines at one of the I/O ports. From those lines the kernal determines how it should init the CRTC video chip for either 50Hz (Europe) or 60Hz (North America), and either for 8 (C6x0) or 14 (C7x0) scanlines per character. xcbm2: (0: CBM 7x0 (50Hz), 1: 60Hz C6x0, 2: 50Hz C6x0) xcbm5x0: (1: 60Hz C5x0, 2: 50Hz C5x0)

`ChargenName`
String specifying the name of the character generator ROM (default for xcbm2: `chargen.600`, default for xcbm5x0: `chargen.500`).

`KernalName`
String specifying the name of the Kernal ROM (default for xcbm2: `kernal`, default for xcbm5x0: `kernal.500`).

`BasicName`
String specifying the name of the Basic ROM (default for xcbm2: `basic.128`, default for xcbm5x0: `basic.500`).

`MachineVideoStandard`
Integer that specifies the video standard of the emulated machine (1: PAL, 2: NTSC).

`MachinePowerFrequency`
Integer that specifies the frequency of the power grid in Hz (50, 60). This is used to determine the tick frequency for the TOD clocks.

Choosing a common CBM-II model is done from the right-button menu instead, by choosing an item from the "Model defaults" submenu. Available models are:

- C510 PAL or NTSC (128KiB RAM)
- C610 PAL or NTSC (128KiB RAM)
- C620 (256KiB RAM)
- C620+ (1024KiB RAM, expanded) PAL or NTSC
- C710 (128KiB RAM) NTSC
- C720 (256KiB RAM) NTSC
- C720+ (1024KiB RAM, expanded) NTSC

Notice that this will **reset the emulated machine**.

Warning: At this time the 5x0 and other machines are implemented in different executables, so switching between those models is not possible.

It is also possible to select the CBM model at startup, with the `-model` command-line option: for example, '`xcbm2 -model 610`' will emulate a CBM 610 while '`xcbm2 -model 620`' will emulate a CBM 620.

7.8.2 CBM-II system ROM settings

7.8.2.1 CBM-II system ROM resources

7.8.2.2 CBM-II system ROM command line options

`-kernal <name>`
: Specify the name of the Kernal ROM file (`KernalName`).

`-basic <name>`
: Specify the name of the Basic ROM file (`BasicName`).

`-chargen <name>`
: Specify the filename for the character generator ROM file (`ChargenName`).

`+cart`
: Disable all cartridges (which would eventually be enabled in the config file).

`-cartreset`
`+cartreset`
: Reset/do not reset machine if a cartridge is attached or detached (`CartridgeReset=1`, `CartridgeReset=0`).

`-cart1 <name>`
: Specify the filename for the ROM image file for the cartridge area $1000-$1FFF (`Cart1Name`).

`-cart2 <name>`
: Specify the filename for the ROM image file for the cartridge area $2000-$2FFF (`Cart2Name`).

`-cart4 <name>`
: Specify the filename for the ROM image file for the cartridge area $4000-$5FFF (`Cart4Name`).

`-cart6 <name>`
: Specify the filename for the ROM image file for the cartridge area $6000-$7FFF (`Cart6Name`).

7.8.3 CBM-II command line options

These are the commandline options specific for the CBM-II models.

`-pal` Use PAL sync factor (`MachineVideoStandard=1`).

`-ntsc` Use NTSC sync factor (`MachineVideoStandard=2`).

`-power50` Use 50Hz power grid frequency. (`MachinePowerFrequency=50`).

`-power60` Use 60Hz power grid frequency. (`MachinePowerFrequency=60`).

`-ramsize <ramsize>`
: Specify size of RAM (`RamSize`). xcbm2: (128, 256, 512, 1024) xcbm5x0: (64, 128, 256, 512, 1024)

`-ram08`
`+ram08` Enable/disable RAM mapping in bank 15 at address $0800-$0FFF (`Ram08=1`, `Ram08=0`).

`-ram1`
`+ram1` Enable/disable RAM mapping in bank 15 at address $1000-$1FFF (`Ram1=1`, `Ram1=0`).

`-ram2`
`+ram2` Enable/disable RAM mapping in bank 15 at address $2000-$3FFF (`Ram2=1`, `Ram2=0`).

`-ram4`
`+ram4` Enable/disable RAM mapping in bank 15 at address $4000-$5FFF (`Ram4=1`, `Ram4=0`).

`-ram6`
`+ram6` Enable/disable RAM mapping in bank 15 at address $6000-$7FFF (`Ram6=1`, `Ram6=0`).

`-ramC`
`+ramC` Enable/disable RAM mapping in bank 15 at address $C000-$CFFF (`RamC=1`, `RamC=0`).

`-model <modelnumber>`
: Specify CBM-II model to emulate (`ModelLine`, `MachineVideoStandard`, `MachinePowerFrequency`, `RamSize`, `KernalName`, `BasicName`, `ChargenName`). xcbm2: (610, 620, 620+, 710, 720, 720+) xcbm5x0: (510)

7.8.4 Changing screen colors

It is also possible to choose what color set is used for the emulation window. This is done by specifying a palette file name (see Section 3.4 [Palette files], page 40) in the `PaletteName` resource. The menu provides the following values:

- `green.vpl` (default, "green"), the good old green-on-black feeling;

- `amber.vpl` ("amber"), an amber phosphor lookalike;
- `white.vpl` ("white"), simple white-on-black palette.

7.9 VSID-specific commands and settings

7.9.1 VSID settings

7.9.1.1 VSID resources

`PSIDKeepEnv`
: Boolean that specifies whether to override PSID settings for Video standard and SID model.

`PSIDTune`
: Integer that specifies the currently played sub tune.

`HVSCRoot`
: String specifying the location of the HVSC root directory, overriding the environment variable `HVSC_BASE`.

`ChargenName`
: String specifying the name of the character generator ROM (default `chargen`).

`BasicName`
: String specifying the name of the Basic ROM (default `basic`).

`KernalName`
: String specifying the name of the Kernal ROM (default `kernal-901227-03.bin`).

`KernalRev`
: String specifying the Kernal revision. This resource can be used to control what revision of the C64 kernal is being used; Changing this resource equals loading one of the provided kernal ROM files. It will be adjusted when changing `KernalName`.

  Valid values are:

  `-1` Unknown kernal. This value can not be set by the user, it will be set when loading a custom kernal via changing `KernalName`.

  `-2` No kernal. This is used by the MAX Machine.

  `0` Kernal revision 0 (Japanese, 906145-02)

  `1` Kernal revision 1 (901227-01)

  `2` Kernal revision 2 (901227-02)

  `3` Kernal revision 3 (901227-03)

  `39` C64 GS (390852-01)

  `67` SX-64 (251104-04)

  `100` Commodore 4064 (also known as "PET64" or "Educator 64") (901246-01)

`MachineVideoStandard`
: Integer that specifies the video standard of the emulated machine (1: PAL, 2: NTSC, 3: Old NTSC, 4: PAL-N).

`MachinePowerFrequency`
: Integer that specifies the frequency of the power grid in Hz (50, 60). This is used to determine the tick frequency for the TOD clocks.

7.9.1.2 VSID command-line options

`-keepenv` Override PSID settings for Video standard and SID model (`PSIDKeepEnv=1`).

`-tune <number>`
: Specify PSID tune <number> (`PSIDTune`).

`-hvsc-root <path>`
: Specify the location of the HVSC root directory, overriding the environment variable `HVSC_BASE`.

(`HVSCRoot`).

`-chargen <name>`
: Specify name of character generator ROM image (`ChargenName`).

`-kernal <name>`
: Specify name of the Kernal ROM image (`KernalName`).

`-kernalrev <revision>`
: Specify Kernal revision (`KernalRev`). (0/jap: japanese 1: rev. 1, 2: rev. 2, 3: rev. 3, 39/gs: C64 GS, 67/sx: sx64, 100/4064: 4064)

`-basic <name>`
: Specify name of the Basic ROM image (`BasicName`).

`-pal` Use PAL sync factor (`MachineVideoStandard=1`).

`-ntsc` Use NTSC sync factor (`MachineVideoStandard=2`).

`-ntscold` Use old NTSC sync factor (`MachineVideoStandard=3`).

`-paln` Use PAL-N sync factor (`MachineVideoStandard=4`).

`-power50` Use 50Hz power grid frequency. (`MachinePowerFrequency=50`).

`-power60` Use 60Hz power grid frequency. (`MachinePowerFrequency=60`).

8 Platform-specific features

This section lists the settings and commands that are platform specificm, ie which control the behaviour and appearance of the host user interface.

8.1 Common features

8.1.1 Common command line options

`-windowxpos <xpos>`
: Set initial horizontal window position. (`Window0Xpos`).

`-windowypos <ypos>`
: Set initial vertical window position. (`Window0Ypos`).

`-windowxpos1 <xpos>`
: Set initial horizontal window position for the second window (C128). (`Window1Xpos`).

`-windowypos1 <ypos>`
: Set initial vertical window position for the second window (C128). (`Window1Ypos`).

`-windowwidth <width>`
: Set initial window width. (`Window0Width`).

`-windowheight <height>`
: Set initial window height. (`Window0Height`).

`-windowwidth1 <width>`
: Set initial window width for the second window (C128), (`Window1Width`).

`-windowheight1 <height>`
: Set initial window height for the second window (C128), (`Window1Height`).

`-saveres`
`+saveres`
: Enable/disable automatic saving of settings on exit (`SaveResourcesOnExit=1`, `SaveResourcesOnExit=0`).

`-confirmonexit`
: Confirm quitting VICE (`ConfirmOnExit=1`).

`+confirmonexit`
: Never confirm quitting VICE (`ConfirmOnExit=0`).

`-kbdstatusbar`
`+kbdstatusbar`
: Enable/Disable keyboard-status bar (when status bar is enabled) (`KbdStatusbar=1`, `KbdStatusbar=0`).

`-minimized`
`+minimized`
: Start/Do not start with minimized window (`StartMinimized=1`, `StartMinimized=0`).

`-maximized`
`+maximized`
: Start/Do not start with maximized window (`StartMaximized=1`, `StartMaximized=0`).

`-jamaction <Type>`
: Specify the action to take when the CPU encounters a 'JAM' opcode (`JAMAction`) (0: Show dialog, 1: Continue emulation, 2: Start monitor, 3: Reset, 4: Power cycle, 5: Quit emulator).

`-directory <Path>`
: Specify the system file search path (`Directory`).

`-quicksaveformat <Format>`
: Specify the format of the quicksave screenshot (`QuicksaveScreenshotFormat`) (png, gif ,bmp, iff, pcx, ppm, 4bt, artstudio, koala, minipaint).

8.1.2 Common resources

`Window0Xpos`
`Window0Ypos`
: Integers specifying the position of the (first) emulator window (all emulators except vsid).

`Window1Xpos`
`Window1Ypos`
: Integers specifying the position of the (second) emulator window (all emulators except vsid).

`Window0Width`
`Window0Height`
: Integers specifying the size of the (first) emulator window (all emulators except vsid).

`Window1Width`
`Window1Height`
: Integers specifying the size of the (second) emulator window (all emulators except vsid).

`DoCoreDump`
: Boolean specifying whether the emulator should dump core when it gets a signal (all emulators except vsid).

`LogFileName`
: String specifying the filename of the current log file.

`LogLimit` Integer specifying the "verbosity" of the log output. Higher numbers mean more verbosity (0: no log, 255: all messages)

`LogToFile`
: Boolean that specifies whether the log should go to the log file.

`LogToStdout`
: Boolean that specifies whether the log should go stdout (usually the launching terminal).

`LogToMonitor`
: Boolean that specifies whether the log should go to the monitor console.

`LogColorize`
: Boolean that specifies whether the log in the terminal should be colorized.

`ExitScreenshotName`
: String specifying the filename of a screenshot file that will be written when the emulator exits.

`ExitScreenshotName1`
: String specifying the filename of a screenshot file that will be written when the emulator exits. (x128)

`QuicksaveScreenshotFormat`
: String specifying the format of the quicksave screenshot (png, gif ,bmp, iff, pcx, ppm, 4bt, artstudio, koala, minipaint)

`SaveResourcesOnExit`
: Boolean specifying whether the emulator should save changed settings before exiting. If this is enabled, the user will be always prompted first, in case the settings have changed.

`ConfirmOnExit`
: Boolean specifying whether to show a confirmation dialog on exit.

`KbdStatusbar`
: Boolean specifying whether the keyboard-status bar is enabled.

`StartMinimized`
: Boolean specifying whether the emulator should start minimized

`StartMaximized`
: Boolean specifying whether the emulator should start maximized

`JAMAction`
: Integer specifying the action to take when the CPU encounters a 'JAM' opcode. (0: Show dialog, 1: Continue emulation, 2: Start monitor, 3: Reset, 4: Power cycle, 5: Quit emulator)

`Directory`
: String specifying the search path for system files. It is defined as a sequence of directory names, separated by colons (‘`:`’), just like the `PATH` variable in the shell. The special string ‘`$$`’ stands for the default search path.

8.2 SDL-specific features

This section lists the settings and commands that are common and specific to SDL, and are thus not present in other platforms.

8.2.1 SDL specific resources

`HotkeyFile`
: String specifying the name of the hotkey file (all emulators except vsid).

`MenuKey` Integer specifying the keycode for activating the SDL menu.

`MenuKeyUp`
Integer specifying the keycode for 'up' in the SDL menu.

`MenuKeyDown`
Integer specifying the keycode for 'down' in the SDL menu.

`MenuKeyLeft`
Integer specifying the keycode for 'left' in the SDL menu.

`MenuKeyRight`
Integer specifying the keycode for 'right' in the SDL menu.

`MenuKeyPageUp`
Integer specifying the keycode for 'page up' in the SDL menu.

`MenuKeyPageDown`
Integer specifying the keycode for 'page down' in the SDL menu.

`MenuKeyHome`
Integer specifying the keycode for 'home' in the SDL menu.

`MenuKeyEnd`
Integer specifying the keycode for 'end' in the SDL menu.

`MenuKeySelect`
Integer specifying the keycode for 'select' in the SDL menu.

`MenuKeyCancel`
Integer specifying the keycode for 'cancel' in the SDL menu.

`MenuKeyExit`
Integer specifying the keycode for 'exit' in the SDL menu.

`MenuKeyMap`
Integer specifying the keycode for 'map' in the SDL menu.

`SDLBitdepth`
Integer specifying the bitdepth used. (0: current, 8, 15, 16, 24, 32)

`SDLLimitMode`
Integer specifying the resolution limit mode. (0: off, 1: max, 2: fixed)

`SDL2Backend`
String specifying the preferred SDL2 backend. (See startup log for available backends, valid ones might be eg: software, opengl, direct3d, direct3d11, opengles2)

`CrtcFullscreenMode`
Integer specifying the fullscreen mode (xcbm2 and xpet only). (0: use desktop resolution, 1: switch to `CrtcFullscreenCustomWidth`, `CrtcFullscreenCustomHeight`)

`CrtcFullscreenCustomWidth`
Integer specifying the custom fullscreen resolution width.

`CrtcFullscreenCustomHeight`
: Integer specifying the custom fullscreen resolution height.

`TEDFullscreenMode`
: Integer specifying the fullscreen mode (xplus4 only). (0: use desktop resolution, 1: switch to `TEDFullscreenCustomWidth`, `TEDFullscreenCustomHeight`)

`TEDFullscreenCustomWidth`
: Integer specifying the custom fullscreen resolution width.

`TEDFullscreenCustomHeight`
: Integer specifying the custom fullscreen resolution height.

`VDCFullscreenMode`
: Integer specifying the fullscreen mode (x128 only). (0: use desktop resolution, 1: switch to `VDCFullscreenCustomWidth`, `VDCFullscreenCustomHeight`)

`VDCFullscreenCustomWidth`
: Integer specifying the custom fullscreen resolution width.

`VDCFullscreenCustomHeight`
: Integer specifying the custom fullscreen resolution height.

`VICFullscreenMode`
: Integer specifying the fullscreen mode (xvic only). (0: use desktop resolution, 1: switch to `VICFullscreenCustomWidth`, `VICFullscreenCustomWidth`)

`VICFullscreenCustomWidth`
: Integer specifying the custom fullscreen resolution width.

`VICFullscreenCustomHeight`
: Integer specifying the custom fullscreen resolution height.

`VICIIFullscreenMode`
: Integer specifying the fullscreen mode (all emulators except xcbm2, xpet, xplus4, xvic and vsid). (0: use desktop resolution, 1: switch to `VICIIFullscreenCustomWidth`, `VICIIFullscreenCustomHeight`)

`VICIIFullscreenCustomWidth`
: Integer specifying the custom fullscreen resolution width.

`VICIIFullscreenCustomHeight`
: Integer specifying the custom fullscreen resolution height.

`JoyDevice1`
: Integer specifying which joystick device the emulator should use for the emulation of joystick 1 (all emulators except xcbm2, xpet and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Joystick (only if joystick support was present in the compile time SDL library))

`JoyDevice2`
: Integer specifying which joystick device the emulator should use for the emulation of joystick 2 (all emulators except xcbm2, xpet, xvic and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Joystick (only if joystick support was present in the compile time SDL library))

`JoyDevice3`
: Integer specifying which joystick device the emulator should use for the emulation of extra joystick 1 (all emulators except xcbm5x0 and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Joystick (only if joystick support was present in the compile time SDL library))

`JoyDevice4`
: Integer specifying which joystick device the emulator should use for the emulation of extra joystick 2 (all emulators except xcbm5x0, xplus4 and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Joystick (only if joystick support was present in the compile time SDL library))

The following resources are only present if the SDL library has joystick support at compile time and applies to all emulators except vsid.

`JoyMapFile`
: String specifying the name of the joystick map file.

`JoyThreshold`
: Integer specifying the joystick threshold. (0..32767)

`JoyFuzz` Integer specifying the joystick fuzz. (0..32767)

`JoyMenuControl`
: Boolean to enable/disable controlling the SDL menu using a joystick.

8.2.2 SDL specific command-line options

`-hotkeyfile <name>`
: Set the hotkey file name (`HotkeyFile`) (all emulators except vsid).

`-menukey <key>`
: Set the keycode of the SDL menu activation key (`MenuKey`).

`-menukeyup <key>`
: Set the keycode of the 'up' key in the SDL menu (`MenuKeyUp`).

`-menukeydown <key>`
: Set the keycode of the 'down' key in the SDL menu (`MenuKeyDown`).

`-menukeyleft <key>`
: Set the keycode of the 'left' key in the SDL menu (`MenuKeyLeft`).

`-menukeyright <key>`
: Set the keycode of the 'right' key in the SDL menu (`MenuKeyRight`).

`-menukeypageup <key>`
: Set the keycode of the 'page up' key in the SDL menu (`MenuKeyPageUp`).

`-menukeypagedown <key>`
: Set the keycode of the 'page down' key in the SDL menu (`MenuKeyPageDown`).

`-menukeyhome <key>`
: Set the keycode of the 'home' key in the SDL menu (`MenuKeyHome`).

`-menukeyend <key>`
: Set the keycode of the 'end' key in the SDL menu (`MenuKeyEnd`).

`-menukeyselect <key>`
: Set the keycode of the 'select' key in the SDL menu (`MenuKeySelect`).

`-menukeycancel <key>`
: Set the keycode of the 'cancel' key in the SDL menu (`MenuKeyCancel`).

`-menukeyexit <key>`
: Set the keycode of the 'exit' key in the SDL menu (`MenuKeyExit`).

`-menukeymap <key>`
: Set the keycode of the 'map' key in the SDL menu (`MenuKeyMap`).

`-sdlbitdepth <bpp>`
: Set the display bitdepth (`SDLBitdepth`). (0: current, 8, 15, 16, 24, 32)

`-sdllimitmode <mode>`
: Set the resolution limit mode (`SDLLimitMode`). (0: off, 1: max, 2: fixed)

`-sdl2backend <renderer name>`
: Set the preferred SDL2 backend (`SDL2Backend`). See startup log for available backends, valid ones might be eg: software, opengl, direct3d, direct3d11, opengles2)

`-CRTCfullmode <Mode>`
: Set the fullscreen mode (`CrtcFullscreenMode`) (xcbm2 and xpet only). (0: use desktop resolution, 1: switch to `CrtcFullscreenCustomWidth`, `CrtcFullscreenCustomHeight`)

`-Crtcfullwidth <width>`
: Set the custom fullscreen resolution width (`CrtcFullscreenCustomWidth`).

`-Crtcfullheight <height>`
: Set the custom fullscreen resolution height (`CrtcFullscreenCustomHeight`).

`-TEDfullmode <Mode>`
: Set the fullscreen mode (`TEDFullscreenMode`) (xplus4 only). (0: use desktop resolution, 1: switch to `TEDFullscreenCustomWidth`, `TEDFullscreenCustomHeight`)

`-TEDfullwidth <width>`
: Set the custom fullscreen resolution width (`TEDFullscreenCustomWidth`).

`-TEDfullheight <height>`
: Set the custom fullscreen resolution height (`TEDFullscreenCustomHeight`).

`-VDCfullmode <Mode>`
: Set the fullscreen mode (`VDCFullscreenMode`) (x128 only). (0: use desktop resolution, 1: switch to `VDCFullscreenCustomWidth`, `VDCFullscreenCustomHeight`)

`-VDCfullwidth <width>`
: Set the custom fullscreen resolution width (`VDCFullscreenCustomWidth`).

`-VDCfullheight <height>`
: Set the custom fullscreen resolution height (`VDCFullscreenCustomHeight`).

`-VICfullmode <Mode>`
: Set the fullscreen mode (`VICFullscreenMode`) (xvic only). (0: use desktop resolution, 1: switch to `VICFullscreenCustomWidth`, `VICFullscreenCustomHeight`)

`-VICfullwidth <width>`
: Set the custom fullscreen resolution width (`VICFullscreenCustomWidth`).

`-VICfullheight <height>`
: Set the custom fullscreen resolution height (`VICFullscreenCustomHeight`).

`-VICIIfullmode <Mode>`
: Set the fullscreen mode (`VICIIFullscreenMode`) (all emulators except xcbm2, xpet, xplus4, xvic and vsid). (0: use desktop resolution, 1: switch to `VICIIFullscreenCustomWidth`, `VICIIFullscreenCustomHeight`)

`-VICIIfullwidth <width>`
: Set the custom fullscreen resolution width (`VICIIFullscreenCustomWidth`).

`-VICIIfullheight <height>`
: Set the custom fullscreen resolution height (`VICIIFullscreenCustomHeight`).

`-joydev1 <0-3> / <0-4>`
: Set the device for joystick emulation of port 1 (`JoyDevice1`) (all emulators except xcbm2, xpet and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Joystick (only if joystick support was present in the compile time SDL library))

`-joydev2 <0-3> / <0-4>`
: Set the device for joystick emulation of port 2 (`JoyDevice2`) (all emulators except xcbm2, xpet, xvic and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Joystick (only if joystick support was present in the compile time SDL library))

`-extrajoydev1 <0-3> / <0-4>`
: Set device for extra joystick port 1 (`JoyDevice3`) (all emulators except xcbm5x0 and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Joystick (only if joystick support was present in the compile time SDL library))

`-extrajoydev2 <0-3> / <0-4>`
: Set device for extra joystick port 2 (`JoyDevice4`) (all emulators except xcbm5x0, xplus4 and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Joystick (only if joystick support was present in the compile time SDL library))

The following command-line options are only present if the SDL library has joystick support at compile time and applies to all emulators except vsid.

`-joymap <name>`
: Set the joystick map file name (`JoyMapFile`).

`-joythreshold <0-32767>`
: Set the joystick threshold (`JoyThreshold`).

`-joyfuzz <0-32767>`
: Set the joystick fuzz (`JoyFuzz`).

`-joymenucontrol`
`+joymenucontrol`
: Enable/disable controlling the SDL menu with a joystick (`JoyMenuControl=1`, `JoyMenuControl=0`).

8.3 GTK3 specific features

This section lists the settings and commands that are common and specific to Unix, and are thus not present in other platforms.

8.3.1 GTK3 specific resources

`FullscreenDecorations`
: Boolean specifying whether the menu and statusbar are shown in full screen mode.

`PauseOnSettings`
: Boolean specifying wheter to pause the emulation when triggering the settings dialog. If set, the emulation will pause when triggering the settings dialog. When closing the settings dialog the pause state will revert to its state before triggering the settings UI.

`JoyDevice1`
: Integer specifying which joystick device the emulator should use for the emulation of joystick 1 (all emulators except xcbm2, xpet and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Analog joystick 1, 5: Analog joystick 2, 6: Analog joystick 3, 7: Analog joystick 4, 8: Analog joystick 5, 9: Analog joystick 6, 10: Digital joystick 1, 11: Digital joystick 2, 12: USB joystick 1, 13: USB joystick 2)

`JoyDevice2`
: Integer specifying which joystick device the emulator should use for the emulation of joystick 2 (all emulators except xcbm2, xpet, xvic and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Analog joystick 1, 5: Analog joystick 2, 6: Analog joystick 3, 7: Analog joystick 4, 8: Analog joystick 5, 9: Analog joystick 6, 10: Digital joystick 1, 11: Digital joystick 2, 12: USB joystick 1, 13: USB joystick 2)

`JoyDevice3`
: Integer specifying which joystick device the emulator should use for the emulation of extra joystick 1 (all emulators except xcbm5x0 and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Analog joystick 1, 5: Analog joystick 2, 6: Analog joystick 3, 7: Analog joystick 4, 8: Analog joystick 5, 9: Analog joystick 6, 10: Digital joystick 1, 11: Digital joystick 2, 12: USB joystick 1, 13: USB joystick 2)

`JoyDevice4`
: Integer specifying which joystick device the emulator should use for the emulation of extra joystick 2 (all emulators except xcbm5x0, xplus4 and vsid). (0:

None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Analog joystick 1, 5: Analog joystick 2, 6: Analog joystick 3, 7: Analog joystick 4, 8: Analog joystick 5, 9: Analog joystick 6, 10: Digital joystick 1, 11: Digital joystick 2, 12: USB joystick 1, 13: USB joystick 2)

The available joysticks might differ depending on operating system and joystick support in the OS.

`Devices 4..9`
Only available if joystick support is available at compile time.

`Devices 10 and 11`
Only available if digital joystick support is available at compile time.

`Devices 12 and 13`
Only available if USB joystick support is available at compile time.

The following resources are only available if MIDI support is available at compile time.

`MIDIName` String specifying the name of the MIDI client (default:"VICE") (ALSA and macOS only, x64, x64sc, xscpu64, x128 and xvic only).

`MIDIInDev`
String specifying the OSS MIDI input device (Windows and OSS, x64, x64sc, xscpu64, x128 and xvic only).

`MIDIOutDev`
String specifying the OSS MIDI output device (Windows and OSS, x64, x64sc, xscpu64, x128 and xvic only).

`MIDIInName`
String specifying the name of the MIDI input port (default:"VICEInput") (macOS only, x64, x64sc, xscpu64, x128 and xvic only).

`MIDIOutName`
String specifying the name of the MIDI output port (default:"VICEOutput") (macOS only, x64, x64sc, xscpu64, x128 and xvic only).

The following resource is only available if MIDI support and ALSA support is available at compile time.

`MIDIDriver`
Enum specifying the MIDI driver (Unix - x64, x64sc, xscpu64, x128 and xvic only). (0: OSS, 1: ALSA)

8.3.2 GTK3 specific command-line options

`-fullscreen-decorations`
`+fullscreen-decorations`
Enable/disable fullscreen decorations

`-pauseonsettings`
Pause emulation when enabling the settings dialog. (`PauseOnSettings=1`).

`+pauseonsettings`
: Do not pause emulation when enabling the settings dialog. (`PauseOnSettings=0`).

`-joydev1 <0-3> / <0-9> / <0-11> / <0-13>`
: Set the device for joystick emulation of port 1 (`JoyDevice1`) (all emulators except xcbm2, xpet and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Analog joystick 1, 5: Analog joystick 2, 6: Analog joystick 3, 7: Analog joystick 4, 8: Analog joystick 5, 9: Analog joystick 6, 10: Digital joystick 1, 11: Digital joystick 2, 12: USB joystick 1, 13: USB joystick 2)

`-joydev2 <0-3> / <0-9> / <0-11> / <0-13>`
: Set the device for joystick emulation of port 2 (`JoyDevice2`) (all emulators except xcbm2, xpet, xvic and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Analog joystick 1, 5: Analog joystick 2, 6: Analog joystick 3, 7: Analog joystick 4, 8: Analog joystick 5, 9: Analog joystick 6, 10: Digital joystick 1, 11: Digital joystick 2, 12: USB joystick 1, 13: USB joystick 2)

`-extrajoydev1 <0-3> / <0-9> / <0-11> / <0-13>`
: Set device for extra joystick port 1 (`JoyDevice3`) (all emulators except xcbm5x0 and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Analog joystick 1, 5: Analog joystick 2, 6: Analog joystick 3, 7: Analog joystick 4, 8: Analog joystick 5, 9: Analog joystick 6, 10: Digital joystick 1, 11: Digital joystick 2, 12: USB joystick 1, 13: USB joystick 2)

`-extrajoydev2 <0-3> / <0-9> / <0-11> / <0-13>`
: Set device for extra joystick port 2 (`JoyDevice4`) (all emulators except xcbm5x0, xplus4 and vsid). (0: None, 1: Numpad, 2: Keyset 1, 3: Keyset 2, 4: Analog joystick 1, 5: Analog joystick 2, 6: Analog joystick 3, 7: Analog joystick 4, 8: Analog joystick 5, 9: Analog joystick 6, 10: Digital joystick 1, 11: Digital joystick 2, 12: USB joystick 1, 13: USB joystick 2)

The available joysticks might differ depending on operating system and joystick support in the OS.

`Devices 4..9`
: Only available if joystick support is available at compile time.

`Devices 10 and 11`
: Only available if digital joystick support is available at compile time.

`Devices 12 and 13`
: Only available if USB joystick support is available at compile time.

The following command-line options are only available if MIDI support is available at compile time.

`-midiname <name>`
: Specify the name of the MIDI client (default:"VICE") (`MIDIName`) (ALSA and macOS only, x64, x64sc, xscpu64, x128 and xvic only).

`-midiin <name>`
: Specify OSS MIDI-In device (`MIDIInDev`) (Windows and OSS, x64, x64sc, xscpu64, x128 and xvic only).

`-midiout <name>`
: Specify OSS MIDI-Out device (`MIDIOutDev`) (Windows and OSS, x64, x64sc, xscpu64, x128 and xvic only).

`-midiinname <name>`
: Specify the name of the MIDI input port (default:"VICEInput") (`MIDIInName`) (macos, x64, x64sc, xscpu64, x128 and xvic only).

`-midioutname <name>`
: Specify the name of the MIDI output port (default:"VICEOutput") (`MIDIOutName`) (macos, x64, x64sc, xscpu64, x128 and xvic only).

The following command-line option is only available if MIDI support and ALSA support is available at compile time.

`-mididrv <driver>`
: Specify MIDI driver (`MIDIDriver`) (Unix - x64, x64sc, xscpu64, x128 and xvic only). (0: OSS, 1: ALSA)

9 Snapshots

Every VICE emulator has a built-in snapshot feature, that saves the complete emulator state into one file for later use. You can therefore save the emulator state - including the state of the game you are playing for example - in a single file.

Important note: In the past the idea was that snapshots with the same major version can be exchanged with later versions of the emulator. However, this proved to be too difficult to maintain, and insanely hard to test for correctness, which is why now the emulator will reject the snapshot when it finds a module that is too old. **Because of this you should not use snapshots for permanent/long term storage.**

9.1 Snapshot usage

A snapshot is one file containining the complete emulator state. A snapshot file can be generated by selecting the "Save snapshot" command at any time. This will pop up a requester from which you can specify whether the snapshot should also contain the disk and ROM status.

A snapshot file can be used to restore the emulator state by selecting the `load snapshot` menu entry at any time. Unfortunately attached ROM images/cartridges are only supported in the VIC20, the PET and the CBM-II emulators at this time.

The memory configuration of the emulator is saved in the snapshot file as well. This configuration is restored when the snapshot is loaded.

A quick snapshot can now be made by pressing the `M-F11` key and reloaded by pressing the `M-F10` key.

9.2 Snapshot format

A snapshot file consists of several modules of mostly different types. Each module has a name and saves the state of an entity like a CIA, the CPU, or the memory.

9.2.1 Terminology

In this section we use certain abbreviations to define the types of the data saved in the snapshot.

`BYTE` 8 bit integer.

`WORD` 16 bit integer. Saved with low-byte first, high-byte last.

`DWORD` 32 bit integer. Saved with low-word first, then high-word. Each word saved with its low-byte first.

`ARRAY` Array of BYTE values. Length depends on the description.

The tables for the single modules state the type, name and description of the data saved in the modules. The data is saved in the order it is in the tables, so no offset is given.

9.2.2 Module framework

The VICE snapshot file starts with the magic string and includes the fileformat version number.

Type	Name	Description
19 BYTE	MAGIC	"VICE Snapshot File\032", padded with 0
BYTE	VMAJOR	fileformat major version number
BYTE	VMINOR	fileformat minor version number
16 BYTE	MACHINENAME	Name of emulated machine, like "PET", "CBM-II", "VIC20", "C64" or "C128". zerobyte-padded.
13 BYTE	VERSION MAGIC	"VICE Version\032", padded with 0
4 BYTE	VERSION	Release version (major, minor, micro). Byte 4 is always zero.
DWORD	SVNVERSION	SVN revision (or 0 if not available)

The file header is followed by a number of different snapshot modules.

Each module has a header with the information given in the table below. The header includes two version numbers, VMAJOR and VMINOR. In the past the idea was that modules with the same VMAJOR should be able to be exchanged. This however proved to be too difficult to maintain, and insanely hard to test for correctness, which is why now the emulator will reject the snapshot when it finds a module that is too old. **Because of this you should not use snapshots for permanent/long term storage.**

Type	Name	Description
16 BYTE	MODULENAME	The name of the module in ASCII, padded with 0 to 16 byte.
BYTE	VMAJOR	major version number
BYTE	VMINOR	minor version number
DWORD	SIZE	size of the module, including this header

9.2.3 Emulator modules

This section lists the modules that are contained in each of the emulators snapshot files.

9.2.3.1 x64 modules

The modules in the x64 emulator are:

Name	Type	Description
MAINCPU	6502	The Main CPU - although it is a 6510, only the 6502 core is saved here
C64MEM	Memory	Holds the RAM contents of the C64. Also the CPU I/O register contents are saved here.
C64ROM	ROM images	Dump of the system ROMs
VIC-II	656*	The VIC-II of the C64/128
CIA1	6526	The CIA for the interrupts and the keyboard
CIA2	6526	The CIA for the userport, IEC-bus and RS232.

SID	6581	The SID sound chip of the C64/C128
REU*		The RAM Extension Unit state (optional)
ACIA1	6551	An ACIA (RS232 interface) at $DE00 (optional)
TPI	6525	A TPI at $DF00 for a parallel IEEE488 interface (optional)
*	Drive modules	The emulated drive(s) have their own modules see Section 9.2.3.6 [Drive modules], page 242,

Some of the modules are optional and are only saved if the specific feature is enabled at save-time. If the module is found when restoring the state the optional features are enabled, and disabled otherwise.

9.2.3.2 x128 modules

The modules in the x128 emulator are:

Name	Type	Description
MAINCPU	6502	The Main CPU - although it is a 6510, only the 6502 core is saved here
C128MEM	Memory	Holds the RAM contents of the C64. Also the CPU I/O register contents are saved here.
C128ROM	ROM images	Dump of the system ROMs
VIC-II	656*	The VIC-II of the C64/128
CIA1	6526	The CIA for the interrupts and the keyboard
CIA2	6526	The CIA for the userport, IEC-bus and RS232.
SID	6581	The SID sound chip of the C64/C128
ACIA1	6551	An ACIA at $DE00 (optional)
TPI	6525	A TPI at $DF00 for a parallel IEEE488 interface (optional)
*	Drive modules	The emulated drive(s) have their own modules see Section 9.2.3.6 [Drive modules], page 242,

Some of the modules are optional and are only saved if the specific feature is enabled at save-time. If the module is found when restoring the state the optional features are enabled, and disabled otherwise.

Not yet supported are the 80 column video chip, cartridges and RAM expansion unit.

9.2.3.3 xvic modules

The modules in the xvic emulator are:

Name	Type	Description
MAINCPU	6502	The Main CPU
VIC20MEM	Memory	Holds the RAM contents of the VIC20.

VIC20ROM	ROM images	Holds the ROM images of the VIC20, including possibly attached cartridges
VIC-I	656*	The VIC-I of the VIC20
VIA1	6522	The VIA for the interrupts and the keyboard
VIA2	6522	The VIA for the userport, IEC-bus and RS232.
*	Drive modules	The emulated drive(s) have their own modules see Section 9.2.3.6 [Drive modules], page 242,

9.2.3.4 xpet modules

The modules in the xpet emulator are:

Name	Type	Description
MAINCPU	6502	The Main CPU
PETMEM	Memory	Holds the RAM contents of the PET.
PETROM	ROM images	Holds the ROM images of the PET, including possibly attached cartridges
CRTC	6545	The CRTC of the PET. This is also included if it is a dump of a PET without CRTC, because the video state is saved here anyway.
PIA1	6520	The PIA for the interrupts, tape and the keyboard
PIA2	6520	The PIA for the IEEE488-bus
VIA	6522	The VIA for IEEE488, userport, sound
ACIA1	6551	The ACIA for the SuperPET. This module is optional.
DWWPIA	6520	The PIA for the DWW hires board.
CPU6809	6809	The extra CPU in the SuperPET. This module is optional.
*	Drive modules	The emulated drive(s) have their own modules see Section 9.2.3.6 [Drive modules], page 242,

9.2.3.5 xcbm2 and xcbm5x0 modules

The modules in the xcbm2 and xcbm5x0 emulators are:

Name	Type	Description
MAINCPU	6502	The Main CPU - although it is a 6509, only the 6502 core is saved here
CBM2MEM	Memory	Holds the RAM contents of the CBM-II models. Also holds the exec-bank and indirection bank registers

C500DATA		Holds additional state information necessary for the C500 (e.g. cycles till the next IRQ)
CBM2ROM	Memory	optional. Holds the ROM images.
CRTC	6545	The video chip for the C6*0 and C7*0 models (only those models).
VIC-II	656?	The video chip for the C5*0 models (only the C5*0 models).
CIA1	6526	The CIA for IEEE 488 and userport.
TPI1	6525	TPI 1 for IEEE488
TPI2	6525	TPI 2 for interrupts and keyboard.
ACIA1	6551	The RS232 interface
SID	6581	The CBM2s SID sound chip
*	Drive modules	The emulated drive(s) have their own modules see Section 9.2.3.6 [Drive modules], page 242,

9.2.3.6 Drive modules

The modules for the real disk drive emulation are included in the emulator when the emulation is enabled during the writing of the snapshot.

Name	Type	Description
*CPU	6502	The Drive 0 CPU
*	*	*

9.2.4 Common Modules

This section shows the basic module framework and the contents of the different types of modules.

The single chip modules contain the **chip** state, and sometimes additional state of the emulator. In the past we tried to make the format as implementation-independent as possible, to allow reuse of snapshots in later versions of the emulator, that however proved to be very impractical and insanely hard to maintain.

9.2.4.1 CPU 6502 module

This module saves the core 6502 state. You will find a clock value there. All other modules save their own clock values relative to this value. However, the drive modules save their clocks relative to their appropriate CPUs of course.

The MAINC64CPU module

Version numbers: Major 1, Minor 5.

Type	**Name**	**Description**
CLOCK	main clock	
UBYTE	a	
UBYTE	x	
UBYTE	y	
UBYTE	sp	
WORD	pc	

UBYTE	status
DWORD	last_opcode_info
DWORD	ane_log_level
DWORD	lxa_log_level
DWORD	maincpu_jammed
DWORD	maincpu_ba_low_flags

src/mainc64cpu.c

The MAINVIC20CPU module

Version numbers: Major 1, Minor 5.

Type	Name	Description
CLOCK	main clock	
UBYTE	a	
UBYTE	x	
UBYTE	y	
UBYTE	sp	
WORD	pc	
UBYTE	status	
DWORD	last_opcode_info	
DWORD	ane_log_level	
DWORD	lxa_log_level	
DWORD	maincpu_jammed	

src/mainviccpu.c

9.2.4.2 CPU 6809 module

This module saves the core 6809 state. You will find a clock value there. All other modules save their own clock values relative to this value. However, the drive modules save their clocks relative to their appropriate CPUs of course.

The CPU6809 module

Version numbers: Major 1, Minor 1.

Type	Name	Description
CLOCK	clock	the current CPU clock value. All other clock values are relative to this.
WORD	x	the x register
WORD	y	the y register
WORD	u	the u register
WORD	s	the s register
WORD	pc	the program counter register
BYTE	dp	the direct page register
BYTE	cc	the condition code register
BYTE	a	the a register
BYTE	b	the b register
WORD	v	the V register (for 6309 compatibility)
BYTE	e	the e register (for 6309 compatibility)

BYTE	f	the f register (for 6309 compatibility)
BYTE	md	the md register (for 6309 compatibility)

src/pet/6809.c

9.2.4.3 CPU 65816 module

This module saves the core 65816 state. You will find a clock value there. All other modules save their own clock values relative to this value. However, the drive modules save their clocks relative to their appropriate CPUs of course.

The MAIN65816CPU module

Version numbers: Major 1, Minor 3.

Type	Name	Description
CLOCK	main clock	
UBYTE	a	
UBYTE	b	
WORD	x	
WORD	y	
WORD	sp	
WORD	dpr	
UBYTE	pbr	
UBYTE	dbr	
UBYTE	emul	
WORD	pc	
UBYTE	status	
DWORD	last_opcode_info	

src/main65816cpu.c

9.2.4.4 CIA module

The CIA 6526 is an I/O port chip with 2 8-bit I/O ports, a shift register, two timers, a Time of Day clock and interrupts.

The CIA module

Version numbers: Major 2, Minor 5.

Type	Name	Version	Description
UBYTE	ORA		Output register A
UBYTE	ORB		Output register B
UBYTE	DDRA		Data direction register A
UBYTE	DDRB		Data direction register B
UWORD	TA		Timer A counter value
UWORD	TB		Timer B counter value
UBYTE	TOD_TEN		Time of Day - current tenth of second
UBYTE	TOD_SEC		Time of Day - current seconds
UBYTE	TOD_MIN		Time of Day - current minutes
UBYTE	TOD_HR		Time of Day - current hours
UBYTE	SDR		contents of shift register

UBYTE	IER		mask of enabled interrupt masks
UBYTE	CRA		Control register A
UBYTE	CRB		Control register B
UWORD	TAL		Timer A latch value
UWORD	TBL		Timer B latch value
UBYTE	IFR		mask of currently active interrupts
UBYTE	PBSTATE		Bit 6/7 reflect the PB6/7 toggle bit state.Bit 2/3 reflect the corresponding port bit state.
UBYTE	SRHBITS		number of half-bits to still shift in/out SDR
UBYTE	ALARM_TEN		Time of Day - alarm tenth of second
UBYTE	ALARM_SEC		Time of Day - alarm seconds
UBYTE	ALARM_MIN		Time of Day - alarm minutes
UBYTE	ALARM_HR		Time of Day - alarm hours
UBYTE	READICR		current clock minus the clock when ICR was read last plus 128.
UBYTE	TODLATCHED		Bit 0: 1= latched for reading, Bit 1: 2=stopped for writing
UBYTE	TODL_TEN		Time of Day - latched tenth of second
UBYTE	TODL_SEC		Time of Day - latched seconds
UBYTE	TODL_MIN		Time of Day - latched minutes
UBYTE	TODL_HR		Time of Day - latched hours
DWORD	TOD_TICKS		clk ticks till next tenth of second
UBYTE	IRQ		0=IRQ line inactive, 1=IRQ line active
WORD	TA state	1.1+	Timer A state bits (see ciatimer.h)
WORD	TB state	1.1+	Timer B state bits (see ciatimer.h)
BYTE	SHIFTER	1.2+	actual shift register (original data)
BYTE	SDR_VALID	1.2+	data in SDR valid to be shifted
BYTE	irq_enabled	1.2+	IRQ enabled
BYTE	todtickcounter	1.2+	TOD tick counter
BYTE	SHIFTER_HI	2.3+	high byte of SHIFTER
BYTE	SDR_ALARM	2.3+	if an sdr_alarm is pending and when
BYTE	SP_CNT_IN	2.3+	input state of SP and CNT
DWORD	SDR_DELAY	2.4+	event/history bits for the serial data register
BYTE	SP_CNT_OUT	2.4+	output state of SP and CNT
DWORD	IFR_DELAY	2.5+	event/history bits for the interrupt flag reg.
BYTE	ACK_IRQFLAGS	2.5+	flags in IFR to acknowledge, i.e. clear.

BYTE	NEW_IRQFLAGS	2.5+	new flags in IFR that were set recently.

src/core/ciacore.c

9.2.4.5 VIA module

The VIA 6522 is the predecessor of the CIA and also an I/O port chip with 2 8-bit I/O ports, a shift register, two timers and interrupts.

The VIA module

Version numbers: Major 2, Minor 2.

Type	Name	Version	Description
UBYTE	ORA		Output register A
UBYTE	DDRA		Data direction register A
UBYTE	ORB		Output register B
UBYTE	DDRB		Data direction register B
UWORD	T1L		timer 1 latch value
UWORD	T1C		timer 1 count value
UBYTE	T2LL		Timer 2 latch lo
UBYTE	T2LH		Timer 2 latch hi
UBYTE	T2CL		timer 2 count value lo
UBYTE	T2CH		timer 2 count value hi
UWORD	T2C		timer 2 counter
UBYTE	RUNFL		bit 7: timer 1 will generate IRQ on underflow; bit 6: timer 2 will generate IRQ on underflow
UBYTE	SR		shift register value
UBYTE	ACR		auxiliary control register
UBYTE	PCR		peripherial control register
UBYTE	IFR		active interrupts
UBYTE	IER		interrupt masks
UBYTE	PB7		bit 7 = pb7 state
UBYTE	SRHBITS		number of half-bits to shift out on SR
UBYTE	CABSTATE		bit 7 = ca2 state, bit6 = cb2 state, bit5 = cb1
UBYTE	ILA		input latch port A (see ACR bit 0)
UBYTE	ILB		input latch port B (see ACR bit 1)
UBYTE	t2_irq_allowed	2.2+	
UBYTE	t2_underflow_alarm	2.2+	0 if not; 1+time delta if set.
UBYTE	t2_shift_alarm	2.2+	0 if not; 1+time delta if set.

src/core/viacore.c

9.2.4.6 PIA module

The PIA 6520 is a chip with two I/O ports (Parallel Interface Adapter) and four additional handshake lines. The chip is pretty the same for Port A and B, only that Port A implements handshake on read operation and port B on write operation.

The PIA module

Version numbers: Major 1, Minor 0.

Type	Name	Description
UBYTE	ORA	Output register A
UBYTE	DDRA	Data Direction Register A
UBYTE	CTRLA	Control Register A
UBYTE	ORB	Output register B
UBYTE	DDRB	Data Direction Register B
UBYTE	CTRLB	Control Register B
UBYTE	CABSTATE	Bit 7 = state of CA2, Bit 6 = state of CB2

src/piacore.c

9.2.4.7 TPI module

The TPI 6525 is a chip with three I/O ports (Tri-Port-Interface). One of the ports can double as an interrupt prioritizer. Therefore we also have to save the states of the interrupt stack etc.

The TPI module

Version numbers: Major 1, Minor 0.

Type	Name	Description
UBYTE	PRA	Port A output register
UBYTE	PRB	Port B output register
UBYTE	PRC	Port C output register (doubles as IRQ latch register)
UBYTE	DDRA	Port A data direction register
UBYTE	DDRB	Port B data direction register
UBYTE	DDRC	Port C data direction register (doubles as IRQ mask register)
UBYTE	CR	Control Register
UBYTE	AIR	Active interrupt register
UBYTE	STACK	irq sources saved on stack
UBYTE	CABSTATE	state of CA and CB pins. Bit 7 = state of CA, Bit 6 = state of CB

src/core/tpicore.c

9.2.4.8 RIOT module

The RIOT 6532 is a chip with two I/O ports, some RAM and a Timer. The chip contains 128 byte RAM, but the RAM is not saved in the RIOT snapshot, but in the memory section.

The RIOT module

Version numbers: Major 0, Minor 0.

Type	Name	Description
UBYTE	ORA	Port A output register
UBYTE	DDRA	Port A data direction register
UBYTE	ORB	Port B output register
UBYTE	DDRB	Port B data direction register
UBYTE	EDGECTRL	edge control value: Bit 0/1: A0/A1 address bits written to edgecontrol registers
UBYTE	IRQFL	IRQ fl: Bit 6/7: see RIOT IRQ flag Bit 0: state of IRQ line (1=active)
UBYTE	N	current timer value
UWORD	divider	Pre-scale divider value (1, 8, 64, or 1024)
UWORD	rest	cycles that the divider has done since last counter tick
UBYTE	irqen	0= timer IRQ disabled, 1= enabled

src/core/riotcore.c

9.2.4.9 SID module

The SID module

Version numbers: Major 1, Minor 5.

Type	Name	Version	Description
BYTE	sids	1.2+	amount of extra sids
BYTE	sound	1.2+	sound active flag
BYTE	engine	1.2+	sound engine
BYTE	model	1.4+	SID model
ARRAY	sid data	1.1+	32 BYTES of SID registers

src/sid/sid-snapshot.c

The SIDRESID module

Version numbers: Major 1, Minor 4.

Type	Name	Description
ARRAY	regs	32 BYTES of SID registers
BYTE	bus value	bus value
DWORD	bus value ttl	bus value ttl
ARRAY	accumulator	3 DWORDS of accumulator data
ARRAY	shift register	3 DWORDS of shift register data
ARRAY	rate counter	3 WORDS of rate counter data
ARRAY	exponential counter	3 WORDS of exponential counter data
ARRAY	envelope counter	3 BYTES of envelope counter data
ARRAY	envelope state	3 BYTES of envelope state data
ARRAY	hold zero	3 BYTES of hold zero flag data
ARRAY	rate counter period	3 WORDS of rate counter period data
ARRAY	exponential counter period	3 WORDS of exponential counter period data
ARRAY	envelope pipeline	3 BYTES of envelope pipeline data

ARRAY	shift pipeline	3 BYTES of shift pipeline data
ARRAY	shift register reset	3 DWORDS of shift register reset data
ARRAY	floating output ttl	3 DWORDS of floating output ttl data
ARRAY	pulse output	3 WORDS of pulse output data
BYTE	write pipeline	write pipeline
BYTE	write address	write address
BYTE	voice mask	voice mask

src/sid/sid-snapshot.c

9.2.4.10 ACIA module

The ACIA 6551 is an RS232 interface chip. VICE emulates RS232 connections via `/dev/ttyS*` (Unix) or `COM:` (DOS/WIN - not yet?). When saving a snapshot, those connections are of course lost. The state of the ACIA however is restored if possible. I.e. if a connection is already open when restoring the snapshot, this connection is used instead. If no connection is open, a carrier/DTR drop is emulated.

The ACIA module

Type	Name	Description
UBYTE	TDR	Transmit Data Register
UBYTE	RDR	Receiver Data Register
UBYTE	SR	Status Register (includes state of IRQ line)
UBYTE	CMD	Command Register
UBYTE	CTRL	Control Register
UBYTE	IN_TX	0 = no data to tx; 1 = Data is being transmitted;
QWORD	TICKSTX	ticks till the next TDR empty interrupt
QWORD	TICKSRX	ticks till the next RDF empty interrupt

src/aciacore.c

9.2.4.11 VIC-I module

The VIC-I module

Version numbers: Major 0, Minor 4.

Type	Name	Description
DWORD	interlace_enabled	
DWORD	interlace_field	
CLOCK	framestart_cycle	
BYTE	raster_cycle	
WORD	raster_line	
WORD	area	
WORD	fetch_state	
DWORD	raster_line	
DWORD	text_cols	
DWORD	text_lines	
DWORD	pending_text_cols	
DWORD	line_was_blank	

DWORD	memptr	
DWORD	memptr_inc	
DWORD	row_counter	
DWORD	buf_offset	
BYTE	light_pen.state	
BYTE	light_pen.triggered	
DWORD	light_pen.x	
DWORD	light_pen.y	
DWORD	light_pen.x_extra_bits	
CLOCK	light_pen.trigger_cycle	
BYTE	vbuf	
ARRAY	color ram	0x400 bytes of color ram data ($9400 - $97ff) FIXME: is this really correct?
10*BYTE	registers	10 VIC-I registers

src/vic20/vic-snapshot.c

9.2.4.12 VIC-II module

The VIC-II module

Version numbers: Major 1, Minor 3.

Type	Name	Description
BYTE	AllowBadLines	flag: if true, bad lines can happen
BYTE	BadLine	flag: this is a bad line
BYTE	Blank	flag: draw lines in border color
ARRAY	ColorBuf	40 bytes, character memory buffer (loaded at bad line)
ARRAY	ColorRam	1024 bytes, contents of color RAM
BYTE	IdleState	flag: idle state enabled
BYTE	LPTrigger	flag: light pen has been triggered
BYTE	LPX	light pen X
BYTE	LPY	light pen Y
ARRAY	MatrixBuf	40 bytes, video matrix buffer (loaded at bad line)
BYTE	NewSpriteDmaMask	value for SpriteDmaMask after drawing sprites
DWORD	RamBase	pointer to the start of RAM seen by the VIC
BYTE	RasterCycle	current vicii.raster cycle
WORD	RasterLine	current vicii.raster line
64*BYTE	Registers	VIC-II registers
BYTE	SbCollMask	sprite-background collisions so far
BYTE	SpriteDmaMask	sprites having DMA turned on
BYTE	SsCollMask	sprite-sprite collisions so far
BYTE	VBank	location of memory bank
WORD	Vc	internal VIC-II counter

BYTE	VcAdd	value to add to Vc at the end of this line (vicii.mem_counter_inc)
WORD	VcBase	internal VIC-II memory pointer
BYTE	VideoInt	status of VIC-II IRQ (vicii.irq_status)
BYTE	SpriteXMemPtr	sprite memory pointer
BYTE	SpriteXMemPtrInc	value to add to the MemPtr after fetch
BYTE	SpriteXExpFlipFlop	sprite expansion flip-flop
DWORD	FetchEventTick	ticks for the next "fetch" (DMA) event
BYTE	FetchEventType	type of event (0: matrix, 1: sprite check, 2: sprite fetch)

src/vicii/vicii-snapshot.c

The VIC-IISC module

Version numbers: Major 1, Minor 4.

Type	**Name**	**Description**
ARRAY	regs	0x40 BYTES state from vicii_s, in the same order
DWORD	raster_cycle	
DWORD	cycle_flags	
DWORD	raster_line	
BYTE	start_of_frame	
BYTE	irq_status	
DWORD	raster_irq_line	
BYTE	raster_irq_triggered	
ARRAY	vbuf	VICII_SCREEN_TEXTCOLS
ARRAY	cbuf	VICII_SCREEN_TEXTCOLS
BYTE	gbuf	
DWORD	dbuf_offset	
ARRAY	dbuf	VICII_DRAW_BUFFER_SIZE
DWORD	ysmooth	
BYTE	allow_bad_lines	
BYTE	sprite_sprite_collisions	
BYTE	sprite_background_collisions	
BYTE	clear_collisions	
DWORD	idle_state	
DWORD	vcbase	
DWORD	vc	
DWORD	rc	
DWORD	vmli	
DWORD	bad_line	
BYTE	light_pen.state	
BYTE	light_pen.triggered	
DWORD	light_pen.x	
DWORD	light_pen.y	
DWORD	light_pen.x_extra_bits	
CLOCK	light_pen.trigger_cycle	

BYTE	reg11_delay	
DWORD	prefetch_cycles	
DWORD	sprite_display_bits	
BYTE	sprite_dma	
BYTE	last_color_reg	
BYTE	last_color_value	
BYTE	last_read_phi1	
BYTE	last_bus_phi2	
BYTE	vborder	
BYTE	set_vborder	
BYTE	main_border	
BYTE	refresh_counter	
ARRAY	color_ram	0x400

src/viciisc/vicii-snapshot.c

The DTVVIC module

Version numbers: Major 1, Minor 3.

Type	Name	Description
BYTE	AllowBadLines	flag: if true, bad lines can happen
BYTE	BadLine	flag: this is a bad line
BYTE	Blank	flag: draw lines in border color
40*BYTE	ColorBuf	character memory buffer (loaded at bad line)
BYTE	IdleState	flag: idle state enabled
BYTE	LPTrigger	flag: light pen has been triggered
BYTE	LPX	light pen X
BYTE	LPY	light pen Y
40*BYTE	MatrixBuf	video matrix buffer (loaded at bad line)
BYTE	NewSpriteDmaMask	value for SpriteDmaMask after drawing sprites
DWORD	RamBase	pointer to the start of RAM seen by the VIC
BYTE	RasterCycle	current vicii.raster cycle
WORD	RasterLine	current vicii.raster line
80*BYTE	Registers	VIC-II registers
x	DTVstuff	DTV stuff goes here
BYTE	SbCollMask	sprite-background collisions so far
BYTE	SpriteDmaMask	sprites having DMA turned on
BYTE	SsCollMask	sprite-sprite collisions so far
BYTE	VBank	location of memory bank
WORD	Vc	internal VIC-II counter
BYTE	VcAdd	value to add to Vc at the end of this line (vicii.mem_counter_inc)
WORD	VcBase	internal VIC-II memory pointer
BYTE	VideoInt	status of VIC-II IRQ (vicii.irq_status)
BYTE	SpriteXMemPtr	sprite memory pointer

BYTE	SpriteXMemPtrInc	value to add to the MemPtr after fetch
BYTE	SpriteXExpFlipFlop	sprite expansion flip-flop
DWORD	FetchEventTick	ticks for the next "fetch" (DMA) event
BYTE	FetchEventType	type of event (0: matrix, 1: sprite check, 2: sprite fetch)

src/vicii/viciidtv-snapshot.c

9.2.4.13 TED module

The TED module

Version numbers: Major 1, Minor 5.

Type	Name	Description
DWORD	last_emulate_line_clk	
BYTE	AllowBadLines	flag: if true, bad lines can happen
BYTE	BadLine	flag: this is a bad line
BYTE	Blank	flag: draw lines in border color
40*BYTE	ColorBuf	character memory buffer (loaded at bad line)
BYTE	IdleState	flag: idle state enabled
40*BYTE	MatrixBuf	video matrix buffer (loaded at bad line)
DWORD	RamBase	pointer to the start of RAM seen by the TED
BYTE	RasterCycle	current ted.raster cycle
WORD	RasterLine	current ted.raster line
64*BYTE	Registers	TED registers
DWORD	ted_raster_counter	
WORD	Vc	internal TED counter
BYTE	mem_counter_inc	(VcInc) value to add to Vc at the end of this line
WORD	VcBase	internal TED memory pointer
BYTE	VideoInt	status of TED IRQ (ted.irq_status)
DWORD	FetchEventTick	ticks for the next "fetch" (DMA) event

src/plus4/ted-snapshot.c

9.2.4.14 CRTC module

The CRTC module

Version numbers: Major 1, Minor 2.

Type	Name	Version	Description
WORD	VADDR_MASK		Mask of the address bits valid when accessing the video memory
WORD	VADDR_CHARSWITCH		If one bit in the video address is used to switch the character generator, it is masked here.

WORD	VADDR_CHAROFFSET	The offset in characters in the character generator that CHARSWITCH switches.
WORD	VADDR_REVSWITCH	If one bit in the video address inverts the screen, it is masked here.
WORD	CHARGEN_MASK	size of character generator in byte - 1
WORD	CHARGEN_OFFSET	offset given by external circuitry
BYTE	HW_CURSOR	external hardware cursor circuitry enabled
BYTE	HW_COLS	number of displayed columns during one character clock cycle
BYTE	HW_BLANK	set if the hardware blank feature is available CRTC register
20 BYTE	REGISTERS	register DUMP of the CRTC registers 0-19.
BYTE	REGNO	The current index in the CRTC register file
BYTE	CHAR	The current cycle within the current rasterline
BYTE	CHARLINE	The current character line
BYTE	YCOUNTER	The current rasterline in the character
BYTE	CRSRCNT	Framecounter for the blinking cursor
BYTE	CRSRSTATE	if set the hardware cursor is visible
BYTE	CRSRLINES	set if ycounter is within the active cursor rasterlines for a char
WORD	CHARGEN_REL	relative base of currently used character generator in ROM (in byte)
WORD	SCREEN_REL	screen address to load the counter at the beginning of the next rasterline
WORD	VSYNC	number of rasterlines left within vsync; 0 = not in vsync
BYTE	VENABLE	vertical enable flipflop; 1= display, 0= blank.
WORD	SCREEN_WIDTH	width of the current display window
WORD	SCREEN_HEIGHT	height of the current display window
WORD	SCREEN_XOFFSET	x position where the first character in a line starts in the window
WORD	HJITTER	but only after adding this jitter

WORD	SCREEN_YOFFSET		x position where the first character in a line starts in the window
WORD	FRAMELINES		expected number of rasterlines for the current frame
WORD	CURRENT_LINE		current rasterline as seen from the CRTC
BYTE	FLAG	1.1+	Bit 0: If 1 then bit in VADDR_REVSWITCH must be set for reverse; if 0 then bit must be cleared for reverse.

src/crtc/crtc-snapshot.c

Here is the reference for the previous CRTC snapshot module. It is outdated and will not be read by this and later versions of VICE.

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	RASTERY	The number of clock cycles from rasterlines start
WORD	RASTERLINE	The current rasterline
WORD	ADDRMASK	The address mask valid for the CRTC. All memory accesses are masked with this value
BYTE	HWFLAG	Bit 0: 1= hardware cursor available. Bit 1: 1= number of columns is doubled by external hardware
20 BYTE	REGISTERS	register DUMP of the CRTC registers 0-19.
BYTE	CRSRSTATE	Hardware cursor: Bits 0-3: frame counter till next crsr line toggle. Bit 7: 1= cursor line active

9.2.4.15 C64 memory module

The C64 memory module actually consists of two modules. The "C64MEM" module is mandatory and contains the RAM dump. The "C64ROM" module is optional and contains a dump of the ROM images.

The size of the C64 memory modules differs with each different memory configuration. The RAM configuration is saved in the snapshot, and restored when the snapshot is loaded.

The C64MEM module

Version numbers: Major 0, Minor 1.

Type	**Name**	**Version**	**Description**
BYTE	pport data	0.0+	CPU port data register
BYTE	pport dir	0.0+	CPU port direction register
BYTE	EXROM	0.0+	EXROM line state
BYTE	GAME	0.0+	GAME line state
ARRAY	RAM	0.0+	65536 BYTES of RAM data

BYTE	pport data out	0.0+	CPU port data out lines state
BYTE	pport data read	0.0+	CPU port data in lines state
BYTE	pport dir read	0.0+	CPU port direction in lines state
DWORD	pport bit6 clock	0.1	CPU port bit 6 falloff clock
DWORD	pport bit7 clock	0.1	CPU port bit 7 falloff clock
BYTE	pport bit 6	0.1	CPU port bit 6 state
BYTE	pport bit 7	0.1	CPU port bit 7 state
BYTE	pport bit 6 falloff	0.1	CPU port bit 6 discharge flag
BYTE	pport bit 7 falloff	0.1	CPU port bit 7 discharge flag

src/c64/c64memsnapshot.c

The C64ROM module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	KERNAL	8192 BYTES of KERNAL ROM data
ARRAY	BASIC	8192 BYTES of BASIC ROM data
ARRAY	CHARGEN	4096 BYTES of CHARGEN ROM data

src/c64/c64memsnapshot.c

The C64SCPUMEM module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	mem_pport	
BYTE	mem_reg_soft_1mhz	
BYTE	mem_reg_sys_1mhz	
BYTE	mem_reg_hwenable	
BYTE	mem_reg_dosext	
BYTE	mem_reg_ramlink	
BYTE	mem_reg_optim	
BYTE	mem_reg_bootmap	
BYTE	mem_reg_simm	
BYTE	exrom	
BYTE	game	
DWORD	mem_simm_ram_mask	
ARRAY	mem_ram	SCPU64_RAM_SIZE
ARRAY	mem_sram	SCPU64_SRAM_SIZE
ARRAY	mem_simm_ram	mem_simm_ram_mask + 1

src/scpu64/scpu64memsnapshot.c

The C64SCPUROM module

Version numbers: Major 0, Minor 1.

Type	Name	Description
ARRAY	mem_chargen_rom	SCPU64_CHARGEN_ROM_SIZE
ARRAY	scpu64rom_scpu64_rom	SCPU64_SCPU64_ROM_MAXSIZE

src/scpu64/scpu64memsnapshot.c

The C64DTVMEM module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	pport.data	
BYTE	pport.dir	
ARRAY	mem_ram	0x200000
BYTE	c64dtvmem_memmapper[0]	
BYTE	c64dtvmem_memmapper[1]	
BYTE	pport.data_out	
BYTE	pport.data_read	
BYTE	pport.dir_read	

src/c64dtv/c64dtvmemsnapshot.c

The C64DTVROM module

Version numbers: Major 0, Minor 1.

Type	Name	Description
ARRAY	c64dtvflash_mem	0x200000
BYTE	c64dtvflash_state	
ARRAY	c64dtvflash_mem_lock	39

src/c64dtv/c64dtvmemsnapshot.c

9.2.4.16 C128 memory module

The C128 memory module actually consists of two modules. The "C128MEM" module is mandatory and contains the RAM dump. The "C128ROM" module is optional and contains a dump of the ROM images.

The size of the C128 memory modules differs with each different memory configuration. The RAM configuration is saved in the snapshot, and restored when the snapshot is loaded. The attached cartridges are also restored upon load if they have been saved in the snapshot.

The C128MEM module

Version numbers: Major 0, Minor 0.

Type	Name	Version	Description
11 * BYTE	mmu regs	0.0	dump of mmu registers
ARRAY	RAM	0.0	0x40000 BYTES of RAM data

src/c128/c128memsnapshot.c

The C128ROM module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	KERNAL	8192 BYTES of KERNAL ROM data
ARRAY	BASIC LO	16384 BYTES of BASIC ROM data
ARRAY	BASIC HI	16384 BYTES of BASIC ROM data

ARRAY	CHARGEN	8192 BYTES of CHARGEN ROM data

src/c128/c128memsnapshot.c

9.2.4.17 VIC20 memory module

The VIC20 memory module actually consists of two modules. The "VIC20MEM" module is mandatory and contains the RAM dump. The "VIC20ROM" module is optional and contains a dump of the ROM images.

The size of the VIC20 memory modules differs with each different memory configuration. The RAM configuration is saved in the snapshot, and restored when the snapshot is loaded. The attached cartridges are also restored upon load if they have been saved in the snapshot.

The VIC20MEM module

Version numbers: Major 1, Minor 1.

Type	Name	Version	Description
UBYTE	CONFIG		Bit 0: 1 = expansion block 0 RAM enabled 1: 1 = expansion block 1 RAM enabled 2: 1 = expansion block 2 RAM enabled 3: 1 = expansion block 3 RAM enabled 5: 1 = expansion block 5 RAM enabled
UBYTE	cpu_last_data	1.1+	
UBYTE	v_bus_last_data	1.1+	
UBYTE	v_bus_last_high	1.1+	
ARRAY	RAM0		1k RAM $0000-$03ff
ARRAY	RAM1		4k RAM $1000-$1fff
ARRAY	BLK0		3k RAM $0400-$0fff (if blk 0 RAM enabled)
ARRAY	BLK1		8k RAM $2000-$3fff (if blk 1 RAM enabled)
ARRAY	BLK2		8k RAM $4000-$5fff (if blk 2 RAM enabled)
ARRAY	BLK3		8k RAM $6000-$7fff (if blk 3 RAM enabled)
ARRAY	BLK5		8k RAM $A000-$Bfff (if blk 5 RAM enabled)

src/vic20/vic20memsnapshot.c

The VIC20ROM module

Version numbers: Major 1, Minor 1.

Type	Name	Version	Description

UBYTE	CONFIG		Bit 0: 1 = ROM block $2*** enabled 1: 1 = ROM block $3*** enabled 2: 1 = ROM block $4*** enabled 3: 1 = ROM block $5*** enabled 4: 1 = ROM block $6*** enabled 5: 1 = ROM block $7*** enabled 6: 1 = ROM block $A*** enabled 7: 1 = ROM block $B*** enabled
ARRAY	KERNAL		8k KERNAL ROM $e000-$ffff
ARRAY	BASIC		8k BASIC ROM $c000-$dfff
ARRAY	CHARGEN		4k CHARGEN ROM
ARRAY	BLK1A		4k ROM $2*** (if CONFIG & 1)
ARRAY	BLK1B		4k ROM $3*** (if CONFIG & 2)
ARRAY	BLK3A		4k ROM $6*** (if CONFIG & 16)
ARRAY	BLK3B		4k ROM $7*** (if CONFIG & 32)
ARRAY	BLK5A		4k ROM $A*** (if CONFIG & 64)
ARRAY	BLK5B		4k ROM $B*** (if CONFIG & 128)
ARRAY	BLK2A	1.1+	4k ROM $4*** (if CONFIG & 4)
ARRAY	BLK2B	1.1+	4k ROM $5*** (if CONFIG & 8)

src/vic20/vic20memsnapshot.c

9.2.4.18 Plus4 memory module

The PLUS4MEM module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	pport.data	
BYTE	pport.dir	
BYTE	pport.data_out	
BYTE	mem_config	
ARRAY	mem_ram	PLUS4_RAM_SIZE

src/plus4/plus4memsnapshot.c

The PLUS4ROM module

Version numbers: Major 1, Minor 0.

Type	Name	Description
ARRAY	kernal rom	PLUS4_KERNAL_ROM_SIZE
ARRAY	basic rom	PLUS4_BASIC_ROM_SIZE
ARRAY	extromlo1	PLUS4_BASIC_ROM_SIZE
ARRAY	extromlo3	PLUS4_BASIC_ROM_SIZE
ARRAY	extromhi1	PLUS4_KERNAL_ROM_SIZE
ARRAY	extromhi3	PLUS4_KERNAL_ROM_SIZE

src/plus4/plus4memsnapshot.c

9.2.4.19 PET memory module

The PET memory module actually consists of three modules. The "PETMEM" module is mandatory and contains the RAM dump. The "PETROM" module is optional and contains a dump of the ROM images. The "PETDWW" module is also optional and contains the image of the hires expansion board (if enabled).

The size of the PET memory modules differs with each different memory configuration. The RAM configuration is saved in the snapshot, and restored when the snapshot is loaded.

The POSITIONAL item has been added in PETMEM snapshot version 1.1. It is ignored by earlier restore routines (V1.0) and the V1.1 restore routines do not change the current setting when reading a V1.0 snapshot.

In V1.2 the new EOIBLANK variable has been added. This implements the "blank screen on EOI" feature that was previously linked to a wrong resource.

In V1.3 the state for SuperPET has been added.

The PETMEM module

Version numbers: Major 1, Minor 3.

Type	Name	Version	Description
UBYTE	CONFIG		Bits 0-3: 0 = 40 col PET without CRTC 1 = 40 col PET with CRTC 2 = 80 col PET (with CRTC) 3 = SuperPET 4 = 8096 5 = 8296 Bit 6: 1= RAM at $9*** Bit 7: 1= RAM at $A***
UBYTE	KEYBOARD		0 = UK business 1 = graphics
UBYTE	MEMSIZE		memory size of low 32k in k (4,8,16,32)
UBYTE	CONF8X96		8x96 configuration register
UBYTE	SUPERPET		SuperPET config: Bit 0: spet_ramen, 1= RAM enabled 1: spet_ramwp, 1= RAM write protected 2: spet_ctrlwp, 1= CTRL reg write prot. 3: spet_diag, 0= diag active 4-7: spet_bank, RAM block in use
ARRAY	RAM		4-32k RAM (not 8296, dep. on MEMSIZE)
ARRAY	VRAM		2/4k RAM (not 8296, dep in CONFIG)
ARRAY	EXTRAM		64k (SuperPET and 8096 only)

ARRAY	RAM		128k RAM (8296 only)
BYTE	POSITIONAL	1.1+	bit 0=0 = symbolic keyboard mapping =1 = positional keyboard mapping
BYTE	EOIBLANK	1.2+	bit 0=0: EOI does not blank screen =1: EOI does blank screen bit 1=0: Screen memory like 3000 and later 1: Screen memory like 2001
WORD	CPU_SWITCH	1.3+	6502 / 6809 / PROG
BYTE	VAL	1.3+	6702 dongle state information
BYTE	PREVODD	1.3+	
BYTE	WANTODD	1.3+	
WORD[8]	SHIFT	1.3+	
BYTE	SuperPET 2	1.3+	Extra bits due to the Super-OS-9 MMU. Bit 5: FIRQ disabled. Bit 6: expansion memory in OS-9 flat mode.

src/pet/petmemsnapshot.c

The PETROM module

Version numbers: Major 1, Minor 1.

Type	**Name**	**Version**	**Description**
UBYTE	CONFIG		Bit 0: 1= $9*** ROM included 1: 1= $a*** ROM included 2: 1= $b*** ROM included 3: 1= $e900-$efff ROM included 4: 1= $9000-$ffff 6809 ROM and upper half CHARGEN ROM included
ARRAY	KERNAL		4k KERNAL ROM image $f000-$ffff
ARRAY	EDITOR		2k EDITOR ROM image $e000-$e800
ARRAY	CHARGEN		2k CHARGEN ROM image
ARRAY	ROM9		4k $9*** ROM (if CONFIG & 1)
ARRAY	ROMA		4k $A*** ROM (if CONFIG & 2)
ARRAY	ROMB		4k $B*** ROM (if CONFIG & 4)
ARRAY	ROMC		4k $C*** ROM
ARRAY	ROMD		4k $D*** ROM
ARRAY	ROME9		7 blocks $e900-$efff ROM (if CONFIG & 8)

ARRAY	ROM6809	1.1+	24k $A000-$FFFF ROM (if CONFIG & 16)
ARRAY	CHARGEN(2)	1.1+	upper half of CHARGEN (if CONFIG & 16)

src/pet/petmemsnapshot.c

The PETDWW module

For storing the state of the DWW hires expansion board, there is a PETDWWPIA module, and a DWWMEM module.

The former has the same format as the PIA1.

The DWWMEM module

Type	Name	Description
WORD	RAM size	size, 0 if not allocated
ARRAY	RAM data	"RAM size" bytes RAM content

src/pet/petdww.c

9.2.4.20 CBM-II memory module

The CBM-II memory module actually consists of two modules. The "CBM2MEM" module is mandatory and contains the RAM dump. The "CBM2ROM" module is optional and contains a dump of the ROM images.

The size of the CBM-II memory modules differs with each different memory configuration. The RAM configuration is saved in the snapshot, and restored when the snapshot is loaded.

The CBM2MEM module

Version numbers: Major 1, Minor 0.

Type	Name	Description
UBYTE	MEMSIZE	Memory size in 128KiB blocks (1=128KiB,2=256KiB, 4=512KiB, 8=1024KiB)
UBYTE	CONFIG	Bit 0 = $f0800-$f0fff RAM 1 = $f1000-$f1fff RAM 2 = $f2000-$f3fff RAM 3 = $f4000-$f5fff RAM 4 = $f6000-$f7fff RAM 5 = $fc000-$fcfff RAM 6: 1= RAM starts at 0 (C500), videoram is 1k VIC-II video, 1k colorram 0= RAM starts at 0x10000 (others), videoram is 2k crtc videoram
UBYTE	HWCONFIG	Bit 0-1: ModelLine
UBYTE	EXECBANK	CPU exec bank register
UBYTE	INDBANK	CPU indirect bank register
ARRAY	SYSRAM	2k system RAM, Bank15 $f0000-$f07ff
ARRAY	VIDEO	2k video RAM, Bank15 $fd000-$fd7ff
ARRAY	RAM	RAM dump, size according to MEMSIZE

ARRAY	RAM08	(only if memsize < 1M) 2k for cart08_ram CONFIG & 1 : 2KiB RAM $f0800-$f0fff
ARRAY	RAM1	(only if memsize < 1M) 4k for cart1_ram CONFIG & 2 : 4KiB RAM $f1000-$f1fff
ARRAY	RAM2	(only if memsize < 1M) 8k for cart2_ram CONFIG & 4 : 8KiB RAM $f2000-$f3fff
ARRAY	RAM4	(only if memsize < 1M) 8k for cart4_ram CONFIG & 8 : 8KiB RAM $f4000-$f5fff
ARRAY	RAM6	(only if memsize < 1M) 8k for cart6_ram CONFIG & 16 : 8KiB RAM $f6000-$f7fff
ARRAY	RAMC	(only if memsize < 1M) 4k for cartC_ram CONFIG & 32 : 4KiB RAM $fc000-$fcfff

src/cbm2/cbm2memsnapshot.c

The RAM* arrays are only saved if the RAM itself is less than 1MiB. If the memory size is 1MiB then those areas are taken from the bank 15 area of the normal RAM.

The memory array starts at $10000 if the memory size is less than 512KiB, or at $00000 if 512KiB or more. In case of a C510, then the memory array also always starts at $00000.

The CBM2ROM module

Version numbers: Major 1, Minor 0.

Type	Name	Description
UBYTE	CONFIG	Bit 1: cart1 ROM included 2: cart2 ROM included 3: cart4 ROM included 4: cart6 ROM included 5: chargen is of C510 type (VIC-II)
ARRAY	KERNAL	8k Kernal ROM ($e000-$ffff)
ARRAY	BASIC	16k Basic ROM ($8000-$bfff)
ARRAY	CHARGEN	4k chargen ROM image ($c*** for VIC-II)
ARRAY	ROM1	4k for cart1 (if config & 2)
ARRAY	ROM2	8k for cart2 (if config & 4)
ARRAY	ROM4	8k for cart4 (if config & 8)
ARRAY	ROM6	8k for cart6 (if config & 16)

src/cbm2/cbm2memsnapshot.c

9.2.4.21 C500 data module

The C500 data module contains simple state information not already saved in the other modules.

The C500DATA module

Version numbers: Major 0, Minor 0.

Type	Name	Description
DWORD	IRQCLK	CPU clock ticks until next 50 Hz IRQ

src/cbm2/cbm5x0.c

9.2.5 other modules

The SID2 module

Version numbers: Major 1, Minor 5.

Type	Name	Version	Description
WORD	address	1.2+	SID address
ARRAY	sid data	1.2+	32 BYTES of SID registers

src/sid/sid-snapshot.c

The SID3 module

Version numbers: Major 1, Minor 5.

Type	Name	Version	Description
WORD	address	1.2+	SID address
ARRAY	sid data	1.2+	32 BYTES of SID registers

src/sid/sid-snapshot.c

The SID4 module

Version numbers: Major 1, Minor 5.

Type	Name	Version	Description
WORD	address	1.4+	SID address
ARRAY	sid data	1.4+	32 BYTES of SID registers

src/sid/sid-snapshot.c

The SID5 module

Version numbers: Major 1, Minor 5.

Type	Name	Version	Description
WORD	address	1.5+	SID address
ARRAY	sid data	1.5+	32 BYTES of SID registers

src/sid/sid-snapshot.c

The SID6 module

Version numbers: Major 1, Minor 5.

Type	Name	Version	Description
WORD	address	1.5+	SID address
ARRAY	sid data	1.5+	32 BYTES of SID registers

src/sid/sid-snapshot.c

The SID7 module

Version numbers: Major 1, Minor 5.

Type	Name	Version	Description
WORD	address	1.5+	SID address
ARRAY	sid data	1.5+	32 BYTES of SID registers

src/sid/sid-snapshot.c

The SID8 module

Version numbers: Major 1, Minor 5.

Type	Name	Version	Description
WORD	address	1.5+	SID address
ARRAY	sid data	1.5+	32 BYTES of SID registers

src/sid/sid-snapshot.c

The SIDFASTSID module

Version numbers: Major 1, Minor 4.

Type	Name	Description
DWORD	factor	speed factor
ARRAY	d	32 BYTES of register data
BYTE	has3	voice 3 enable flag
BYTE	vol	4-bit volume value
ARRAY	adrs	16 DWORDS of ADSR counter step values for each adsr values
ARRAY	sz	16 DWORDS of sustain values compared to 31-bit ADSR counter
DWORD	speed1	internal constant used for sample rate dependent calculations
BYTE	update	structure needs updating flag
BYTE	newsid	new SID flag
BYTE	laststore	last store
BYTE	laststorebit	last store bit
DWORD	laststoreclk	CLOCK of the last store
DWORD	emulatefilter	emulate filters flag
DOUBLE	filterDy	filter Dy
DOUBLE	filterResDy	filter Res Dy
BYTE	filterType	filter type
BYTE	filterCurType	current filter type
WORD	filterValue	filter value
ARRAY	voice nr	3 DWORDS of voice numbers
ARRAY	voice f	3 DWORDS of voice counter value data
ARRAY	voice fs	3 DWORDS of voice counter step data
ARRAY	voice noise	3 BYTES of voice noise flag data
ARRAY	voice adsr	3 DWORDS of voice 31-bit ADSR counter data
ARRAY	voice adsrs	3 DWORDS of voice ADSR counter step data
ARRAY	voice adsrz	3 DWORDS of voice ADSR sustain level compared to the 31-bit counter data
ARRAY	voice sync	3 BYTES of voice hard sync flag data
ARRAY	voice filter	3 BYTES of voice filter flag data

ARRAY	voice update	3 BYTES of voice structure needs updating flag data
ARRAY	voice gateflip	3 BYTES of voice do multiple gate flips flag data
ARRAY	voice adsrm	3 BYTES of voice ADSR mode data
ARRAY	voice attack	3 BYTES of voice 4-bit attack value data
ARRAY	voice decay	3 BYTES of voice 4-bit decay value data
ARRAY	voice sustain	3 BYTES of voice 4-bit sustain value data
ARRAY	voice release	3 BYTES of voice 4-bit release value data
ARRAY	voice rv	3 DWORDS of voice noise shift reguster data
ARRAY	voice wt	3 BYTES of voice wavetable index data
ARRAY	voice wt_offset	3 WORDS of voice wavetable offset data
ARRAY	voice wtpf	3 DWORDS of voice 32-bit offset data
ARRAY	voice wtl	3 DWORDS of voice wavetable length data
ARRAY	voice wtr[0]	3 WORDS of ring modulation flag data
ARRAY	voice wtr[1]	3 WORDS of ring modulation flag data
ARRAY	voice filtIO	3 BYTES of filtIO data
ARRAY	voice filtLow	3 DOUBLES of filtLow data
ARRAY	voice filtRef	3 DOUBLES of filtRef data

src/sid/sid-snapshot.c

The SIDCWMKIII module

Version numbers: Major 1, Minor 4.

Type	Name	Description
BYTE	ntsc	NTSC flag
DWORD	cycles per second	cycles per second
ARRAY	registers	32 BYTES of register data

src/sid/sid-snapshot.c

The SIDHARDSID module

Version numbers: Major 1, Minor 4.

Type	Name	Version	Description
ARRAY	registers	1.2+	32 BYTES of register data
DWORD	main clock	1.2+	main clock
DWORD	alarm clock	1.2+	alarm clock
DWORD	last access clock	1.2+	last access clock
DWORD	last access ms	1.2+	last access ms
DWORD	last access chipno	1.2+	last access chipno
DWORD	chip used	1.2+	chip used
ARRAY	device map	1.2+	2 DWORDS of device map data
ARRAY	extra device map	1.3	2 DWORDS of device map data

src/sid/sid-snapshot.c

The SIDPARSID module

Version numbers: Major 1, Minor 4.

Type	Name	Description
ARRAY	registers	32 BYTES of register data
BYTE	ctr port	control port state

src/sid/sid-snapshot.c

The SIDUSBSID module

Version numbers: Major 1, Minor 4.

Type	Name	Description
ARRAY	registers	(0x20 * 4) BYTES of register data
QWORD	main clk	
QWORD	alarm clk	
BYTE	lastaccess_chipno	

src/sid/sid-snapshot.c

The MIDI module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	ctrl	control
BYTE	status	status
BYTE	rxdata	RX data
BYTE	rxdata	TX data
BYTE	last read	last read value
DWORD	ticks	midi ticks
BYTE	intx	intx
BYTE	rx irq	RX IRQ
BYTE	rx irq	TX IRQ
BYTE	alarm active	alarm active flag
BYTE	IRQ	IRQ
DWORD	IRQ res	IRQ res
BYTE	mode	midi mode
DWORD	int num	interrupt number
DWORD	alarm clk	alarm clock

src/midi.c

The T64IMAGE module

Version numbers: Major 1, Minor 1.

Type	Name	Description
DWORD	t64_size	
ARRAY	t64 file data	

src/tape/tape-snapshot.c

The TAPIMAGE module

Version numbers: Major 1, Minor 0.

Type	Name	Description
DWORD	tap_size	
ARRAY	tap file data	

src/tape/tape-snapshot.c

The TAPE module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	read_only	
BYTE	type	
BYTE	t64->current_file_number	
DWORD	t64->current_file_seek_position	
DWORD	tap->size	
BYTE	tap->version	
BYTE	tap->system	
DWORD	tap->current_file_seek_position	
DWORD	tap->offset	
DWORD	tap->cycle_counter	
DWORD	tap->cycle_counter_total	
DWORD	tap->counter	
DWORD	tap->mode	
DWORD	tap->read_only	
DWORD	tap->has_changed	

src/tape/tape-snapshot.c

The FDC module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	STATE	FDC state
DWORD	CLK	clk ticks till next fdc invocation
BYTE	NDRV	number of drives (1 or 2)
BYTE	LTRACK0	last track
BYTE	LSECTOR0	last sector
BYTE	LTRACK1	last track (if ndrv == 2)
BYTE	LSECTOR1	last sector (if ndrv == 2)

src/drive/ieee/fdc.c

The DRIVE module

Version numbers: Major 2, Minor 0.

Type	Name	Description

BYTE	has_tde[unit]
BYTE	has_drives[unit]
DWORD	sync_factor
CLOCK	attach_clk
BYTE	byte_ready_level
BYTE	unit->clock_frequency
WORD	current_half_track + (side * DRIVE_HALFTRACKS_1571))
CLOCK	detach_clk
BYTE	extend_image_policy
DWORD	GCR_head_offset
BYTE	GCR_read
BYTE	GCR_write_value
BYTE	unit->idling_method
BYTE	unit->parallel_cable
BYTE	read_only
DWORD	rotation_table_ptr[unr]
DWORD	unit->type
DWORD	snap_accum
CLOCK	snap_rotation_last_clk
DWORD	snap_bit_counter
DWORD	snap_zero_count
WORD	snap_last_read_data
BYTE	snap_last_write_data
DWORD	snap_seed
DWORD	snap_speed_zone
DWORD	snap_ue7_dcba
DWORD	snap_ue7_counter
DWORD	snap_uf4_counter
DWORD	snap_fr_randcount
DWORD	snap_filter_counter
DWORD	snap_filter_state
DWORD	snap_filter_last_state
DWORD	snap_write_flux
DWORD	snap_PulseHeadPosition
DWORD	snap_xorShift32
DWORD	snap_so_delay
DWORD	snap_cycle_index
CLOCK	snap_ref_advance
DWORD	snap_req_ref_cycles
CLOCK	attach_detach_clk
BYTE	byte_ready_edge
BYTE	byte_ready_active

src/drive/drive-snapshot.c

The DRIVECPU65C02 module

Version numbers: Major 1, Minor 3.

Type	Name	Description
CLOCK	clock	
UBYTE	a	
UBYTE	x	
UBYTE	y	
UBYTE	sp	
WORD	pc	
UBYTE	status	
DWORD	last_opcode_info	
CLOCK	last_clk	
CLOCK	cycle_accum	
CLOCK	last_exc_cycles	
CLOCK	stop_clk	
UBYTE	cpu_last_data	
ARRAY	drive RAM	size depends on drive

src/drive/drivecpu65c02.c

The DRIVECPU module

Version numbers: Major 1, Minor 3.

Type	Name	Description
CLOCK	clock	
UBYTE	a	
UBYTE	x	
UBYTE	y	
UBYTE	sp	
WORD	pc	
UBYTE	status	
DWORD	last_opcode_info	
CLOCK	last_clk	
CLOCK	cycle_accum	
CLOCK	last_exc_cycles	
CLOCK	stop_clk	
UBYTE	cpu_last_data	
ARRAY	drive RAM	size depends on drive

src/drive/drivecpu.c

The CMDHD module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	LEDs	
ARRAY	i8255a_i	3
ARRAY	i8255a_o	3
BYTE	scsi_dir	

BYTE	preadyff

src/drive/iec/cmdhd.c

The WD1770 module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	data	
BYTE	track	
BYTE	sector	
BYTE	status	
BYTE	cmd	
WORD	crc	
BYTE	command	
DWORD	type	
DWORD	step	
DWORD	byte_count	
DWORD	tmp	
DWORD	direction	
CLOCK	clk	
BYTE	irq	
BYTE	dden	
BYTE	sync	
BYTE	is1772	

src/drive/iec/wd1770.c

The FDD module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	number	
BYTE	disk_change	
BYTE	write_protect	
BYTE	track	
BYTE	tracks	
BYTE	head	
BYTE	sectors	
BYTE	motor	
BYTE	rate	
BYTE	sector_size	
BYTE	iso	
BYTE	gap2	
BYTE	gap3	
BYTE	head_invert	
BYTE	disk_rate	
DWORD	image_sectors	
DWORD	index_count	

DWORD	raw.head	
BYTE	raw.track_head	
BYTE	raw.dirty	
ARRAY	raw.data	raw.size
ARRAY	raw.sync	(raw.size + 7) >> 3

src/drive/iec/fdd.c

The DRIVEROM module

Version numbers: Major 1, Minor 0.

Type	Name	Description
ARRAY	drive ROM	size depends on drive

src/drive/driverom.c

The FSDRIVE module

Version numbers: Major 0, Minor 0.

src/serial/fsdrive.c

The FSDRIVE module

Version numbers: Major 0, Minor 0.

src/serial/fsdrive-snapshot.c

The PAPERCLIP64SC module

Version numbers: Major 0, Minor 1.

Type	Name	Description
DWORD	counter	counter
BYTE	command	command
BYTE	state	state

src/joyport/paperclip64sc.c

The SPACEBALLS module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	GROUNDS	userport PBx state

src/joyport/spaceballs.c

The JOYSTICK module

Type	Name	Description
WORD	joystick value	current/last value read from the joystick port

src/joyport/joystick.c

The JOYPORT module

Type	Name	Description
BYTE	device id	device id connected to this joystick port

src/joyport/joyport.c

The JPIOSIM module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	DATA OUT	data out value
BYTE	DATA IN	data in value
BYTE	POTX	pot-x state
BYTE	POTY	pot-y state

src/joyport/joyport_io_sim.c

The PAPERCLIP64E module

Version numbers: Major 0, Minor 1.

Type	Name	Description
DWORD	counter	counter
BYTE	command	command
BYTE	state	state

src/joyport/paperclip64e.c

The LIGHTPEN module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	value	lightpen return value
BYTE	type	lightpen type
DWORD	buttons	buttons state
DWORD	button y	button Y state
DWORD	button x	button X state

src/joyport/lightpen.c

The MOUSE_NEOS module

Type	Name	Description
BYTE	digital value	digital pins return value
DWORD	buttons	buttons state
BYTE	neos x	neos X
BYTE	neos y	neos Y
BYTE	neos last x	neos last X
BYTE	neos last y	neos last Y
DWORD	neos state	state
DWORD	neos prev	previous state
DWORD	last trigger	last trigger clock
DWORD	neos time out cycles	time out cycles

src/joyport/mouse_neos.c

The PROTOPAD module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	COUNTER	counter value
BYTE	CLOCK	clock line state
BYTE	MODE	mode line state
BYTE	DPADMODE	dpad up mode state
BYTE	PERMRAPID	permanent rapid mode state
BYTE	RAPIDSPEED	rapid speed selection

src/joyport/protopad.c

The NINJASNESPAD module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	COUNTER	counter value
BYTE	LATCH	latch line state
BYTE	CLOCK	clock line state

src/joyport/ninja_snespad.c

The MOUSE_AMIGA module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	digital value	digital pins return value
BYTE	quadrature X	quadrature X
BYTE	quadrature Y	quadrature Y
BYTE	polled joyval	polled joyval
DWORD	buttons	buttons state

src/joyport/mouse_quadrature.c

The MOUSE_CX22 module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	digital value	digital pins return value
BYTE	quadrature X	quadrature X
BYTE	quadrature Y	quadrature Y
BYTE	polled joyval	polled joyval

src/joyport/mouse_quadrature.c

The MOUSE_ST module

Version numbers: Major 1, Minor 0.

Type	Name	Description

BYTE	digital value	digital pins return value
BYTE	quadrature X	quadrature X
BYTE	quadrature Y	quadrature Y
BYTE	polled joyval	polled joyval
DWORD	buttons	buttons state

src/joyport/mouse_quadrature.c

The BBRTC module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	RST	reset line state
BYTE	CLK	clock line state
BYTE	DATA	data line state

src/joyport/bbrtc.c

The PADDLES module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	digital value	digital pins return value
BYTE	paddle 1 x-value	paddle 1 x-value
BYTE	paddle 1 y-value	paddle 1 y-value
BYTE	old paddle 1 x-value	old paddle 1 x-value
BYTE	old paddle 1 y-value	old paddle 1 y-value

src/joyport/mouse_paddle.c

The KOALAPAD module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	digital value	digital pins return value
BYTE	paddle 1 x-value	paddle 1 x-value
BYTE	paddle 1 y-value	paddle 1 y-value
BYTE	old paddle 1 x-value	old paddle 1 x-value
BYTE	old paddle 1 y-value	old paddle 1 y-value

src/joyport/mouse_paddle.c

The INCEPTION module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	COUNTER	counter value
BYTE	CLOCK	clock line state

src/joyport/inception.c

The MOUSE_1351 module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	digital value	digital pins return value

src/joyport/mouse_1351.c

The MOUSE_MICROMYS module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	digital value	digital pins return value
DWORD	up down counter	up down counter
DWORD	up down pulse end	up down pulse end

src/joyport/mouse_1351.c

The MOUSE_SMART module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	digital value	digital pins return value

src/joyport/mouse_1351.c

The PAPERCLIP2 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
DWORD	counter	counter
BYTE	command	command
BYTE	state	state

src/joyport/paperclip2.c

The CX21 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	PORT	PORT register state

src/joyport/cx21.c

The WAASOFT module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	COUNTER	counter value
BYTE	RESET	reset line state
BYTE	CLOCK	clock line state

src/joyport/waasoft_dongle.c

The VIZAWRITE64 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	COUNTER	counter value

src/joyport/vizawrite64_dongle.c

The PAPERCLIP64 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
DWORD	counter	counter
BYTE	command	command
BYTE	state	state

src/joyport/paperclip64.c

The MULTIJOY module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	ADDRESS	which joystick is active

src/joyport/multijoy.c

The POWERPAD module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	COUNTER	counter value
BYTE	CLEAR	clear line state
BYTE	CLOCK	clock line state

src/joyport/powerpad.c

The TRAPTHEMSNESPAD module

Version numbers: Major 0, Minor 2.

Type	Name	Description
BYTE	COUNTER	counter value
BYTE	LATCH	latch line state
BYTE	CLOCK	clock line state

src/joyport/trapthem_snespad.c

The MOUSE_DIGITAL module

Type	Name	Description
BYTE	digital value	digital pins return value
DWORD	buttons	buttons state
BYTE	new x	X
BYTE	new y	Y
BYTE	last x	last X

BYTE	last y	last Y
DWORD	digital state	state
DWORD	clock_vert_move	last trigger clock (Vertical)
DWORD	clock_hori_move	last trigger clock (Horizontal)

src/joyport/mouse_digital.c

The BEHRBONZ module

Version numbers: Major 0, Minor 2.

Type	Name	Description
BYTE	bank_reg	
BYTE	reset_mode	
BYTE	write_once	
ARRAY	cart_rom	0x200000 bytes of ROM data

src/vic20/cart/behrbonz.c

The RABBIT module

Version numbers: Major 0, Minor 1.

Type	Name	Description
ARRAY	cart_rom	2 * 0x400 bytes of ROM data

src/vic20/cart/rabbit.c

The CARTMIDI module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	mode	midi mode

src/vic20/cart/vic20-midi.c

The VIC20CART module

Version numbers: Major 2, Minor 1.

Type	Name	Description
DWORD	vic20cart_type	
BYTE	number_of_carts	number of attached carts
ARRAY	cart_ids	number_of_carts * DWORD cartridge IDs

src/vic20/cart/vic20cart.c

The ULTIMEM module

Version numbers: Major 2, Minor 0.

Type	Name	Description
ARRAY	ultimem	
ARRAY	cart_ram	
ARRAY	cart_rom	

src/vic20/cart/ultimem.c

The CARTGENERICVIC20 module

Version numbers: Major 2, Minor 1.

Type	Name	Description
DWORD	generic_ram_blocks	flags for ram blocks
DWORD	generic_rom_blocks	flags for rom blocks
ARRAY	cart_ram	0x9000 bytes of RAM data
ARRAY	cart_rom	0x9000 bytes of ROM data

src/vic20/cart/vic20-generic.c

The IO2RAMCART module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	RAM	1024 BYTES of RAM data

src/vic20/cart/ioramcart.c

The IO3RAMCART module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	RAM	1024 BYTES of RAM data

src/vic20/cart/ioramcart.c

The FINALEXPANSION module

Version numbers: Major 2, Minor 0.

Type	Name	Description
BYTE	register_a	
BYTE	register_b	
BYTE	lock_bit	
ARRAY	cart_ram	CART_RAM_SIZE
ARRAY	flash_data	CART_ROM_SIZE

src/vic20/cart/finalexpansion.c

The MINIMON module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	active	cartridge active flag
BYTE	io rom	cartridge rom in io
BYTE	bios write	bios writable flag
BYTE	image readonly	image read-only flag
BYTE	bios changed	bios changed flag
ARRAY	BIOS	2048 bytes of BIOS data ($9800-$9FFF)

src/vic20/cart/minimon.c

The MIKROASSEMBLER module

Version numbers: Major 1, Minor 0.

Type	Name	Description
ARRAY	cart_ram	0x400 * 3 bytes of RAM data
ARRAY	cart_rom	0x400 * 8 bytes of ROM data

src/vic20/cart/mikroassembler.c

The SIDCART module

Version numbers: Major 0, Minor 0.

Type	Name	Description
WORD	address	sidcart address
BYTE	clock	sidcart clock

src/vic20/cart/vic20-sidcart.c

The WRITENOW module

Version numbers: Major 0, Minor 1.

Type	Name	Description
ARRAY	cart_rom	8 * 0x400 bytes of ROM data

src/vic20/cart/writenow.c

The MEGACART module

Version numbers: Major 2, Minor 0.

Type	Name	Description
BYTE	bank_low_reg	
BYTE	bank_high_reg	
BYTE	oe_flop	
BYTE	nvram_en_flop	
ARRAY	cart_ram	0x8000 bytes of RAM data
ARRAY	cart_rom	0x200000 bytes of ROM data
ARRAY	cart_nvram	0x2000 bytes of NVRAM data

src/vic20/cart/megacart.c

The VICFLASHPLUGIN module

Version numbers: Major 2, Minor 0.

Type	Name	Description
BYTE	cart_bank_reg	
BYTE	cart_cfg_reg	
ARRAY	cart_ram	CART_RAM_SIZE
ARRAY	cart_rom	CART_ROM_SIZE

src/vic20/cart/vic-fp.c

The SUPEREXPANDER module

Version numbers: Major 1, Minor 0.

Type	Name	Description
ARRAY	cart_ram	0x400 * 3 bytes of RAM data
ARRAY	cart_rom	0x400 * 4 bytes of ROM data

src/vic20/cart/superexpander.c

The UPJOYHIT module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	retval	current serial port brigde value

src/userport/userport_hks_joystick.c

The UPJOYKINGSOFT module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	retval	current serial port brigde value

src/userport/userport_hks_joystick.c

The UPJOYSTARBYTE module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	retval	current serial port bridge value

src/userport/userport_hks_joystick.c

The UPDIGIMAX module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	address	current register address
ARRAY	sound	4 BYTES of sound data
BYTE	voice 0	voice 0 data
BYTE	voice 1	voice 1 data
BYTE	voice 2	voice 2 data
BYTE	voice 3	voice 3 data

src/userport/userport_digimax.c

The UPWIC64 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	input_state	
BYTE	input_length	

src/userport/userport_wic64.c

The UPJOYV8 module

Type	Name	Description
BYTE	select	joyport select

src/userport/userport_v8_joystick.c

The UP8BSS module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	channel	channel flag

src/userport/userport_8bss.c

The UPJOYSYNERGY module

Type	Name	Description
BYTE	select	joyport select

src/userport/userport_synergy_joystick.c

The UP_JOY_WOJ module

Type	Name	Description
BYTE	select	joyport select

src/userport/userport_woj_joystick.c

The UP_JOY_CGA module

Type	Name	Description
BYTE	select	joyport select

src/userport/userport_joystick.c

The UPIOSIM module

Version numbers: Major 0, Minor 2.

Type	Name	Description
BYTE	PBX IN	port b in state
BYTE	PBX OUT	port b out state
BYTE	PAX IN	port a in state
BYTE	PAX OUT	port a out state

src/userport/userport_io_sim.c

The UPSUPERPAD64 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	COUNTER	current count
BYTE	LATCH	latch line state

src/userport/userport_superpad64.c

The USERPORT module

Type	Name	Description
BYTE	active	userport active flag
BYTE	id	device id

src/userport/userport.c

The UPDAC module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	sound data	sound data
BYTE	voice	voice

src/userport/userport_dac.c

The UPRTCDS1307 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	SDA	SDA line state
BYTE	SCL	SCL line state
BYTE	rtc save	save rtc offset upon detacht

src/userport/userport_rtc_ds1307.c

The UPPETSCII module

Version numbers: Major 0, Minor 2.

Type	Name	Description
BYTE	COUNTER	current count
BYTE	CLOCK	clock line state
BYTE	LATCH	latch line state

src/userport/userport_petscii_snespad.c

The UPDIAGPIN module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	address	current state of the diag pin

src/userport/userport_diag_pin.c

The UP_RTC_58321A module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	read	read line active
BYTE	rtcsave	save rtc offset when detaching

src/userport/userport_rtc_58321a.c

The UPPRINTER module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	value	return value
BYTE	strobe	strobe flag

src/printerdrv/interface-userport.c

The DATASETTE module

Type	Name	Description
BYTE	datasette_motor	
BYTE	notape_mode	
CLOCK	last_write_clk	
CLOCK	motor_stop_clk	
BYTE	datasette_alarm_pending	
CLOCK	alarm_clk	
CLOCK	datasette_long_gap_pending	
CLOCK	datasette_long_gap_elapsed	
BYTE	datasette_last_direction	
DWORD	datasette_counter_offset	
BYTE	reset_datasette_with_maincpu	
DWORD	datasette_zero_gap_delay	
DWORD	datasette_speed_tuning	
DWORD	datasette_tape_wobble_frequency	
DWORD	datasette_tape_wobble_amplitude	
DWORD	datasette_tape_azimuth_error	
BYTE	fullwave	
CLOCK	fullwave_gap	

src/datasette/datasette.c

The RTC_DS1307 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	clock halt	clock halt flag
DWORD	clock halt latch hi	high DWORD of clock halt offset
DWORD	clock halt latch lo	low DWORD of clock halt offset
BYTE	am pm	AM/PM flag
DWORD	latch hi	high DWORD of latch offset
DWORD	latch lo	low DWORD of latch offset
DWORD	offset hi	high DWORD of RTC offset
DWORD	offset lo	low DWORD of RTC offset
DWORD	old offset hi	high DWORD of old RTC offset
DWORD	old offset lo	low DWORD of old RTC offset
ARRAY	clock regs	8 BYTES of register data
ARRAY	old clock regs	8 BYTES of old register data
ARRAY	clock regs for read	8 BYTES of register read data
ARRAY	RAM	56 BYTES of RAM data

ARRAY	old RAM	56 BYTES of old RAM data
BYTE	state	current state
BYTE	reg	current register
BYTE	reg ptr	register pointer
BYTE	bit	current bit
BYTE	io byte	current I/O BYTE
BYTE	sclk	SCLK line state
BYTE	data	DATA line state
BYTE	clock register	clock register flag
STRING	device	device name STRING

src/core/rtc/ds1307.c

The RTC_PCF8583 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	clock halt	clock halt flag
DWORD	clock halt latch hi	high DWORD of clock halt offset
DWORD	clock halt latch lo	low DWORD of clock halt offset
BYTE	am pm	AM/PM flag
DWORD	read bit shift	special case bit pattern shift
DWORD	latch hi	high DWORD of latch offset
DWORD	latch lo	low DWORD of latch offset
DWORD	offset hi	high DWORD of RTC offset
DWORD	offset lo	low DWORD of RTC offset
DWORD	old offset hi	high DWORD of old RTC offset
DWORD	old offset lo	low DWORD of old RTC offset
ARRAY	clock regs	16 BYTES of register data
ARRAY	old clock regs	16 BYTES of old register data
ARRAY	clock regs for read	16 BYTES of clock read register data
ARRAY	RAM	240 BYTES of RAM data
ARRAY	old RAM	240 BYTES of old RAM data
BYTE	state	current state
BYTE	reg	current register
BYTE	reg ptr	current register pointer
BYTE	bit	current bit
BYTE	io byte	current I/O BYTE
BYTE	sclk	SCLK line state
BYTE	data	DATA line state
BYTE	clock register	current clock register
STRING	device	device name STRING

src/core/rtc/pcf8583.c

The RTC_DS1602 module

Version numbers: Major 0, Minor 0.

Type	Name	Description

DWORD	latch hi	high DWORD of latch offset
DWORD	latch lo	low DWORD of latch offset
DWORD	offset 0 hi	high DWORD of RTC offset 0
DWORD	offset 0 lo	low DWORD of RTC offset 0
DWORD	offset hi	high DWORD of RTC offset
DWORD	offset lo	low DWORD of RTC offset
DWORD	old offset hi	high DWORD of old RTC offset
DWORD	old offset lo	low DWORD of old RTC offset
DWORD	reg	current register
BYTE	state	current state
BYTE	bit	current bit
BYTE	io byte	current I/O BYTE
BYTE	rst	RST line state
BYTE	clk	CLK line state
BYTE	data line in	DATA in line state
BYTE	data line out	DATA out line state
STRING	device	device name STRING

src/core/rtc/ds1602.c

The RTC_DS1202_1302 module

Version numbers: Major 0, Minor 0.

Type	**Name**	**Description**
BYTE	rtc type	RTC type
BYTE	clock halt	clock halt flag
DWORD	clock halt latch hi	high DWORD of clock halt offset
DWORD	clock halt latch lo	low DWORD of clock halt offset
BYTE	am pm	AM/PM flag
BYTE	write protect	write protect flag
DWORD	latch hi	high DWORD of latch offset
DWORD	latch lo	low DWORD of latch offset
DWORD	offset hi	high DWORD of RTC offset
DWORD	offset lo	low DWORD of RTC offset
DWORD	old offset hi	high DWORD of old RTC offset
DWORD	old offset lo	low DWORD of old RTC offset
ARRAY	clock regs	8 BYTES of register data
ARRAY	old clock regs	8 BYTES of old register data
BYTE	trickle charge	trickle charge
ARRAY	RAM	32 BYTES of RAM data
ARRAY	old RAM	32 BYTES of old RAM data
BYTE	state	current RTC read/write state
BYTE	reg	current register
BYTE	bit	current bit
BYTE	output bit	current output bit
BYTE	io byte	current I/O BYTE
BYTE	sclk	SCLK line state
BYTE	clock register	clock register flag

STRING	device	device name STRING

src/core/rtc/ds1202_1302.c

The RTC_58321A module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	stop	stop flag
BYTE	24 hours	24 hours flag
BYTE	address	current address
DWORD	latch hi	high DWORD of latch offset
DWORD	latch lo	low DWORD of latch offset
DWORD	offset hi	high DWORD of RTC offset
DWORD	offset lo	low DWORD of RTC offset
DWORD	old offset hi	high DWORD of old RTC offset
DWORD	old offset lo	low DWORD of old RTC offset
STRING	device	device name STRING

src/core/rtc/rtc-58321a.c

The RTC_BQ4830Y module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	clock halt	clock halt flag
DWORD	clock halt latch hi	high DWORD of clock halt offset
DWORD	clock halt latch lo	low DWORD of clock halt offset
BYTE	read latch	read latch flag
BYTE	write latch	write latch flag
DWORD	latch hi	high DWORD of the read/write offset
DWORD	latch lo	low DWORD of the read/write offset
DWORD	offset hi	high DWORD of the RTC offset
DWORD	offset lo	low DWORD of the RTC offset
DWORD	old offset hi	high DWORD of the old RTC offset
DWORD	old offset lo	low DWORD of the old RTC offset
ARRAY	clock regs	8 BYTES of register data
ARRAY	old clock regs	8 BYTES of old register data
ARRAY	clock regs changed	8 BYTES of changed register data
ARRAY	RAM	32768 BYTES of RAM data
ARRAY	old RAM	32768 BYTES of old RAM data
STRING	device	device name STRING

src/core/rtc/bq4830y.c

The RTC_72421 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	stop	stop flag

BYTE	24 hours	24 hours flag
BYTE	day offset	offset for day of week
BYTE	day latch	latch for day of week
BYTE	day	day of week (needed for above 2)
DWORD	latch hi	high DWORD of latch offset
DWORD	latch lo	low DWORD of latch offset
DWORD	offset hi	high DWORD of RTC offset
DWORD	offset lo	low DWORD of RTC offset
DWORD	old offset hi	high DWORD of old RTC offset
DWORD	old offset lo	low DWORD of old RTC offset
STRING	device	device name string

src/core/rtc/rtc-72421.c

The RTC_DS12C887 module

Version numbers: Major 0, Minor 0.

Type	**Name**	**Description**
BYTE	clock halt	clock halt flag
DWORD	clock halt latch hi	high DWORD of the clock halt offset
DWORD	clock halt latch lo	low DWORD of the clock halt offset
BYTE	am pm	AM/PM flag
BYTE	set	RTC set in progress flag
DWORD	set latch hi	high DWORD of the set offset
DWORD	set latch lo	low DWORD of the set offset
DWORD	offset hi	high DWORD of the RTC offset
DWORD	offset lo	low DWORD of the RTC offset
DWORD	old offset hi	high DWORD of the old RTC offset
DWORD	old offset lo	low DWORD of the old RTC offset
BYTE	bcd	BCD mode flag
BYTE	alarm flag	alarm flag
BYTE	end of update flag	end of update flag
ARRAY	clock regs	11 BYTES of register data
ARRAY	old clock regs	11 BYTES of old register data
ARRAY	clock regs changed	11 BYTES of changed register data
ARRAY	ctrl regs	2 BYTES of control register data
ARRAY	RAM	128 BYTES of RAM data
ARRAY	old RAM	128 BYTES of old RAM data
BYTE	reg	current register
BYTE	prev second	previous second
STRING	device	device name STRING

src/core/rtc/ds12c887.c

The RTC_DS1216E module

Version numbers: Major 0, Minor 0.

Type	**Name**	**Description**
BYTE	reset	reset flag

BYTE	inactive	inactive flag
BYTE	12hours	12 hours flag
BYTE	pattern pos	pattern position
BYTE	pattern ignore	pattern ignore flag
BYTE	output	current output bit
BYTE	output pos	current output position
BYTE	day offset	day of week offset
BYTE	day latch	day of week latch
DWORD	latch hi	high DWORD of the latch offset
DWORD	latch lo	low DWORD of the latch offset
DWORD	offset hi	high DWORD of the RTC offset
DWORD	offset lo	low DWORD of the RTC offset
DWORD	old offset hi	high DWORD of the old RTC offset
DWORD	old offset lo	low DWORD of the old RTC offset
ARRAY	clock regs	8 BYTES of register data
ARRAY	old clock regs	8 BYTES of old register data
ARRAY	clock regs changed	8 BYTES of changed register data
STRING	device	device name STRING

src/core/rtc/ds1216e.c

The FLASH040 module

Version numbers: Major 2, Minor 0.

Type	**Name**	**Description**
BYTE	state	
BYTE	base_state	
BYTE	program_byte	
ARRAY	erase_mask	FLASH040_ERASE_MASK_SIZE
BYTE	last_read	

src/core/flash040core.c

The FLASH800 module

Version numbers: Major 2, Minor 0.

Type	**Name**	**Description**
BYTE	state	
BYTE	base_state	
BYTE	program_byte	
ARRAY	erase_mask	FLASH800_ERASE_MASK_SIZE
BYTE	last_read	

src/core/flash800core.c

The SCSI module

Version numbers: Major 0, Minor 0.

Type	**Name**	**Description**
BYTE	state	state

BYTE	target	target
BYTE	databus	databus
BYTE	ack	ack
BYTE	req	req
BYTE	bsyi	bsyi
BYTE	bsyo	bsyo
BYTE	sel	sel
BYTE	rst	rst
BYTE	atn	atn
BYTE	cd	cd
bYTE	io	io
BYTE	msg	msg
BYTE	link	link
BYTE	status	status
BYTE	lun	lun
BYTE	command	command
BYTE	sensekey	sensekey
BYTE	asc	asc
BYTE	msg_after_status	msg_after_status
DWORD	seq	seq
DWORD	cmd_size	cmd_size
DWORD	address	address
DWORD	blocks	blocks
DWORD	data_max	data_max
DWORD	max_imagesize	max_imagesize
ARRAY	cmd_buf	256 bytes of cmd_buf
ARRAY	data_buf	512 bytes of data_buf

src/core/scsi.c

The M93C86 module

Version numbers: Major 0, Minor 1.

Type	**Name**	**Description**
BYTE	eeprom_cs	
BYTE	eeprom_clock	
BYTE	eeprom_data_in	
BYTE	eeprom_data_out	
BYTE	input_shiftreg	
BYTE	input_count	
BYTE	output_shiftreg	
BYTE	output_count	
BYTE	command	
BYTE	addr	
BYTE	data0	
BYTE	data1	
BYTE	write_enable_status	
BYTE	ready_busy_status	

ARRAY	m93c86_data	2048 bytes of ROM data

src/core/m93c86.c

The EN25QH128A module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	eeprom_cs	
BYTE	eeprom_clock	
BYTE	eeprom_data_in	
BYTE	eeprom_data_out	
BYTE	input_shiftreg	
BYTE	input_count	
BYTE	output_shiftreg	
BYTE	output_count	
BYTE	command	
BYTE	addr	
BYTE	write_enable_status	
BYTE	ready_busy_status	
ARRAY	spi_flash_data	MAX_ROM_SIZE

src/core/spi-flash.c

The CBM2CART module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	number_of_carts	number of attached carts
BYTE	mem_cartridge_type	main type
ARRAY	cart_ids	number_of_carts * DWORD cartridge IDs

src/cbm2/cart/cbm2cart.c

The CARTGENERICCBM2 module

Version numbers: Major 0, Minor 2.

Type	Name	Version	Description
ARRAY	ROM C1	0.1+	4kiB of ROM data
ARRAY	ROM C2	0.1+	8kiB of ROM data
ARRAY	ROM C4	0.1+	8kiB of ROM data
ARRAY	ROM C6	0.1+	8kiB of ROM data

src/cbm2/cart/cbm2-generic.c

The EVENT module

Version numbers: Major 0, Minor 1.

Type	Name	Description
DWORD	type	
CLOCK	clock	

DWORD	size
ARRAY	data

src/event.c

The CARTJACINT1MB module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	bankreg	0.1+	state of banking register
ARRAY	ROM	0.1+	1MiB of ROM data

src/plus4/cart/jacint1mb.c

The CARTGENERICPLUS4 module

Version numbers: Major 0, Minor 2.

Type	Name	Version	Description
ARRAY	ROM C1LO	0.1+	16kiB of ROM data
ARRAY	ROM C1HI	0.1+	16kiB of ROM data

src/plus4/cart/plus4-generic.c

The PLUS4CART module

Type	Name	Description
BYTE	number_of_carts	number of attached carts
BYTE	mem_cartridge_type	main type
ARRAY	cart_ids	number_of_carts * DWORD cartridge IDs

src/plus4/cart/plus4cart.c

The CARTSPEEDY module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	freeze_active	0.1+	was freeze pressed?
BYTE	freeze_active_q	0.1+	was freeze activated?
ARRAY	ROM	0.1+	8KiB of ROM data

src/plus4/cart/speedy.c

The CARTMAGIC module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	bankreg	0.1+	state of banking register
DWORD	filesize	0.1+	size of the ROM
ARRAY	ROM	0.1+	512KiB/1MiB/2MiB of ROM data

src/plus4/cart/magiccart.c

The CARTMULTI module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	bankreg	0.1+	state of banking register
DWORD	filesize	0.1+	size of the ROM (combined)
ARRAY	ROM C1LO	0.1+	1MiB/2MiB of ROM data
ARRAY	ROM C1HI	0.1+	1MiB/2MiB of ROM data

src/plus4/cart/multicart.c

The SCPU64GLUE module

Version numbers: Major 1, Minor 1.

Type	Name	Description
BYTE	glue_logic_type	
BYTE	old_vbank	
BYTE	glue_alarm_active	

src/scpu64/scpu64gluelogic.c

The CART128GENERIC module

Version numbers: Major 0, Minor 1.

Type	Name	Description
ARRAY	ext_function_rom	0x8000 bytes of ROM data

src/c128/cart/generic.c

The MAGICDESK128 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	md128reg	
BYTE	rombank	
BYTE	bankmask	
ARRAY	ext_function_rom	0x40*0x4000 bytes of ROM data

src/c128/cart/magicdesk128.c

The COMAL80C128 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	comal80_register	bank register value
ARRAY	ext_function_rom	(0x8000 * 5) + 0x4000 bytes of ROM data

src/c128/cart/comal80.c

The WARPSPEED128 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
ARRAY	ext_function_rom	0x4000 bytes of ROM data

src/c128/cart/warpspeed128.c

The CARTGMOD2C128 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	c128gmod2_bank	
ARRAY	flash_data	GMOD2_FLASH_SIZE

src/c128/cart/gmod2c128.c

The PARTNER128 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	regvalue	
BYTE	rambank	
BYTE	isdefreezing	
ARRAY	nmivector	2 bytes
ARRAY	ext_function_rom	PARTNER_ROM_SIZE
ARRAY	rambanks	PARTNER_RAM_SIZE

src/c128/cart/partner128.c

The VDC module

Version numbers: Major 0, Minor 0.

src/vdc/vdc-snapshot.c

The KEYBOARD module

Version numbers: Major 1, Minor 1.

Type	Name	Description
WORD ARRAY	keyarr	
WORD ARRAY	rev_keyarr	

src/keyboard.c

The TP_CP_CLOCK_F83 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	motor	motor state

src/tapeport/cp-clockf83.c

The TAPEPORT module

Type	Name	Description
BYTE	active	tapeport active flag
BYTE	id1	device id port 1
BYTE	id2	device id port 2

src/tapeport/tapeport.c

The TP_DTLBASIC module

Version numbers: Major 0, Minor 1.

Type	Name	Description
DWORD	counter	counter
DWORD	write	write line state
DWORD	sense	sense line state
DWORD	state	device state

src/tapeport/dtl-basic-dongle.c

The HREMEM module

Type	Name	Description
WORD	reg_E888	

src/pet/pethre.c

The C64DTVBLITTER module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	regs	32 BYTES of register data
DWORD	source A off	source A offset
DWORD	source B off	source B offset
DWORD	dest off	destination offset
DWORD	busy	busy flag
DWORD	IRQ	IRQ state
DWORD	on IRQ	on IRQ flag
DWORD	active	blitter active flag
ARRAY	source A data	4 BYTES of source A data
DWORD	source A data off	source A data offset
DWORD	source A fetched	source A fetched counter
ARRAY	source B data	4 BYTES of source B data
DWORD	source B data off	source B data offset
BYTE	source A	source A
BYTE	source B	source B
DWORD	count	blitter count
DWORD	state	blitter state
DWORD	source A line off	source A line offset
DWORD	source B line off	source B line offset
DWORD	dest line off	destination line offset
BYTE	last A	last A

src/c64dtv/c64dtvblitter.c

The C64DTVDMA module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	regs	32 BYTES of register data
DWORD	source off	source offset
DWORD	dest off	destination offset
DWORD	busy	DMA busy flag
DWORD	IRQ	DMA IRQ state
DWORD	on IRQ	on IRQ
DWORD	active	DMA active flag
BYTE	data	DMA data
BYTE	data swap	DMA data swap flag
DWORD	count	DMA counter
DWORD	state	DMA state
DWORD	source line off	source line offset
DWORD	dest line off	destination line offset
BYTE	source mem type	source memory type
BYTE	dest mem type	destination memory type

src/c64dtv/c64dtvdma.c

The HUMMERADC module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	hummeradc_value	
BYTE	hummeradc_channel	
BYTE	hummeradc_control	
BYTE	hummeradc_chanattr	
BYTE	hummeradc_chanwakeup	
BYTE	hummeradc_prev	

src/c64dtv/hummeradc.c

The MAINCPU module

Version numbers: Major 1, Minor 3.

Type	Name	Description
CLOCK	main clock	
UBYTE	a	
UBYTE	x	
UBYTE	y	
UBYTE	sp	
WORD	pc	
UBYTE	status	
BYTE	r3	
BYTE	r4	
BYTE	r5	
BYTE	r6	
BYTE	r7	
BYTE	r8	

BYTE	r9
BYTE	r10
BYTE	r11
BYTE	r12
BYTE	r13
BYTE	r14
BYTE	r15
BYTE	ACM
BYTE	YXM
4*BYTE	burst_cache
WORD	burst_addr
DWORD	dtvclockneg
DWORD	last_opcode_info
DWORD	ane_log_level
DWORD	lxa_log_level

src/maincpu.c

The VDRIVEIMAGE module

Version numbers: Major 2, Minor 0.

src/vdrive/vdrive-snapshot.c

The C64MEMHACKS module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	hacks	which memory hack is active

src/c64/c64-memory-hacks.c

The C64GLUE module

Version numbers: Major 1, Minor 1.

Type	Name	Description
BYTE	type	glue logic type
BYTE	old vbank	old video bank
BYTE	alarm active	alarm is active

src/c64/c64gluelogic.c

The C64VSIDROM module

Version numbers: Major 0, Minor 1.

Type	Name	Description
ARRAY	KERNAL	8192 BYTES of KERNAL ROM data
ARRAY	BASIC	8192 BYTES of BASIC ROM data
ARRAY	CHARGEN	4096 BYTES of CHARGEN ROM data

src/c64/vsidmemsnapshot.c

The C64VSIDMEM module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	pport data	0.0+	CPU port data register
BYTE	pport dir	0.0+	CPU port direction register
BYTE	EXROM	0.0+	EXROM line state
BYTE	GAME	0.0+	GAME line state
ARRAY	RAM	0.0+	65536 BYTES of RAM data
BYTE	pport data out	0.0+	CPU port data out lines state
BYTE	pport data read	0.0+	CPU port data in lines state
BYTE	pport dir read	0.0+	CPU port direction in lines state

src/c64/vsidmemsnapshot.c

The CARTFC3 module

Version numbers: Major 1, Minor 2.

Type	Name	Version	Description
BYTE	ROML banks	1.2	amount of ROML banks
BYTE	register	1.2	register
BYTE	reg enabled	0.0+	register enabled flag
ARRAY	ROML	1.1+	32768 or 131072 BYTES of ROML data
ARRAY	ROMH	1.1+	32768 or 131072 BYTES of ROML data

src/c64/cart/final3.c

The CARTDQBB module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	enabled	cartridge enabled flag
BYTE	read write	read/write flag
BYTE	a000 map	$A000 mapped flag (GAME line)
BYTE	off	dqbb off flag
BYTE	register	register
BYTE	exrom	state of EXROM line
BYTE	size	RAM size in kb
BYTE	bank	selected ram bank
ARRAY	RAM	<size> BYTES of RAM data

src/c64/cart/dqbb.c

The CARTEXOS module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	ROMH	8192 BYTES of ROMH data

src/c64/cart/exos.c

The CARTFCP module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	enable	cartridge enabled flag
BYTE	bit7	bit 7 of the register
BYTE	ROML bank	ROML bank
BYTE	ROMH bank	ROMH bank
ARRAY	ROML	16384 BYTES of ROML data
ARRAY	ROMH	8192 BYTES of ROMH data

src/c64/cart/finalplus.c

The CARTUC2 module

Version numbers: Major 0, Minor 2.

Type	Name	Description
BYTE	regA	bank register value
BYTE	regB	mode
BYTE	bankmask	bank mask
ARRAY	roml_banks	32 * 8192 bytes of ROML data
ARRAY	romh_banks	32 * 8192 bytes of ROMH data
ARRAY	cart_ram	512kiB of RAM data

src/c64/cart/uc2.c

The CARTZAXXON module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	ROML	8192 BYTES of ROML data
ARRAY	ROMH	16384 BYTES of ROMH data

src/c64/cart/zaxxon.c

The CARTMF module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	RAM page	current RAM page
BYTE	I/O-1 enable	I/O-1 enable flag
BYTE	kernal enable	kernal enable flag
BYTE	freeze enable	freeze enable flag
BYTE	export game	game line state
BYTE	hw version	hardware version
ARRAY	ROML	131072 BYTES of ROML data
ARRAY	RAM	8192 BYTES of RAM data
BYTE	CTRL A	A control register

BYTE	CTRL B	B control register
BYTE	DATA A	A data register
BYTE	DATA B	B data register
BYTE	DIR A	A direction register
BYTE	DIR B	B direction register
BYTE	CA2	CA2 line flag
BYTE	CA2 state	CA2 line state
BYTE	CB2	CB2 line flag
BYTE	CB2 state	CB2 line state

src/c64/cart/magicformel.c

The CARTSILVERROCK128 module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	regval	0.1	register
BYTE	bank	0.0+	current bank
ARRAY	ROML	0.0+	262144 BYTES of ROML data

src/c64/cart/silverrock128.c

The CARTKCS module

Version numbers: Major 0, Minor 3.

Type	Name	Version	Description
BYTE	config	0.2	current configuration
ARRAY	ROML	0.0+	8192 BYTES of ROML data
ARRAY	ROMH	0.0+	8192 BYTES of ROMH data
ARRAY	RAM	0.0+	128 BYTES of RAM data

src/c64/cart/kcs.c

The CARTOCEAN module

Version numbers: Major 1, Minor 0.

Type	Name	Description
BYTE	bank	current bank
BYTE	IO1 mask	I/O-1 mask
BYTE	register	register
DWORD	ROML size	ROML size
ARRAY	ROML	524288 BYTES of ROML data

src/c64/cart/ocean.c

The CARTP64 module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	bank	0.1	current bank
BYTE	register	0.1	register

ARRAY	ROML	0.0+	262144 BYTES of ROML data

src/c64/cart/prophet64.c

The CARTREXUTIL module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	active	0.1	cartridge active flag
ARRAY	ROML	0.0+	524288 BYTES of ROML data

src/c64/cart/rexutility.c

The CARTFREEZEF module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	ROM 8000	0.1	ROM $8000 active flag
BYTE	ROM E000	0.1	ROM $E000 active flag
ARRAY	ROML	0.0+	8192 BYTES of ROML data

src/c64/cart/freezeframe.c

The CARTIDE module

Version numbers: Major 0, Minor 0.

Type	Name	Description
WORD	version	IDE64 version
ARRAY	ROML	65536 (3.x) / 131072 (4.1) / 524288 (4.2) BYTES of ROML data
ARRAY	RAM	32768 BYTES of RAM data
DWORD	bank	current bank
DWORD	config	current config
DWORD	kill port	kill port flag
DWORD	idrive	idrive
WORD	in d030	input state of $d030 register
WORD	out d030	output state of $d030 register

src/c64/cart/ide64.c

The CARTSIMON module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	a000 enable	0.1	ROM at $A000 flag
ARRAY	ROML	0.0+	8192 BYTES of ROML data
ARRAY	ROMH	0.0+	8192 BYTES of ROMH data

src/c64/cart/simonsbasic.c

The CARTKINGSOFT module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	mode	mode
ARRAY	ROML	8192 BYTES of ROML data
ARRAY	ROMH	16384 BYTES of ROMH data

src/c64/cart/kingsoft.c

The CARTSE5 module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	bank	0.1	current bank
ARRAY	ROML	0.0+	16384 BYTES of ROML data

src/c64/cart/superexplode5.c

The CARTMACH5 module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	active	0.1	cartridge active flag
ARRAY	ROML	0.0+	8192 BYTES of ROML data

src/c64/cart/mach5.c

The CARTDIGIMAX module

Version numbers: Major 0, Minor 0.

Type	Name	Description
DWORD	base	base address of the control registers
ARRAY	sound data	4 BYTES of sound data
BYTE	voice 0	voice 0 data
BYTE	voice 1	voice 1 data
BYTE	voice 2	voice 2 data
BYTE	voice 3	voice 3 data

src/c64/cart/digimax.c

The CARTRRF module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	cart_enabled	cartridge active flag
BYTE	ram_enabled	RAM enabled at $8000
BYTE	ram_writeable	RAM is writeable
BYTE	ram_bank	currently selected RAM bank
ARRAY	ROML	8192 BYTES of ROML data
ARRAY	RAM	256k BYTES of RAM data

src/c64/cart/rexramfloppy.c

The CARTROSS module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	is32k	0.1	cart is 32KB flag
BYTE	bank	0.0+	current bank
ARRAY	ROML	0.0+	16384 BYTES of ROML data
ARRAY	ROMH	0.0+	16384 BYTES of ROMH data

src/c64/cart/ross.c

The CARTGS module

Version numbers: Major 1, Minor 1.

Type	Name	Description
BYTE	regval	bank register value
BYTE	currbank	current bank
ARRAY	roml_banks	64 * 8192 bytes of ROM data

src/c64/cart/gs.c

The CARTSFXSS module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	IO swap	0.1	VIC20 I/O swap flag
BYTE	sound data	0.0+	sound data

src/c64/cart/sfx_soundsampler.c

The CARTFFMK2 module

Version numbers: Major 0, Minor 0.

Type	Name	Version	Description
BYTE	ROML toggle	0.0+	ROML toggle flag
BYTE	freeze mode	0.0+	flag: are we in freeze mode
ARRAY	ROML	0.0+	8192 BYTES of ROML data
ARRAY	ROMH	0.0+	8192 BYTES of ROMH data

src/c64/cart/freezeframe2.c

The CARTWEST module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	ROM a000	0.1	ROM at $A000 flag
ARRAY	ROML	0.0+	8192 BYTES of ROML data
ARRAY	ROMH	0.0+	8192 BYTES of ROMH data

src/c64/cart/westermann.c

The CARTBB3 module

Version numbers: Major 1, Minor 0.

Type	Name	Version	Description
BYTE	bb3_rom_enabled	1.0	1 if ROM is enabled (informal)
ARRAY	ROML	1.0	8192 of ROML data

src/c64/cart/blackbox3.c

The CARTSNAP64 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	ROM config	ROM configuration
ARRAY	ROML	4096 BYTES of ROML data

src/c64/cart/snapshot64.c

The CARTUC1 module

Version numbers: Major 0 , Minor 2 .

Type	Name	Description
BYTE	regval	
BYTE	bankmask	
ARRAY	roml_banks	0x2000 * MAXBANKS
ARRAY	romh_banks	0x2000 * MAXBANKS
ARRAY	cart_ram	CART_RAM_SIZE

src/c64/cart/uc1.c

The CARTCOMAL module

Version numbers: Major 0, Minor 2.

Type	Name	Description
BYTE	register	control register
BYTE	extra rom	image contains extra eprom
BYTE	revision	hardware variant
ARRAY	ROML	32768 or 65536 BYTES of ROML data
ARRAY	ROMH	32768 or 65536 BYTES of ROMH data

src/c64/cart/comal80.c

The CARTMIDI module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	mode	midi mode

src/c64/cart/c64-midi.c

The CARTSFXSE module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	IO swap	0.1	VIC20 I/O swap flag

DWORD	chip type	0.0+	chip type
BYTE	sound command	0.0+	sound command
DWORD	ch 0 slot 0 ar	0.0+	channel 0, slot 0, ar
DWORD	ch 0 slot 0 dr	0.0+	channel 0, slot 0, dr
DWORD	ch 0 slot 0 rr	0.0+	channel 0, slot 0, rr
BYTE	ch 0 slot 0 KSR	0.0+	channel 0, slot 0, KSR
BYTE	ch 0 slot 0 ksl	0.0+	channel 0, slot 0, ksl
BYTE	ch 0 slot 0 ksr	0.0+	channel 0, slot 0, ksr
BYTE	ch 0 slot 0 mul	0.0+	channel 0, slot 0, mul
DWORD	ch 0 slot 0 Cnt	0.0+	channel 0, slot 0, Cnt
DWORD	ch 0 slot 0 Incr	0.0+	channel 0, slot 0, Incr
BYTE	ch 0 slot 0 FB	0.0+	channel 0, slot 0, FB
DWORD	ch 0 slot 0 connect1	0.0+	channel 0, slot 0, connect1
DWORD	ch 0 slot 0 op1 out 0	0.0+	channel 0, slot 0, op1 out 0
DWORD	ch 0 slot 0 op1 out 1	0.0+	channel 0, slot 0, op1 out 1
BYTE	ch 0 slot 0 CON	0.0+	channel 0, slot 0, CON
BYTE	ch 0 slot 0 eg type	0.0+	channel 0, slot 0, eg type
BYTE	ch 0 slot 0 state	0.0+	channel 0, slot 0, state
DWORD	ch 0 slot 0 TL	0.0+	channel 0, slot 0, TL
DWORD	ch 0 slot 0 TLL	0.0+	channel 0, slot 0, TLL
DWORD	ch 0 slot 0 volume	0.0+	channel 0, slot 0, volume
DWORD	ch 0 slot 0 sl	0.0+	channel 0, slot 0, sl
BYTE	ch 0 slot 0 eg sh ar	0.0+	channel 0, slot 0, eg sh ar
BYTE	ch 0 slot 0 eg sel ar	0.0+	channel 0, slot 0, eg sel ar
BYTE	ch 0 slot 0 eg sh dr	0.0+	channel 0, slot 0, eg sh dr
BYTE	ch 0 slot 0 eg sel dr	0.0+	channel 0, slot 0, eg sel dr
BYTE	ch 0 slot 0 eg sh rr	0.0+	channel 0, slot 0, eg sh rr
BYTE	ch 0 slot 0 eg sel rr	0.0+	channel 0, slot 0, eg sel rr
DWORD	ch 0 slot 0 key	0.0+	channel 0, slot 0, key
DWORD	ch 0 slot 0 AMmask	0.0+	channel 0, slot 0, AMmask
BYTE	ch 0 slot 0 vib	0.0+	channel 0, slot 0, vib
WORD	ch 0 slot 0 wavetable	0.0+	channel 0, slot 0, wavetable
DWORD	ch 0 slot 1 ar	0.0+	channel 0, slot 1, ar
DWORD	ch 0 slot 1 dr	0.0+	channel 0, slot 1, dr
DWORD	ch 0 slot 1 rr	0.0+	channel 0, slot 1, rr
BYTE	ch 0 slot 1 KSR	0.0+	channel 0, slot 1, KSR
BYTE	ch 0 slot 1 ksl	0.0+	channel 0, slot 1, ksl
BYTE	ch 0 slot 1 ksr	0.0+	channel 0, slot 1, ksr
BYTE	ch 0 slot 1 mul	0.0+	channel 0, slot 1, mul
DWORD	ch 0 slot 1 Cnt	0.0+	channel 0, slot 1, Cnt
DWORD	ch 0 slot 1 Incr	0.0+	channel 0, slot 1, Incr
BYTE	ch 0 slot 1 FB	0.0+	channel 0, slot 1, FB
DWORD	ch 0 slot 1 connect1	0.0+	channel 0, slot 1, connect1
DWORD	ch 0 slot 1 op1 out 0	0.0+	channel 0, slot 1, op1 out 0
DWORD	ch 0 slot 1 op1 out 1	0.0+	channel 0, slot 1, op1 out 1
BYTE	ch 0 slot 1 CON	0.0+	channel 0, slot 1, CON

src/c64/cart/sfx_soundexpander.c

The CARTDELAEP64 module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	regval	0.1	register
BYTE	bank	0.0+	current bank
ARRAY	ROML	0.0+	73728 BYTES of ROML data

src/c64/cart/delaep64.c

The CARTZIPP module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	active	0.1	cartridge active flag
DWORD	alarm	0.0+	alarm time
ARRAY	ROML	0.0+	8192 BYTES of ROML data

src/c64/cart/zippcode48.c

The CARTISEPIC module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	enabled	cartridge enabled flag
BYTE	switch	switch flag
BYTE	page	current page
ARRAY	RAM	2048 BYTES of RAM data

src/c64/cart/isepic.c

The C64CART module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	num carts	number of active cartridges
DWORD	mem_cartridge_type	
BYTE	game	
BYTE	exrom	
DWORD	romh_bank	
DWORD	roml_bank	
BYTE	export_ram	
BYTE	ultimax_phi1	
BYTE	ultimax_phi2	
CLOCK	cart_freeze_alarm_time	
CLOCK	cart_nmi_alarm_time	
BYTE	export_slot1.game	
BYTE	export_slot1.exrom	

BYTE	export_slot1.ultimax_phi1	
BYTE	export_slot1.ultimax_phi2	
BYTE	export_slotmain.game	
BYTE	export_slotmain.exrom	
BYTE	export_slotmain.ultimax_phi1	
BYTE	export_slotmain.ultimax_phi2	
BYTE	export_passthrough.game	
BYTE	export_passthrough.exrom	
BYTE	export_passthrough.ultimax_phi1	
BYTE	export_passthrough.ultimax_phi2	
DWORD	reserved	
DWORD	reserved	
DWORD	reserved	
DWORD	reserved	
ARRAY	cart ids	<num cart> IDs of active cartridges

src/c64/cart/c64carthooks.c

The CARTGENERICC64 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
ARRAY	ROML	8192 BYTES of ROML data
ARRAY	ROMH	8192 BYTES of ROMH data

src/c64/cart/c64-generic.c

The CARTMEGABYTER module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	register 0	register 0
BYTE	register 2	register 2
ARRAY	ROML	1048576 BYTES of ROML data

src/c64/cart/megabyter.c

The CARTCAPTURE module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	enabled	cartridge enabled flag
BYTE	freeze pressed	freeze button pressed flag
BYTE	register enable	register enable flag
BYTE	ROMH enable	ROMH enable flag
ARRAY	ROMH	8192 BYTES of ROMH data
ARRAY	RAM	8192 BYTES of RAM data

src/c64/cart/capture.c

The CARTAP module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	active	cartridge active flag
BYTE	ram at a000	RAM at $A000 flag
ARRAY	ROML	32768 BYTES of ROML data
ARRAY	RAM	8192 BYTES of RAM data

src/c64/cart/atomicpower.c

The CARTBB3 module

Version numbers: Major 1, Minor 0.

Type	Name	Version	Description
BYTE	bis_rom_enabled	1.0	1 if ROM is enabled (informal)
BYTE	bis_rom_kb	1.0	2/4/8 kb ROM
ARRAY	ROML	1.0	8192 of ROML data

src/c64/cart/bisplus.c

The CARTBMPDATATURBO module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	ROM 8000	0.1	ROM at $8000 flag
ARRAY	ROML	0.0+	8192 BYTES of ROML data
ARRAY	ROMH	0.0+	8192 BYTES of ROMH data

src/c64/cart/bmpdataturbo.c

The CARTMAGICD module

Version numbers: Major 0, Minor 2.

Type	Name	Description
BYTE	regval	bank register value
BYTE	bankmask	bank mask
ARRAY	roml_banks	128 * 8192 bytes of ROM data

src/c64/cart/magicdesk.c

The CARTDS12C887RTC module

Version numbers: Major 0, Minor 0.

Type	Name	Description
DWORD	base	base address of the RTC

src/c64/cart/ds12c887rtc.c

The CARTDELAEP7X8 module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	regval	0.1	register

BYTE	bank	0.0+	current bank
ARRAY	ROML	0.0+	65536 BYTES of ROML data

src/c64/cart/delaep7x8.c

The CARTSS4 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	RAM config	RAM configuration
BYTE	ROM config	ROM configuration
ARRAY	ROML	16384 BYTES of ROML data
ARRAY	ROMH	16384 BYTES of ROMH data
ARRAY	RAM	8192 BYTES of RAM data

src/c64/cart/supersnapshot4.c

The CARTSS5 module

Version numbers: Major 0, Minor 3.

Type	Name	Version	Description
BYTE	ROM bank	0.2	current ROM bank
BYTE	register	0.2	register
BYTE	ROM config	0.0+	ROM configuration
BYTE	RAM bank	0.0+	current RAM bank
BYTE	32K enabled	0.1+	32KB enabled flag
BYTE	ROM disable	0.1+	ROM disable flag
BYTE	ROM banks	0.3	number of ROM banks (4 or 8)
ARRAY	ROML	0.0+	0x8000 or 0x10000 BYTES of ROML data
ARRAY	ROMH	0.0+	0x8000 or 0x10000 BYTES of ROMH data
ARRAY	RAM	0.0+	32768 BYTES of RAM data

src/c64/cart/supersnapshot.c

The CARTF64 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	enabled	cartridge enabled flag
ARRAY	ROMH	32768 BYTES of ROMH data
BYTE	CTRL A	control register A
BYTE	CTRL B	control register B
BYTE	DATA A	data register A
BYTE	DATA B	data register B
BYTE	DIR A	direction register A
BYTE	DIR B	direction register B
BYTE	CA2	CA2 line flag
BYTE	CA2 state	CA2 line state

BYTE	CB2	CB2 line flag
BYTE	CB2 state	CB2 line state

src/c64/cart/formel64.c

The CARTSTARDOS module

Version numbers: Major 0, Minor 1.

Type	Name	Description
DWORD	stardos_alarm_time	alarm time for cap discharge
DWORD	cap_voltage	cap voltage
BYTE	roml_enable	is roml enabled?
ARRAY	roml_banks	8192 bytes of ROML data
ARRAY	romh_banks	8192 bytes of ROMH data

src/c64/cart/stardos.c

The CARTAR3 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	active	cartridge active flag
BYTE	register	cartridge register
ARRAY	ROML	16768 BYTES of ROML data

src/c64/cart/actionreplay3.c

The CARTMULTIMAX module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	currbank	current ROM bank
BYTE	reg_enabled	flag, is the register enabled?
ARRAY	ROML	512K of ROML data
ARRAY	ROMH	512K of ROMH data
ARRAY	RAM	2048 BYTES of RAM data

src/c64/cart/multimax.c

The CARTFUNPLAY module

Version numbers: Major 1, Minor 1.

Type	Name	Version	Description
BYTE	regval	1.1	register
BYTE	bank	0.0+	current bank
ARRAY	ROML	1.0+	131072 BYTES of ROML data

src/c64/cart/funplay.c

The CARTBLACKBOX9 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	currmode	cartridge mode
BYTE	currbank	selected bank
ARRAY	ROML	8192 bytes of ROM data
ARRAY	ROMH	8192 bytes of ROM data

src/c64/cart/blackbox9.c

The CARTREXEP256 module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	regval	0.1	register
ARRAY	EPROM sizes	0.0+	8 WORDS of EPROM sizes in BYTES
ARRAY	ROML offsets	0.0+	8 BYTES of EPROM ofsets
ARRAY	ROML	0.0+	270336 BYTES of ROML data

src/c64/cart/rexep256.c

The REU1764 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
DWORD	size	size of REU in KB
ARRAY	registers	16 BYTES of register data
ARRAY	RAM	131072, 262144, 524288, 1048576, 2097152, 4194304, 8388608 or 16777216 BYTES of RAM data

src/c64/cart/reu.c

The CARTMD16 module

Version numbers: Major 0, Minor 2.

Type	Name	Description
BYTE	regval	bank register value
BYTE	bankmask	bank mask
ARRAY	roml_banks	128 * 8192 bytes of ROML data
ARRAY	romh_banks	128 * 8192 bytes of ROMH data

src/c64/cart/magicdesk16.c

The SHORTBUSDIGIMAX module

Version numbers: Major 0, Minor 0.

Type	Name	Description
DWORD	base	base address
ARRAY	sound data	4 BYTES of sound data
BYTE	voice 0	voice 0 state
BYTE	voice 1	voice 1 state

BYTE	voice 2	voice 2 state
BYTE	voice 3	voice 3 state

src/c64/cart/shortbus_digimax.c

The CARTFINALV1 module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	active	0.1	cartridge active flag
ARRAY	ROML	0.0+	8192 BYTES of ROML data
ARRAY	ROMH	0.0+	8192 BYTES of ROMH data

src/c64/cart/final.c

The CARTEASYCALC module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	ROML	8192 BYTES of ROML data
ARRAY	ROMH	16384 BYTES of ROMH data

src/c64/cart/easycalc.c

The CARTWARP module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	ROM 8000	0.1	ROM at $8000 flag
ARRAY	ROML	0.0+	8192 BYTES of ROML data
ARRAY	ROMH	0.0+	8192 BYTES of ROMH data

src/c64/cart/warpspeed.c

The CARTHYPERBASIC module

Version numbers: Major 0, Minor 0.

Type	Name	Version	Description
BYTE	regval	0.0	register
ARRAY	ROML	0.0	65536 BYTES of ROML data

src/c64/cart/hyperbasic.c

The CARTBB8 module

Version numbers: Major 1, Minor 1.

Type	Name	Version	Description
BYTE	ROML banks	1.1	amount of ROML banks
BYTE	register	1.1	register
ARRAY	ROML	1.1	32768 or 65536 BYTES of ROML data
ARRAY	ROMH	1.1	32768 or 65536 BYTES of ROML data

src/c64/cart/blackbox8.c

The CARTDSM module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	active	0.1	cartridge active flag
ARRAY	ROML	0.0+	8192 BYTES of ROML data

src/c64/cart/diashowmaker.c

The CARTAR2 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	enabled	cartidge enabled flag
DWORD	cap enable	capacitor enable counter
DWORD	cap disable	capacitor disable counter
ARRAY	ROML	16768 BYTES of ROML data

src/c64/cart/actionreplay2.c

The CARTAR module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	active	cartridge is active
ARRAY	ROML	32768 BYTES of ROML data
ARRAY	ROMH	32768 BYTES of ROMH data
ARRAY	RAM	8192 BYES of RAM data

src/c64/cart/actionreplay.c

The CARTAR4 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	active	cartridge active flag
ARRAY	ROML	32768 BYTES of ROML data

src/c64/cart/actionreplay4.c

The CARTGMOD3 module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	gmod3_cmode	
BYTE	gmod3_bank	
ARRAY	gmod3_rom	GMOD3_16MB_FLASH_SIZE

src/c64/cart/gmod3.c

The CARTEPYX module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	active	0.1	cartridge active flag
DWORD	alarm	0.0+	alarm time
ARRAY	ROML	0.0+	8192 BYTES of ROML data

src/c64/cart/epyxfastload.c

The CARTRR module

Version numbers: Major 0, Minor 3.

Type	Name	Version	Description
BYTE	revision	0.1+	RR revision
BYTE	active	0.0+	cartridge active flag
BYTE	frozen	0.2+	frozen flag
BYTE	cmode	0.3	cmode
BYTE	clockport enabled	0.0+	clockport enabled flag
BYTE	bank	0.0+	current bank
BYTE	write once	0.0+	write once flag
BYTE	allow bank	0.0+	allow bank flag
BYTE	no freeze	0.0+	no freeze flag
BYTE	REU mapping	0.0+	REU mapping flag
BYTE	RAM at a000	0.1+	RAM at $A000 flag
BYTE	flash jumper	0.0+	flash jumper state
BYTE	bank jumper	0.0+	bank jumper state
BYTE	ROM offset	0.0+	ROM offset
ARRAY	ROML	0.0+	131072 BYTES of ROML data
ARRAY	RAM	0.0+	32768 BYTES of RAM data

src/c64/cart/retroreplay.c

The CARTGMOD2 module

Version numbers: Major 0, Minor 2.

Type	Name	Description
BYTE	gmod2_cmode	
BYTE	gmod2_bank	
ARRAY	flash_data	GMOD2_FLASH_SIZE

src/c64/cart/gmod2.c

The CARTLTK module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	rom	ltk_rom
BYTE	raml	ltk_raml
BYTE	ramh	ltk_ramh
BYTE	ramwrite	ltk_ramwrite
BYTE	freeze	ltk_freeze

BYTE	on	ltk_on
BYTE	in2	ltk_in2
BYTE	io	ltk_io
BYTE	port	ltk_port
DWORD	alarm_time	ltk_alarm_time
ARRAY	ROML	8192 BYTES of ROML data (boot rom, $8000-$9FFF)
ARRAY	export_ram0	16384 BYTES of export RAM data (RAML & RAMH)
MC6821	SNAPSHOT6821	ltk_6821
SCSI	SNAPSHOTSCSI	ltk_scsi

src/c64/cart/ltkernal.c

The CARTGK module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	disable	cartridge disable flag
ARRAY	ROMH	8192 BYTES of ROMH data

src/c64/cart/gamekiller.c

The CARTSTB module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	bank	0.1	current bank
BYTE	active	0.1	cartridge active flag
ARRAY	ROML	0.0+	16384 BYTES of ROML data

src/c64/cart/stb.c

The CARTMDP module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	regval	bank register value
BYTE	bankmask	bank mask
ARRAY	roml_banks	128 * 8192 bytes of ROM data
BYTE	revision	hardware revision
BYTE	page_reg	page register value
BYTE	ctrl_reg	control register value
DWORD	sram_size	SRAM size (0 means none)
ARRAY	sram_ram	SRAM data (128k bytes)
DWORD	eeprom_size	EEPROM size (0 means none)
ARRAY	eeprom data	EEPROM data (8k or 32k)

src/c64/cart/magicdeskplus.c

The CARTPROFIDOS module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	ROMH	$4000 BYTES of ROMH data

src/c64/cart/profidos.c

The CARTMMC64 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	clockport enable	clockport enable flag
WORD	clockport address	clockport address
BYTE	bios write	bios writable flag
BYTE	bit 7 unlocked	bit 7 unlocked flag
ARRAY	unlocking	2 BYTES of unlocking data
BYTE	bios changed	bios changed flag
BYTE	flash jumper	flash jumper state
BYTE	write protect	write protect flag
BYTE	active	cartridge active flag
BYTE	SPI mode	SPI mode
BYTE	extrom	extrom flag
BYTE	flash mode	flash mode
BYTE	cport	cport
BYTE	speed	speed
BYTE	cardsel	cardsel
BYTE	biossel	biossel
BYTE	extexrom	EXROM line state
BYTE	extgame	GAME line state
BYTE	revision	MMC64 revision
BYTE	SD type	SD type
BYTE	image readonly	image read-only flag
ARRAY	BIOS	8194 BYTES of BIOS data
BYTE	BIOS offset	BIOS offset
BYTE	BIOS type	BIOS type

src/c64/cart/mmc64.c

The CARTDREAN module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	regval	bank register value
BYTE	bankmask	bank mask
ARRAY	roml_banks	4 * 8192 bytes of ROML data

src/c64/cart/drean.c

The CARTTURTLEGFX module

Version numbers: Major 0, Minor 2.

Type	Name	Version	Description
BYTE	bank	0.1	current bank
BYTE	active	0.1	cartridge active flag
ARRAY	ROML	0.0+	16384 BYTES of ROML data

src/c64/cart/turtlegraphics.c

The CARTRAMCART module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	enabled	cartridge enabled flag
BYTE	readonly	read-only flag
DWORD	BSIZE	RAM size in BYTES
BYTE	KBSIZE	RAM size in KB
ARRAY	registers	2 BYTES of register data
ARRAY	RAM	65536 or 131072 BYTES of RAM data

src/c64/cart/ramcart.c

The CARTDINAMIC module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	bank	current bank
ARRAY	ROML	8192 BYTES of ROML data

src/c64/cart/dinamic.c

The CARTEF module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	jumper	jumper
BYTE	register 0	register 0
BYTE	register 2	register 2
ARRAY	RAM	256 BYTES of RAM data
ARRAY	ROML	524288 BYTES of ROML data
ARRAY	ROMH	524288 BYTES of ROMH data

src/c64/cart/easyflash.c

The CARTMAXBASIC module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	ROML	8192 BYTES of ROML data
ARRAY	ROMH	8192 BYTES of ROMH data
ARRAY	RAM	2048 BYTES of RAM data

src/c64/cart/maxbasic.c

The CARTPARTNER64 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	active	cartridge is active
ARRAY	ROML	8192 BYTES of ROML data
ARRAY	ROMH	8192 BYTES of ROMH data
ARRAY	RAM	8192 BYES of RAM data

src/c64/cart/partner64.c

The SHORTBUS module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	amount	amount of active shortbus devices
BYTE	digimax active	digimax active flag
BYTE	duart active	duart active flag
BYTE	etfe active	etfe active flag
BYTE	eth64 active	eth64 active flag

src/c64/cart/shortbus.c

The CARTSDBOX module

Version numbers: Major 1, Minor 0.

Type	Name	Version	Description
BYTE	regval	1.0	register
BYTE	currbank	1.0	current bank
BYTE	reg_enable	1.0	flag: is the register enabled
BYTE	regs	1.0	4 bytes of "RAM"
ARRAY	ROML	1.0	8*8k BYTES of ROML data
ARRAY	ROMH	1.0	8*8k BYTES of ROMH data

src/c64/cart/sdbox.c

The CARTEXPERT module

Version numbers: Major 0, Minor 0.

Type	Name	Description
BYTE	mode	cartridge mode
BYTE	register enable	register enable flag
BYTE	RAM writable	RAM writable flag
BYTE	RAMH enable	RAMH enable flag
ARRAY	RAM	8192 BYTES of RAM data

src/c64/cart/expert.c

The CARTPAGEFOX module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description

BYTE	enabled	0.1	cartridge enabled flag
BYTE	bank	0.0+	current bank
ARRAY	RAM	0.0+	32768 BYTES of RAM data
ARRAY	ROML	0.0+	32768 BYTES of ROML data
ARRAY	ROMH	0.0+	32768 BYTES of ROMH data

src/c64/cart/pagefox.c

The CARTRGCD module

Version numbers: Major 0, Minor 4.

Type	Name	Version	Description
BYTE	regval	0.1+	register
BYTE	disabled	0.2	cartridge disabled flag
BYTE	revision	0.3	hw revision
BYTE	mask	0.4	bank mask
ARRAY	ROML	0.1+	65536 BYTES of ROML data

src/c64/cart/rgcd.c

The CARTRAMLINK module

Version numbers: Major 0, Minor 0.

Type	Name	Description
INT32	cardsizemb	rl_cardsizemb
UINT32	rombase	rl_rombase
UINT32	kernbase	rl_kernbase
UINT32	rambase	rl_rambase
UINT32	cardaddr	rl_cardaddr
UINT32	io1mode	rl_io1mode
UINT32	reu_trap	rl_reu_trap
BYTE	on	rl_on
BYTE	dos	rl_dos
ARRAY	i8255a_i	3 internal inputs for I8255A
ARRAY	i8255a_o	3 internal outputs for I8255A
ARRAY	rom	rl_rom 64 KiB for firmware
ARRAY	ram	rl_ram 8 KiB for SRAM
I8255A	SNAPSHOTI8255A	rl_8255
ARRAY	rl_card	Contents of RAMCARD (cardsibemb) in MiB

src/c64/cart/ramlink.c

The CARTSUPERGAMES module

Version numbers: Major 0, Minor 2.

Type	Name	Version	Description
BYTE	mode	0.2	current mode
BYTE	regval	0.2	register
BYTE	bank	0.0+	current bank

BYTE	reg latched	0.1	register latched flag
ARRAY	ROML	0.0+	32768 BYTES of ROML data
ARRAY	ROMH	0.0+	32768 BYTES of ROMH data

src/c64/cart/supergames.c

The CARTFREEZEM module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	ROM A14	0.0+	A14 line state
BYTE	ROML toggle	0.0+	ROML toggle flag
BYTE	allow toggle	0.1	allow toggle flag
ARRAY	ROML	0.0+	16384 BYTES of ROML data
ARRAY	ROMH	0.0+	16384 BYTES of ROMH data

src/c64/cart/freezemachine.c

The CPMCART module

Version numbers: Major 0, Minor 0.

Type	Name	Description
DWORD	CLK	main CPU clock
BYTE	A	A register
BYTE	B	B register
BYTE	C	C register
BYTE	D	D register
BYTE	E	E register
BYTE	F	F register
BYTE	H	H register
BYTE	L	L register
BYTE	IXH	IXH register
BYTE	IXL	IXL register
BYTE	IYH	IYH register
BYTE	IYL	IYL register
WORD	SP	stack pointer register
DWORD	PC	program counter register
BYTE	I	I register
BYTE	R	R register
BYTE	IFF1	IFF1 register
BYTE	IFF2	IFF2 register
BYTE	im mode	im mode flag
BYTE	A2	A2 register
BYTE	B2	B2 register
BYTE	C2	C2 register
BYTE	D2	D2 register
BYTE	E2	E2 register
BYTE	F2	F2 register
BYTE	H2	H2 register

BYTE	L2	L2 register
DWORD	opcode info	last opcode info
DWORD	opcode address	last opcode address

src/c64/cart/cpmcart.c

The GEORAM module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	io swap	0.1	VIC20 I/O swap flag
DWORD	size	0.0+	size in KiB
ARRAY	regs	0.0+	2 BYTES of register data
ARRAY	RAM	0.0+	65536, 131072, 262144, 524288, 1048576, 2097152 or 4194304 BYTES of RAM data

src/c64/cart/georam.c

The CARTDELAEP256 module

Version numbers: Major 0, Minor 1.

Type	Name	Version	Description
BYTE	regval	0.1	register
BYTE	bank	0.0+	current bank
ARRAY	ROML	0.0+	262144 BYTES of ROML data

src/c64/cart/delaep256.c

The CARTBB4 module

Version numbers: Major 1, Minor 0.

Type	Name	Version	Description
BYTE	bb4_rom_enabled	1.0	1 if ROM is enabled (informal)
ARRAY	ROML	1.0	8192 of ROML data
ARRAY	ROMH	1.0	8192 of ROMH data

src/c64/cart/blackbox4.c

The CARTIEEEFLASH64 module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	ROMH	8192 BYTES of ROMH data

src/c64/cart/ieeeflash64.c

The CARTMIKROASS module

Version numbers: Major 0, Minor 0.

Type	Name	Description
ARRAY	ROML	8192 BYTES of ROML data

src/c64/cart/mikroass.c

The PLUS256K module

Version numbers: Major 0, Minor 1.

Type	Name	Description
BYTE	register	register
BYTE	video bank	current video bank
BYTE	low bank	current low bank
BYTE	high bank	current high bank
BYTE	write protect	write protect flag
ARRAY	RAM	262144 BYTES of RAM data

src/c64/plus256k.c

The C64_256K module

Version numbers: Major 0, Minor 0.

Type	Name	Description
WORD	base	base address of the control registers
BYTE	DDA	register A direction
BYTE	PRA	register A data
BYTE	CRA	register A control
BYTE	DDB	register B direction
BYTE	PRB	register B data
BYTE	CRB	register B control
BYTE	vbank	video bank
BYTE	segment0	segment 0 bank
BYTE	segment1	segment 1 bank
BYTE	segment2	segment 2 bank
BYTE	segment3	segment 3 bank
ARRAY	RAM	262144 BYTES of RAM data

src/c64/c64_256k.c

The PLUS60K module

Version numbers: Major 0, Minor 1.

Type	Name	Description
WORD	base	base address of register
BYTE	register	register
ARRAY	RAM	61440 BYTES of RAM data

src/c64/plus60k.c

10 Media images

10.1 Media images resources

`OCPOversizeHandling`
: Integer specifying the way the oversized input should be handled (all emulators except vsid). (0: scale down, 1: crop left top, 2: crop center top, 3: crop right top, 4: crop left center, 5: crop center, 6: crop right center, 7: crop left bottom, 8: crop center bottom, 9: crop right bottom)

`OCPUndersizeHandling`
: Integer specifying the way the undersized input should be handled (all emulators except vsid). (0: scale, 1: borderize)

`OCPMultiColorHandling`
: Integer specifying the way the multicolor to hires should be handled (all emulators except vsid). (0: b&w, 1: 2 colors, 2: 4 colors, 3: gray scale, 4: best cell colors)

`OCPTEDLumHandling`
: Integer specifying the way the TED luminosity should be handled (all emulators except vsid). (0: ignore, 1: dither)

`KoalaOversizeHandling`
: Integer specifying the way the oversized input should be handled (all emulators except vsid). (0: scale down, 1: crop left top, 2: crop center top, 3: crop right top, 4: crop left center, 5: crop center, 6: crop right center, 7: crop left bottom, 8: crop center bottom, 9: crop right bottom)

`KoalaUndersizeHandling`
: Integer specifying the way the undersized input should be handled (all emulators except vsid). (0: scale, 1: borderize)

`KoalaTEDLumHandling`
: Integer specifying the way the TED luminosity should be handled (all emulators except vsid). (0: ignore, 1: dither)

`MinipaintOversizeHandling`
: Integer specifying the way the oversized input should be handled (all emulators except vsid). (0: scale down, 1: crop left top, 2: crop center top, 3: crop right top, 4: crop left center, 5: crop center, 6: crop right center, 7: crop left bottom, 8: crop center bottom, 9: crop right bottom)

`MinipaintUndersizeHandling`
: Integer specifying the way the undersized input should be handled (all emulators except vsid). (0: scale, 1: borderize)

`MinipaintTEDLumHandling`
: Integer specifying the way the TED luminosity should be handled (all emulators except vsid). (0: ignore, 1: dither)

`FFMPEGFormat`
String specifying the current FFMPEG output driver.

`FFMPEGAudioBitrate`
Integer specifying the current FFMPEG audio bitrate.

`FFMPEGVideoBitrate`
Integer specifying the current FFMPEG video bitrate.

`FFMPEGAudioCodec`
Integer specifying the current FFMPEG audio codec.

`FFMPEGVideoCodec`
Integer specifying the current FFMPEG video codec.

`FFMPEGVideoHalveFramerate`
Boolean, if true record only every other frame.

`ZMBVFormat`
String specifying the current ZMBV output driver.

`ZMBVAudioCodec`
Integer specifying the current ZMBV audio codec.

`ZMBVVideoCodec`
Integer specifying the current ZMBV video codec.

10.2 Media images command-line options

`-ocpoversize <method>`
Select the way the oversized input should be handled (`OCPOversizeHandling`) (all emulators except vsid). (0: scale down, 1: crop left top, 2: crop center top, 3: crop right top, 4: crop left center, 5: crop center, 6: crop right center, 7: crop left bottom, 8: crop center bottom, 9: crop right bottom)

`-ocpundersize <method>`
Select the way the undersized input should be handled (`OCPUndersizeHandling`) (all emulators except vsid). (0: scale, 1: borderize)

`-ocpmc <method>`
Select the way the multicolor to hires should be handled (`OCPMultiColorHandling`) (all emulators except vsid). (0: b&w, 1: 2 colors, 2: 4 colors, 3: gray scale, 4: best cell colors)

`-ocptedlum <method>`
Select the way the TED luminosity should be handled (`OCPTEDLumHandling`) (all emulators except vsid). (0: ignore, 1: dither)

`-koalaoversize <method>`
Select the way the oversized input should be handled (`KoalaOversizeHandling`) (all emulators except vsid). (0: scale down, 1: crop left top, 2: crop center top, 3: crop right top, 4: crop left center, 5: crop center, 6: crop right center, 7: crop left bottom, 8: crop center bottom, 9: crop right bottom)

`-koalaundersize <method>`
: Select the way the undersized input should be handled (`KoalaUndersizeHandling`) (all emulators except vsid). (0: scale, 1: borderize)

`-koalatedlum <method>`
: Select the way the TED luminosity should be handled (`KoalaTEDLumHandling`) (all emulators except vsid). (0: ignore, 1: dither)

`-minipaintoversize <method>`
: Select the way the oversized input should be handled (`MinipaintOversizeHandling`) (all emulators except vsid). (0: scale down, 1: crop left top, 2: crop center top, 3: crop right top, 4: crop left center, 5: crop center, 6: crop right center, 7: crop left bottom, 8: crop center bottom, 9: crop right bottom)

`-minipaintundersize <method>`
: Select the way the undersized input should be handled (`MinipaintUndersizeHandling`) (all emulators except vsid). (0: scale, 1: borderize)

`-minipainttedlum <method>`
: Select the way the TED luminosity should be handled (`MinipaintTEDLumHandling`) (all emulators except vsid). (0: ignore, 1: dither)

`-ffmpegaudiobitrate <value>`
: Set bitrate for audio stream in media file

`-ffmpegvideobitrate <value>`
: Set bitrate for video stream in media file

11 Event history

VICE supports recording an arbitrary session on the emulated machine and playing back this session later. This is done by saving a snapshot at the beginning of the recording session and then remembering all the user interaction such as keyboard and joystick input. We call this an 'event history'. The main purpose for having an event history is to create game sessions, e.g. recording how to solve a game. An example walkthrough for the well known game "Fort Apocalypse" is available.

11.1 Recommended Settings

When using the event history feature it is possible that the playback session differs from what was done at recording time. This might arise due to a problem in the initial snapshot or settings. Here are some suggestions to minimize the chance of failures in the session: a. Reset to default settings. b. Choose refresh rate 1/1. c. Choose joystick and Video/Doublesize settings as desired or needed. d. SID engine must be reSID (which is the default). e. Choose Drive settings/Idle method: None Do not change any settings during recording or playback!

11.2 Recorded Events

The following is a list of the user interaction that will be recorded: - Joystick movement and button - Keyboard - Resetting the machine (Reset and power cycle) - Attaching or detaching disk/tape images (see 8. Limitations) - Datasette controls

11.3 Recording an Event History

Recording an event history will create one or two files for a snapshot and the list of the user events. First, create an empty directory in which these files are to be saved and then select this directory and the name of the snapshot files via 'Snapshot//Select History files/directory'. Next, attach the disk or tape image with the game you want to record and load and run the game.

Start recording via 'Snapshot//Start/Stop Recording History'. Play the game. All actions will be recorded. After the game is finished, stop recording via 'Snapshot//Start/Stop Recording History'. The selected directory should now contain the two snapshot files (default ist start.vsf and end.vsf).

11.4 Setting and Returning to Milestones

It is difficult to finish a game in one sitting and noone wants to record all their mistakes and lost lives. Use the milestone feature in a recording session in the following way:

Set a milestone when you have finished a level or completed a task ('Snapshot//Set recording milestone' or ALT-E). This will save the event history and a snapshot of the machine to the file end.vsf but recording will continue.

Return to the last milestone when you have made a mistake or lost a life ('Snapshot//Return to milestone' or ALT-U). This will reset the game and the event history to the last milestone snapshot so that you can try again.

11.5 Continuing an Event History

If you have stopped a recording session and want to continue it later, you should create a backup of your start.vsf and end.vsf files first to avoid overwriting them by accident.

Then change the event history start mode: 'Snapshot//Recording start mode//Load existing snapshot'. When you start recording now, you will continue where the session was stopped last time.

Another way of continuing an existing history is to start somewhere inside the history (e.g. you have recorded 10 minutes of a game and later recognize that you made a mistake after 6 minutes that makes it impossible to finish the game). For this you have to select the start mode 'Overwrite playback'. Now you can start the playback with 'Snapshot//Start/Stop Playback History' and when you reach the point where you want to change the history you can directly switch to recording via 'Snapshot//Start/Stop Recording History'.

11.6 Playing Back an Event History

To play back an event history, select the directory with the history files start.vsf and end.vsf via 'Snapshot//Select History directory' and start the playback with 'Snapshot//Start/Stop Playback History'. Enjoy! During playback any user interaction is disabled. The playback stops when the end of the session is reached or if 'Snapshot//Select History directory' is selected again.

11.7 Limitations and Suggestions

a. Snapshot files will be quite big (>1MiB) if a disk image has been attached. If possible, use PRG or T64 images to reduce the size of snapshot files. b. Snapshots may not be 100% accurate even with all the recommended settings.

11.8 Event history resources

`EventSnapshotDir`
: String specifying the directory used for loading and saving snapshots (all emulators except vsid).

`EventStartSnapshot`
: String specifying the filename for the start snapshot (all emulators except vsid).

`EventEndSnapshot`
: String specifying the filename for the end snapshot (all emulators except vsid).

`EventStartMode`
: Integer specifying how to start event recording (all emulators except vsid). (0: save new snapshot, 1: load existing snapshot, 2: reset, 3: playback)

`EventImageInclude`
: Boolean specifying whether to include ROM and Disk images in the snapshots (all emulators except vsid).

11.9 Event history command-line options

`-playback`
: Playback recorded events (all emulators except vsid).

`-eventsnapshotdir <Name>`
: Set event snapshot directory (`EventSnapshotDir`) (all emulators except vsid).

`-eventstartsnapshot <Name>`
: Set event start snapshot filename (`EventStartSnapshot`) (all emulators except vsid).

`-eventendsnapshot <Name>`
: Set event end snapshot filename (`EventEndSnapshot`) (all emulators except vsid).

`-eventstartmode <Mode>`
: Set event start mode (`EventStartMode`) (all emulators except vsid). (0: save new snapshot, 1: load existing snapshot, 2: reset, 3: playback)

`-eventimageinc`
`+eventimageinc`
: Enable/disable the inclusion of disk images in the event (`EventImageInclude=1`, `EventImageInclude=0`) (all emulators except vsid).

12 Monitor

The VICE emulator has a complete built-in monitor, which can be used to examine, disassemble and assemble machine language programs, as well as debug them through breakpoints. It can be activated by using "Activate monitor" from the menu. The monitor will either run in a seperate window, or in a terminal emulation program (such as `rxvt` or `xterm`) when "native monitor" has been enabled in the settings.

12.1 Terminology

'`address_space`'
: This refers to the range of memory locations and a set of registers. This can be the addresses available to the computer's processor, the disk drive's processor or a specific memory configuration of one of the mentioned processors.

'`bankname`'
: The CPU can only see 64KiB of memory at any one time, due to its 16 bit address bus. The C64 and other computers have more than this amount, and this is handled by banking: a memory address can have different contents, depending on the active memory bank. A bankname names a specific bank in the current address_space.

'`register`'
: One of the following: program counter (PC), stack pointer (SP), accumulator (A), X register (X), or Y register (Y).

'`address`' A specific memory location in the range $0000 to $FFFF.

'`address_range`'
: Two addresses. If the second address is less than the first, the range is assumed to wraparound from $FFFF to $0000. Both addresses must be in the same address space.

'`address_opt_range`'
: An address or an address range.

'`radix_type`'
: Radix for the output, H for hex, D for decimal, O for octal, or B for binary.

'`label`' `label` is the name of a label. It must start with a dot (`"."`) in order for the monitor to recognize it as a label. Register names preceeded by a dot (for example `.PC`) are special labels that evaluate to value of the respective register at the time it is used, and thus can not be used as a regular label.

'`prompt`' The prompt has the format (address space:current address).

'`checkpoint`'
: The monitor has the ability to setup triggers that perform an action when a specified situation occurs. There are three types of checkpoints; breakpoints, tracepoints and watchpoints.

'`breakpoint`'
: A breakpoint is triggered based on the program counter. When it is triggered, the monitor is entered.

‘`tracepoint`’
: Like breakpoints, a tracepoint is triggered based on the program counter. Instead of entering the monitor, the program counter is printed and execution continues.

‘`watchpoint`’
: Watchpoints are triggered by a read and/or write to an address. When a watchpoint is triggered, the monitor is entered.

‘`memmap`’
: The memmap keeps track of RAM/ROM/IO read/write/execute accesses. The feature must be enabled with "–enable-cpuhistory" configure option, as it might decrease performance notably on slower hardware. The option also enables CPU history.

‘`<...>`’
: A data type.

‘`*`’
: Zero or more occurrences.

‘`[...]`’
: An optional argument.

12.2 General commands

`help [<command>]`
: If no argument is given, prints out a list of all available commands. If an argument is given, prints out specific help for that command.

`print <expression>`
`p <expression>`
: Evaluate the specified expression and output the result.

`~ <number>`
: Display the specified number in decimal, hex, octal and binary.

12.3 Monitor state commands

`device [c:|8:|9:|10:|11:]`
`dev [c:|8:|9:|10:|11:]`
: Set the default address space to either the computer (`c:`) or the disk (`8:|9:|10:|11:`). Switches to computer when not given a device.

`sidefx [on|off|toggle]`
`sfx [on|off|toggle]`
: Control how monitor generated reads affect memory locations that have read side-effects, like CIA interrupt registers for example. If the argument is 'on' then reads may cause side-effects. If the argument is 'off' then reads don't cause side-effects. If the argument is 'toggle' then the current mode is switched. No argument displays the current state.

`log [on|off|toggle]`
: Control whether the monitor output is logged into a logfile. If the argument is 'on' then all output will be written into the logfile. If the argument is 'off' then no log is produced. If the argument is 'toggle' then the current mode is switched. No argument displays the current state.

`logname "<filename>"`
: Sets the filename of the logfile.

`exit`
`x`
: Leave the monitor and return to execution.

`quit`
`q`
: Exit the emulator immediately.

`radix [H|D|O|B]`
`rad [H|D|O|B]`
: Set the default radix to hex, decimal, octal, or binary. With no argument, the current radix is printed.

12.4 Machine state commands

`backtrace`
`bt`
: Print JSR call chain (most recent call first). Stack offset relative to SP+1 is printed in parentheses. This is a best guess only.

`bank [<bankname>]`
: Without a bankname, display all available banks for the current address_space. With a bankname given, switch to the specified bank. If a bank is not completely filled (ROM banks for example) normally the `ram` bank is used where the bank has holes. The `cpu` bank uses the bank currently used by the CPU.

`cpuhistory [<count>] [c:] [8:] [9:] [10:] [11:]`
`chis [<count>] [c:] [8:] [9:] [10:] [11:]`
: Show <count> last executed commands on up to five devices. If no devices are specified, then the default device is shown. VICE emulation runs each CPU for a variable number of cycles before switching between them. They will be synchronized when communication between them occurs. (disabled by default; configure with –enable-cpuhistory to enable)

`dump "<filename>"`
: Write a snapshot of the machine into the file specified. This snapshot is compatible with a snapshot written out by the UI. Note: No ROM images are included into the dump.

`goto <address>`
`g <address>`
: Change the PC to address and continue execution. If no address is given, execution will continue from where it stopped. Also, if `KeepMonitorOpen` is 1, the monitor window will stay open in the background. This can be useful to watch eg trace points or log output in real time.

`io [<address>]`
: Display i/o registers. Invoking without an address shows a dump of the entire io range, if an address is given then details for the chip at the respective (base-)address are displayed (if available).

`jpdb <port> [<value>]`
: Set the value to be used with the "Joystick port I/O simulation". Note that the value is the raw value as read from the I/O port, ie "active" lines are zero. When no value is provided, the command shows the currently used value.

`next [<count>]`
`n [<count>]`
: Advance to the next instruction(s). COUNT allows stepping more than a single instruction at a time. Subroutines are treated as a single instruction ("step over").

`registers [<reg_name> = <number> [, <reg_name> = <number>]*]`
`r [<reg_name> = <number> [, <reg_name> = <number>]*]`
: Assign respective registers (use FL for status flags). With no parameters, display register values.

`reset [<type>]`
: Reset the machine or drive. `type`: 0 = reset, 1 = power cycle, 8-11 = drive.

`return`
`ret`
: Continues execution and returns to the monitor just after the next RTS or RTI is executed ("step out").

`step [<count>]`
`z [<count>]`
: Single step through instructions. An optional count allows stepping more than a single instruction at a time ("step into").

`stopwatch [reset]`
: Print the CPU cycle counter of the current device. 'reset' sets the counter to 0.

`undump "<filename>"`
: Read a snapshot of the machine from the file specified.

`updb [<value>]`
: Set the value to be used with the "User port I/O simulation". Note that the value is the raw value as read from the I/O port, ie "active" lines are zero. When no value is provided, the command shows the currently used value.

`warp [on|off|toggle]`
: Turn warp mode on or off. If the argument is 'toggle' then the current mode is toggled. When no argument is given the current mode is displayed.

`cartfreeze`
: Use cartridge freeze.

`cpu <type>`
: Specify the type of CPU currently used (6502/z80).

`export`
`exp`
: Print out list of attached expansion port devices.

`keybuf <string>`
: Put the specified string into the keyboard buffer. Note that you can specify specific keycodes by using C-style escaped hexcodes ("\x0a").

12.5 Memory commands

`compare <address_range> <address>`
`c <address_range> <address>`
: Compare memory from the source specified by the address range to the destination specified by the address. The regions may overlap. Any values that miscompare are displayed using the default displaytype.

`fill <address_range> <data_list>`
`f <address_range> <data_list>`
: Fill memory in the specified address range with the data in <data_list>. If the size of the address range is greater than the size of the data_list, the data_list is repeated.

`hunt <address_range> <data_list>`
`h <address_range> <data_list>`
: Hunt memory in the specified address range for the data in <data_list>. If the data is found, the starting address of the match is displayed. The entire range is searched for all possible matches. The data list may have 'xx' as a wildcard.

`i <address_opt_range>`
: Display memory contents as PETSCII text.

`ii <address_opt_range>`
: Display memory contents as screen code text

`mem [<radix_type>] [<address_opt_range>]`
`m [<radix_type>] [<address_opt_range>]`
: Display the contents of memory. If no datatype is given, the default is used. If only one address is specified, the length of data displayed is based on the datatype. If no addresses are given, the 'dot' address is used.

`memmapshow [<mask>] [<address_opt_range>]`
`mmsh [<mask>] [<address_opt_range>]`
: Show the memmap. The mask can be specified to show only those locations with accesses of certain type(s). The mask is a number with the bits "ioRWXrwx", where RWX are for ROM and rwx for RAM. Optionally, an address range can be specified. (disabled by default; configure with –enable-cpuhistory to enable)

`memmapzap`
`mmzap`
: Clear the memmap. (disabled by default; configure with –enable-cpuhistory to enable)

`memmapsave "<filename>" <format>`
`mmsave "<filename>" <format>`
: Save the memmap as a picture. `format`: 0 = BMP, 1 = PCX, 2 = PNG, 3 = GIF, 4 = IFF. (disabled by default; configure with –enable-cpuhistory to enable)

`memchar [<radix_type>] [<address_opt_range>]`
`mc [<radix_type>] [<address_opt_range>]`
Display the contents of memory as character data. If only one address is specified, only one character is displayed. If no addresses are given, the "dot" address is used.

`memsprite [<radix_type>] [<address_opt_range>]`
`ms [<radix_type>] [<address_opt_range>]`
Display the contents of memory as sprite data. If only one address is specified, only one sprite is displayed. If no addresses are given, the "dot" address is used.

`move <address_range> <address>`
`t <address_range> <address>`
Move memory from the source specified by the address range to the destination specified by the address. The regions may overlap.

`screen`
`sc` Displays the contents of the screen.

`> [<address>] <data_list>`
Write the specified data at `address`.

12.6 Assembly commands

`a <address> [ <instruction> [: <instruction>]* ]`
Assemble instructions to the specified address. If only one instruction is specified, enter assembly mode (enter an empty line to exit assembly mode).

`disass [<address> [<address>]]`
`d [<address> [<address>]]`
Disassemble instructions. If two addresses are specified, they are used as a start and end address. If only one is specified, it is treated as the start address and a default number of instructions are disassembled. If no addresses are specified, a default number of instructions are disassembled from the dot address.

12.7 Checkpoint commands

`break [load|store|exec] [address [address] [if <cond_expr>]]`
This command allows setting a breakpoint or listing the current breakpoints. If no address is given, the currently valid checkpoints are printed. If an address is given, a breakpoint is set for that address and the breakpoint number is printed. The `"load|store|exec"` parameter can be either `"load"`, `"store"` or `"exec"` (or any combination of these) to determine on which operation the monitor breaks. If not specified, the monitor breaks on `"exec"`. A conditional expression can also be specified for the breakpoint. For more information on conditions, see the CONDITION command.

When a breakpoint hits, the following info will be printed: The first line shows the type of checkpoint, the address, current raster line and -cycle. The second line shows a dump and disassemble, followed by the CPU registers and flags. The number at the end of the line is the stopwatch cycle counter.

```
#1 (Stop on  exec ea31)   41/$029,  35/$23
.C:ea31  20 EA FF    JSR $FFEA      - A:00 X:ED Y:0A SP:ed ..-..IZ.   27481706
```

`enable <checknum>`
`disable <checknum>`

Each checkpoint can be enabled or disabled. This command allows changing between these states.

`command <checknum> "<command>"`

When checkpoint `checknum` is hit, the specified command is executed by the monitor. Note that the `x` command is not yet supported as a command argument.

`condition <checknum> if <cond_expr>`
`cond <checknum> if <cond_expr>`

Each time the specified checkpoint is examined, the condition is evaluated. If it evalutes to true, the checkpoint is activated. Otherwise, it is ignored. If registers are specified in the expression, the values used are those at the time the checkpoint is examined, not when the condition is set.

The condition can use registers (A, X, Y, PC, SP, FL and other cpu specific registers (see manual)) and compare them (==, !=, <, >, <=, >=) against other registers or constants. RL can be used to refer to the current rasterline, and CY refers to the current cycle in the line.

Full expressions are also supported (+, -, *, /, &, |, &&, ||). This lets you for example check specific bits in the FL register using the bitwise boolean operators (& and |).

Please note that currently expressions are evaluated strictly left to right, so use parentheses in complex expressions to indicate the desired evaluation order.

Registers can be the registers of other devices; this is denoted by a memspace prefix (i.e., c:, 8:, 9:, 10:, 11:) Examples: A == $0, X == Y, 8:X == X

You can also compare against the value of a memory location in a specific bank, i.e you can break only if the vic register $d020 is $f0. use the form @[bankname]:[$<address>] | [.label]. Note this is for the C: memspace only. Examples : if @io:$d020 == $f0, if @io:.vicBorder == $f0

As an extension as the previous case, you can also dereference an expression, using the form @[bankname]:(expression) . The parentheses around the expression distinguish it from the previous form. Example: break load 0 $ffff if @cpu:(pc - $1) == $37

`delete <checknum>`
`del <checknum>`

Delete the specified checkpoint.

`ignore <checknum> [<count>]`

Ignore a checkpoint after a given number of crossings. If no count is given, the default value is 1.

`trace [load|store|exec] [address [address] [if <cond_expr>]]`
`tr [load|store|exec] [address [address] [if <cond_expr>]]`
: This command is similar to the `break` command except that it operates on tracepoints. A tracepoint differs from a breakpoint by not stopping execution but simply printing the PC, giving the user an execution trace. The second optional address can be used to specify the end of an range of addresses to be traced. If no addresses are given, a list of all the checkpoints is printed. The "load|store|exec" parameter can be either "load", "store" or "exec" (or any combination of these) to determine which operation the monitor traces. If not specified, the monitor traces all operations. A conditional expression can also be specified for the tracepoint. For more information on conditions, see the CONDITION command.

`until [<address>]`
`un [<address>]`
: If no address is given, the currently valid breakpoints are printed. If an address is given, a temporary breakpoint is set for that address and the breakpoint number is printed. Control is returned to the emulator by this command. The breakpoint is deleted once it is hit.

`watch [load|store|exec] [address [address] [if <cond_expr>]]`
`w [load|store|exec] [address [address] [if <cond_expr>]]`
: This command is similar to the `break` command except that it operates on watchpoints. A watchpoint differs from a breakpoint by stopping on a read and/or write to an address or range of addresses. If no addresses are given, a list of all the checkpoints is printed. The "load|store|exec" parameter can be either "load", "store" or "exec" (or any combination of these) to determine on which operation the monitor breaks. If not specified, the monitor breaks on "load" and "store" operations. A conditional expression can also be specified for the watchpoint. For more information on conditions, see the CONDITION command.

`dummy [on|off|toggle]`
: Control whether the checkpoints will trigger on dummy accesses. If the argument is 'on' then dummy accesses will cause checkpoints to trigger. If the argument is 'off' then dummy accesses will not trigger any checkpoints. If the argument is 'toggle' then the current mode is switched. No argument displays the current state.

12.8 File commands

`cd <directory>`
: Change the working directory.

`dir [<directory>]`
`ls [<directory>]`
: Display the directory contents.

`pwd` Show current working directory.

`mkdir <directory>`
: Create directory.

`rmdir <directory>`
: Remove directory.

`attach "<filename>" <device>`
: Attach file to device. (device 32 = cart)

`autostart "<filename>" [file_index]`
: Autostart a given disk/tape image or program.

`autoload "<filename>" [file_index]`
: Autoload given disk/tape image or program.

`block_read <track> <sector> [<address>]`
`br <track> <sector> [<address>]`
: Read the block at the specified track and sector. If an address is specified, the data is loaded into memory. If no address is given, the data is displayed using the default datatype.

`block_write <track> <sector> <address>`
`bw <track> <sector> <address>`
: Write a block of data at `address` to the specified track and sector of disk in drive 8.

`detach <device>`
: Detach file from device. (device 32 = cart)

`@<disk command>`
: Perform a disk command on the currently attached disk image on virtual drive 8. Since the emulated drive is halted when the monitor is open, DOS commands can not be sent to the actual drive emulation and must be handled by the virtual drive implementation. As a consequence, all limitations of the virtual drives apply. Use the regular monitor commands to eg execute code in the drive.

`list [<device>]`
: List disk contents.

`load "<filename>" <device> [<address>]`
`l "<filename>" <device> [<address>]`
: Load the specified file into memory. If no address is given, the file is loaded to the address specified by the first two bytes read from the file. If address is given, the file is loaded to the specified address and the first two bytes read from the file are skipped. If device is 0, the file is read from the file system.

`loadbasic "<filename>" <device> [<address>]`
`ldb "<filename>" <device> [<address>]`
: Load the specified file into memory and set BASIC pointers as if the file was loaded by the Kernal. If no address is given, the file is loaded to the address specified by the first two bytes read from the file. If address is given, the file is loaded to the specified address and the first two bytes read from the file are skipped. If device is 0, the file is read from the file system.

`bload "<filename>" <device> <address>`
`bl "<filename>" <device> <address>`
Load the specified file into memory at the specified address. If device is 0, the file is read from the file system.

`save "<filename>" <device> <address1> <address2>`
`s "<filename>" <device> <address1> <address2>`
Save the memory from address1 to address2 to the specified file. Write two-byte load address. If device is 0, the file is written to the file system.

`bsave "<filename>" <device> <address1> <address2>`
`bs "<filename>" <device> <address1> <address2>`
Save the memory from address1 to address2 to the specified file. If device is 0, the file is written to the file system.

`verify "<filename>" <device> [<address>]`
`v "<filename>" <device> [<address>]`
Compare the specified file with memory. If no address is given, the address is specified by the first two bytes read from the file. If address is given, the specified address is used and the first two bytes read from the file are skipped. If device is 0, the file is read from the file system.

`bverify "<filename>" <device> <address>`
`bv "<filename>" <device> <address>`
Compare the specified file with memory at the specified address. If device is 0, the file is read from the file system.

`tapectrl <command>`
Control the datasette. `command`: 0 = stop, 1 = start, 2 = forward, 3 = rewind, 4 = record, 5 = reset, 6 = reset counter.

`tapeoffs <offset>`
Set the attached .tap to the given `offset`. When no offset is given, show the current offset.

`screenshot "<filename>" [<format>]`
`scrsh "<filename>" [<format>]`
Take a screenshot. `format`: default = BMP, 1 = PCX, 2 = PNG, 3 = GIF, 4 = IFF.

12.9 Command file commands

`playback "<filename>"`
`pb "<filename>"`
Monitor commands from the specified file are read and executed. This command stops at the end of file or when a STOP command is read.

`record "<filename>"`
`rec "<filename>"`
After this command, all commands entered are written to the specified file until the STOP command is entered.

`stop` Stop recording commands. See `record`.

`; <text>` Add a comment to a file to be read by the monitor. For example:

```
; Set border and background to black
> d020 00 00
```

12.10 Label commands

`add_label <address> <label>`
`al <address> <label>`

Map a given address to a label. This label can be used when entering assembly code and is shown during disassembly. Additionally, it can be used whenever an address must be specified.

<label> is the name of the label; it must start with a dot (".") in order for the monitor to recognize it as a label.

`delete_label [<address_space>] <label>`
`dl [<address_space>] <label>`

Remove the specified label from the label tables. If no address space is checked, all tables are checked.

`load_labels [<address_space>] "<filename>"`
`ll [<address_space>] "<filename>"`

Load a file containing a mapping of labels to addresses. If no address space is specified, the default readspace is used.

The file must contain commands the monitor understands, e.g. add_label. The compiler cc65 can create such label files.

Vice can also load label files created by the Acme assembler. Their syntax is e.g. "labelname = $1234 ; Maybe a comment". A dot will be added automatically to label names assigned in this way to fit to the Vice label syntax. Normally the semicolon seperates commands but after an assignment of this kind it may be used to start a comment to end of line, so unchanged Acme label files can be fed into Vice.

`save_labels [<address_space>] "<filename>"`
`sl [<address_space>] "<filename>"`

Save labels to a file. If no address space is specified, all of the labels are saved.

`show_labels [<address_space>]`
`shl [<address_space>]`

Display current label mappings. If no address space is specified, show all labels from default address space.

`clear_labels [<address_space>]`
`cl [<address_space>]`

Clear current label mappings. If no address space is specified, clear all labels from default address space.

12.11 Profiling commands

Profiling commands are executed through the `profile` command (alt. abbreviated `prof`) followed by subcommand and arguments per below.

`profile on`
: Start profiling and flush old profiling data.

`profile off`
: Stop profiling.

`profile flat [<num=20>]`
: Show flat summary of `num` top functions sorted by self time.

`profile graph [<ctx>] [depth <d>]`
: Show callgraph up to `d` levels deep. If `ctx` is given, zoom on that subtree.

`profile func <function>`
: Show aggregate statistics for a function including callers and callees.

`profile disass <function>`
: Show per-instruction profiling for a given function.

`profile context <ctx>`
: Show detailed context information including per-instruction profiling for a function a call graph context.

`profile clear <function>`
: Clears all profiling stats for a function.

12.12 Resources commands

`resourceget "<resource>"`
`resget "<resource>"`
: Displays the value of the `resource`.

`resourceset "<resource>" "<value>"`
`resset "<resource>" "<value>"`
: Sets the value of the `resource`.

`load_resources "<file>"`
`resload "<file>"`
: Load resources from `file`.

`save_resources "<file>"`
`ressave "<file>"`
: Save resources to `file`.

13 Binary monitor

The binary remote monitor commands are sent over a dedicated connection, specified with the command line options `-binarymonitor` & `-binarymonitoraddress`. See Section 6.17 [Monitor settings], page 112. The remote monitor detects a binary command because it starts with ASCII STX (0x02). Note that there is no termination character. The command length acts as synchronisation point.

All multibyte values are in little endian order unless otherwise specified.

13.1 Command Structure

byte 0: 0x02 (STX)

byte 1: API version ID (currently 0x02)

The API version identifies incompatible changes, such as modifying the header structure, or rearranging or changing the meaning of existing response fields. The API version does not need to be incremented for additional fields. If all the variable length fields are prefixed with their lengths then you should be able to add new ones to any response. The server can assume default values for older clients, and for newer clients with longer commands it should be able to ignore the extra fields safely.

byte 2-5: length

Note that the command length does *not* count the STX, the command length, the command byte, or the request ID. Basically nothing in the header, just the body.

byte 6-9: request id

In little endian order. All multibyte values are in little endian order, unless otherwise specified. There is no requirement for this to be unique, but it makes it easier to match up the responses if you do.

byte 10: The numeric command type

See Section 13.4 [Binary Commands], page 345.

byte 11+: The command body.

See Section 13.4 [Binary Commands], page 345.

13.2 Response Structure

byte 0: 0x02 (STX)

byte 1: API version ID (currently 0x02)

The API version identifies incompatible changes, such as modifying the header structure, or rearranging or changing the meaning of existing response fields. The API version does not need to be incremented for additional fields. If all the variable length fields are prefixed with their lengths then you should be able to add new ones to any response. The client can assume default values for older versions of VICE, and for newer versions of VICE with longer responses it should be able to ignore the extra fields safely.

byte 2-5: response body length. Does not include any header fields
byte 6: response type
This is usually the same as the command ID

byte 7: error code

`0x00`	OK, everything worked
`0x01`	The object you are trying to get or set doesn't exist.
`0x02`	The memspace is invalid
`0x80`	Command length is not correct for this command
`0x81`	An invalid parameter value was present
`0x82`	The API version is not understood by the server
`0x83`	The command type is not understood by the server
`0x8f`	The command had parameter values that passed basic checks, but a general failure occurred

See Section 13.4 [Binary Commands], page 345, for other error codes

byte 8-11: request ID
This is the request ID given to initiate this response. If the value is 0xffffffff, Then the response was initiated by an event, such as hitting a checkpoint.

byte 12+: response body.
See Section 13.4 [Binary Commands], page 345.

13.3 Example Exchange

1. Client connects to ip4://127.0.0.1:6502
2. Client sends a command to set a temporary checkpoint:

```
02 | 02 | 08 00 00 00 | ad de 34 12 | 12 | e2 fc | e3 fc | 01 | 01 | 04 | 01
```

`0x02` Begin command

`0x02` API version 2

`0x00000008`
The command excluding the header is 8 bytes long.

`0x1234dead`
The request ID is 0x1234dead. The response will contain this ID.

`0x12` See Section 13.4.4 [MON_CMD_CHECKPOINT_SET], page 347.

`0xfce2` The address range of the checkpoint starts at 0xfce2.

`0xfce3` The address range of the checkpoint ends at 0xfce3.

`0x01` The checkpoint will cause the emulator to stop.

`0x01` The checkpoint is enabled.

`0x04` The checkpoint will trigger on exec from 0xfce2 - 0xfce3.

`0x01` The checkpoint is temporary.

3. The transmission of any command causes the emulator to stop, similar to the regular monitor. This causes the server to respond with a list of register values.

```
02 | 02 | 26 00 00 00 | 31 | 00 | ff ff ff ff | 09 00 [ 03 { 03 | cf e5 } 03 { 00
| 00 00 } ... ]
```

`0x02` Begin response

`0x02` API Version 2

`0x00000026`
Response length is 38

`0x31` See Section 13.5.3 [MON_RESPONSE_REGISTER_INFO], page 363.

`0x00` No error occurred

`0xffffffff`
This response was not directly triggered by a command from the client.

`0x0009` The register array is 9 items long

PC:

`0x03` The register array item is 3 bytes long

`0x03` The register is the PC (ID 3) Note: you should find the names to these IDs using the MON_CMD_REGISTERS_AVAILABLE command. See Section 13.4.20 [MON_CMD_REGISTERS_AVAILABLE], page 355. Do not rely on them being consistent.

`0xe5cf` The register value is 0xe5cf

A:

`0x03` The register array item is 3 bytes long

`0x00` The register is A (ID 0) Note: you should find the names to these IDs using the MON_CMD_REGISTERS_AVAILABLE command. See Section 13.4.20 [MON_CMD_REGISTERS_AVAILABLE], page 355. Do not rely on them being consistent.

`0x0000` The register value is 0x0000

4. After the register information, the server sends a stopped event to indicate that the emulator is stopped.

```
02 | 02 | 02 00 00 00 | 62 | 00 | ff ff ff ff | cf e5
```

`0x02` Begin response

`0x02` API Version 2

`0x00000002`
Response is two bytes long.

`0x62` Response type is 0x62, MON_RESPONSE_STOPPED.

`0xffffffff`
This response was not directly triggered by a command from the client.

`0xe5cf` The current program counter

5. The server processes the checkpoint set command, and sends a response to the client.

```
... | 11 | ... | 02 00 00 00 | 00 | e2 fc | e3 fc | 01 | 01 | 04 | 01 | 00 00 00
00 | 00 00 00 00 | 00
```
(Some response header fields are omitted (...) for brevity.)

`0x11` See Section 13.5.2 [MON_RESPONSE_CHECKPOINT_INFO], page 362.

`0x00000002`
Checkpoint number is 2

`0x00` Checkpoint was not hit (as it was just created)

`0xfce2` Checkpoint start address

`0xfce3` Checkpoint end address

`0x01` The checkpoint will cause the emulator to stop.

`0x01` The checkpoint is enabled.

`0x04` The checkpoint will trigger on exec from 0xfce2 - 0xfce3.

`0x01` The checkpoint is temporary.

`0x00000000`
The checkpoint has been hit zero times.

`0x00000000`
The checkpoint has been ignored zero times.

6. Client sends a command to continue:

```
... | aa
```
(Some command header fields are omitted (...) for brevity.)

`0xaa` See Section 13.4.27 [MON_CMD_EXIT], page 360.

7. Server acknowledges the command:

   ```
   ... | aa | ...
   ```
   (Some response header fields are omitted (...) for brevity.)

   `0xaa` See Section 13.4.27 [MON_CMD_EXIT], page 360.
8. Server resumes execution and sends a resume event:

   ```
   ... | 63 | ... | cf e5
   ```
   (Some response header fields are omitted (...) for brevity.)

   `0x63` See Section 13.5.6 [MON_RESPONSE_RESUMED], page 364.

   `0xe5cf` Program counter is currently at 0xe5cf
9. Some time later, the server hits the breakpoint. This causes it to emit a checkpoint response. This is identical to the previous checkpoint response, except that it is marked as "hit" and the hit and ignore counts are updated.
10. The server emits the register information and the stopped event when reentering the monitor, as seen previously.

13.4 Commands

These are the possible command types and responses, without the header portions mentioned above.

13.4.1 Memory get (0x01)

Reads a chunk of memory from a start address to an end address (inclusive).

Minimum VICE version: 3.5

Command body:

```
FX | SA SA | EA EA | MS | BI BI
```

FX: 1 byte: side effects?
: Should the read cause side effects?

SA: 2 bytes: start address
EA: 2 bytes: end address
MS: 1 byte: memspace
: Describes which part of the computer you want to read:

- 0x00: main memory
- 0x01: drive 8
- 0x02: drive 9

- 0x03: drive 10
- 0x04: drive 11

BI: 2 bytes: bank ID

Describes which bank you want. This is dependent on your machine. See Section 13.4.19 [MON_CMD_BANKS_AVAILABLE], page 354. If the memspace selected doesn't support banks, this value is ignored.

Response type:

0x01: MON_RESPONSE_MEM_GET

Response body:

```
ML ML | MM[0] MM[1] ... MM[ML-1]
```

ML: 2 bytes: Memory segment length.

Will be zero for start 0x0000, end 0xffff.

MM: ML bytes: The memory at the address.

13.4.2 Memory set (0x02)

Writes a chunk of memory from a start address to an end address (inclusive).

Minimum VICE version: 3.5

Command body:

```
FX | SA SA | EA EA | MS | BI BI | MM[0] MM[1] ... MM[EA-SA]
```

FX: 1 byte: side effects?

Should the write cause side effects?

SA: 2 bytes: start address
EA: 2 bytes: end address
MS: 1 byte: memspace

Describes which part of the computer you want to write:

- 0x00: main memory
- 0x01: drive 8
- 0x02: drive 9
- 0x03: drive 10
- 0x04: drive 11

BI: 2 bytes: bank ID

Describes which bank you want. This is dependent on your machine. See Section 13.4.19 [MON_CMD_BANKS_AVAILABLE], page 354. If the memspace selected doesn't support banks, this byte is ignored.

MM: 1+EA-SA bytes: Memory contents to write

Response type:

0x02: MON_RESPONSE_MEM_SET

Response body:

```
Currently empty.
```

13.4.3 Checkpoint get (0x11)

Gets any type of checkpoint. (break, watch, trace)

Minimum VICE version: 3.5

Command body:

```
CN CN CN CN
```

CN: 4 bytes: checkpoint number

See Section 13.5.2 [MON_RESPONSE_CHECKPOINT_INFO], page 362.

13.4.4 Checkpoint set (0x12)

Sets any type of checkpoint. This combines the functionality of several textual commands (break, watch, trace) into one, as they are all the same with only minor variations. To set conditions, see Section 13.4.8 [MON_CMD_CONDITION_SET], page 349, after executing this one.

Command body:

```
SA SA | EA EA | ST | EN | OP | TM | MS?
```

SA: 2 bytes: start address
EA: 2 bytes: end address
ST: 1 byte: stop when hit
>=0x01: true, 0x00: false

EN: 1 byte: enabled
>=0x01: true, 0x00: false

OP: 1 byte: CPU operation
>=0x01: load, 0x02: store, 0x04: exec

TM: 1 byte: temporary
Deletes the checkpoint after it has been hit once. This is similar to "until" command, but it will not resume the emulator.

MS: 1 byte (optional): memspace
Describes which part of the computer to checkpoint:

- 0x00: main memory
- 0x01: drive 8
- 0x02: drive 9

- 0x03: drive 10
- 0x04: drive 11

See Section 13.5.2 [MON_RESPONSE_CHECKPOINT_INFO], page 362.

13.4.5 Checkpoint delete (0x13)

Deletes any type of checkpoint. (break, watch, trace)

Minimum VICE version: 3.5

Command body:

```
CN CN CN CN
```

CN: 4 bytes: checkpoint number

Response type:

0x13: MON_RESPONSE_CHECKPOINT_DELETE

Response body:

```
Currently empty.
```

13.4.6 Checkpoint list (0x14)

Minimum VICE version: 3.5

Command body:

```
Currently empty.
```

Response type:

Emits a series of MON_RESPONSE_CHECKPOINT_INFO responses (see Section 13.5.2 [MON_RESPONSE_CHECKPOINT_INFO], page 362) followed by

0x14: MON_RESPONSE_CHECKPOINT_LIST

Response body:

```
CC CC CC CC
```

CC: 4 bytes: The total count of checkpoints

13.4.7 Checkpoint toggle (0x15)

Minimum VICE version: 3.5

Command body:

```
CN CN CN CN | EN
```

CN: 4 bytes: Checkpoint number
EN: 1 byte: Enabled?
0x00: disabled, 0x01: enabled

Response type:

0x15: MON_RESPONSE_CHECKPOINT_TOGGLE

Response body:

`Currently empty.`

13.4.8 Condition set (0x22)

Sets a condition on an existing checkpoint. It is not currently possible to retrieve conditions after setting them.

Minimum VICE version: 3.5

Command body:

`CN CN CN CN | EL | ES[0] ES[1] ... ES[EL-1]`

CN: 4 bytes: checkpoint number
EL: 1 byte: condition expression length
ES: EL bytes: condition expression string
This is the same format used in the text monitor. Not null terminated.

Response type:

0x22: MON_RESPONSE_CONDITION_SET

Response body:

`Currently empty.`

13.4.9 Registers get (0x31)

Get details about the registers

Minimum VICE version: 3.5

Command body:

`MS`

MS: 1 byte: memspace
Describes which part of the computer you want to read:

- 0x00: main memory

- 0x01: drive 8
- 0x02: drive 9
- 0x03: drive 10
- 0x04: drive 11

See Section 13.5.3 [MON_RESPONSE_REGISTER_INFO], page 363.

13.4.10 Registers set (0x32)

Set the register values

Minimum VICE version: 3.5

Command body:

```
MS | RC RC [
    IS[0] { RI[0] | RV[0] RV[0] }
    ...
    IS[RC-1] { RI[RC-1] | RV[RC-1] RV[RC-1] }
]
```

MS: 1 byte: memspace

Describes which part of the computer you want to write:

- 0x00: main memory
- 0x01: drive 8
- 0x02: drive 9
- 0x03: drive 10
- 0x04: drive 11

RC: 2 bytes: Register count
Array: RC*(IS+1) bytes: An array with items of structure:

[

IS: 1 byte: Item size, excluding this byte
RI: 1 byte: Register ID
RV: 2 bytes: Register value

]

See Section 13.5.3 [MON_RESPONSE_REGISTER_INFO], page 363.

13.4.11 Dump (0x41)

Saves the machine state to a file.

Minimum VICE version: 3.5

Command body:

```
SR | SD | FL | FN[0] FN[1] ... FN[FL-1]
```

SR: 1 byte: Save ROMs to snapshot file?
>=0x01: true, 0x00: false

SD: 1 byte: Save disks to snapshot file?
>=0x01: true, 0x00: false

FL: 1 byte: Length of filename
FN: FL bytes: Filename
The filename to save the snapshot to.

Response type:

0x41: MON_RESPONSE_DUMP

Response body:

`Currently empty.`

13.4.12 Undump (0x42)

Loads the machine state from a file.

Minimum VICE version: 3.5

Command body:

`FL | FN[0] FN[1] ... FN[FL-1]`

FL: 1 byte: Length of filename
FN: FL bytes: Filename
The filename to load the snapshot from.

Response type:

0x42: MON_RESPONSE_UNDUMP

Response body:

`PC PC`

PC: 2 bytes: The current program counter position

13.4.13 Resource Get (0x51)

Get a resource value from the emulator. See Section “Settings and resources” in `Settings and resources`.

Minimum VICE version: 3.5

Command body:

`NL | RN[0] RN[1] ... RN[NL-1]`

NL: 1 byte: Resource name length
RN: NL bytes: Resource name

Response type:

0x51: MON_RESPONSE_RESOURCE_GET

Response body:

```
RT | VL | RV[0] RV[1] ... RV[VL-1]
```

RT: 1 byte: Resource type
0x00: String, 0x01: Integer

VL: 1 byte: Resource value length
RV: VL bytes: The resource value

13.4.14 Resource Set (0x52)

Set a resource value in the emulator. See Section "Settings and resources" in `Settings and resources`.

Minimum VICE version: 3.5

Command body:

```
RT | NL | RN[0] RN[1] ... RN[NL-1] | VL | RV[0] RV[1] ... RV[VL-1]
```

RT: 1 byte: Type of the resource value
0x00: String, 0x01: Integer
Strings will be interpreted if the destination is an Integer.

NL: 1 byte: Resource name length
RN: NL bytes: The resource name
VL: 1 byte: Resource value length
RV: VL bytes: The resource value

Response type:

0x52: MON_RESPONSE_RESOURCE_SET

Response body:

```
Currently empty.
```

13.4.15 Advance Instructions (0x71)

Step over a certain number of instructions.

Minimum VICE version: 3.5

Command body:

```
SO | IC IC
```

SO: 1 byte: Step over subroutines?
Should subroutines count as a single instruction?

IC: 2 bytes: How many instructions to step over.

Response type:

0x71: MON_RESPONSE_ADVANCE_INSTRUCTIONS

Response body:

```
Currently empty.
```

13.4.16 Keyboard feed (0x72)

Add text to the keyboard buffer.

Minimum VICE version: 3.5

Minimum API version: 2

Command body:

```
TL | TC[0] TC[1] ... TC[TL-1]
```

TL: 1 byte: Text Length
TC: TL bytes: The text content, in PETSCII

Response type:

0x72: MON_RESPONSE_KEYBOARD_FEED

Response body:

```
Currently empty.
```

13.4.17 Execute until return (0x73)

Continues execution and returns to the monitor just after the next RTS or RTI is executed.

This command is the same as "return" in the text monitor.

Minimum VICE version: 3.5

Command body:

```
Currently empty.
```

Response type:

0x73: MON_RESPONSE_EXECUTE_UNTIL_RETURN
Response body:
`Currently empty.`

13.4.18 Ping (0x81)

Get an empty response
Minimum VICE version: 3.5
Command body:
`Always empty`

Response type:
0x81: MON_RESPONSE_PING
Response body:
`Always empty`

13.4.19 Banks available (0x82)

Gives a listing of all the bank IDs for the running machine with their names.
Minimum VICE version: 3.5
Command body:
`Currently empty.`

Response type:
0x82: MON_RESPONSE_BANKS_AVAILABLE
Response body:

```
BC BC [
    IS[0] { BI[0] BI[0] | NL[0] | BN[0][0] BN[0][1] ... BN[0][NL[0]-1] }
    ...
    IS[BC-1] { ... }
]
```

BC: 2 bytes: Bank item count
Array 1+BC*IS bytes: An array with items of structure:

[

IS: 1 byte: Item size, excluding this byte
BI: 2 bytes: Bank ID
NL: 1 byte: Name Length
BN: NL bytes: Name

]

13.4.20 Registers available (0x83)

Gives a listing of all the registers for the running machine with their names.

Minimum VICE version: 3.5

Command body:

```
MS
```

MS: 1 byte: memspace

Describes which part of the computer you want to read:

- 0x00: main memory
- 0x01: drive 8
- 0x02: drive 9
- 0x03: drive 10
- 0x04: drive 11

Response type:

0x83: MON_RESPONSE_REGISTERS_AVAILABLE

Response body:

```
RC RC [ IS[0] { RI[0] | RS[0] | NL[0] | RN[0][0] RN[0][1] ... RN[0][NL[0]-1] } ... IS[RC-1]
```

RC: 2 bytes: Register item count
Array: RC*(IS+1) bytes: An array with items of structure:

IS: 1 byte: Item Size, excluding this byte
RI: 1 byte: Register ID
RS: 1 byte: Register Size in bits
NL: 1 byte: Length of name
RN: NL bytes : Register name

13.4.21 Display Get (0x84)

Gets the current screen in a requested bit format.

Minimum VICE version: 3.5

Minimum API version: 2

Command body:

```
VC | FM
```

VC: 1 byte: USE VIC-II?
Must be included, but ignored for all but the C128. If true, (>=0x01) the screen returned will be from the VIC-II. If false (0x00), it will be from the VDC.

FM: 1 byte: Format
0x00: Indexed, 8 bit

Response type:

0x84: MON_RESPONSE_DISPLAY_GET

Response body:

```
FL FL FL FL | DW DW | DH DH | XO XO | YO YO | IW IW | IH IH | BP |
    BL BL BL BL | BD[0] BD[1] ... BD[BL-1]
```

FL: 4 bytes: Length of the fields before the display buffer (DW...BP)
DW: 2 bytes: Debug width of display buffer (uncropped)
The largest width the screen gets.

DH: 2 bytes: Debug height of display buffer (uncropped)
The largest height the screen gets.

XO: 2 bytes: X offset
X offset to the inner part of the screen.

YO: 2 bytes: Y offset
Y offset to the inner part of the screen.

IW: 2 bytes: Width of the inner part of the screen.
IH: 2 bytes: Height of the inner part of the screen.
BP: 1 byte: Bits per pixel of display buffer, 8
BL: 4 bytes: Length of display buffer
BD: BL bytes: Display buffer data

13.4.22 VICE info (0x85)

Get general information about VICE. Currently returns the versions.

Minimum VICE version: 3.6

Command body:

```
Always empty
```

Response type:

0x85: MON_RESPONSE_VICE_INFO

Response body:

```
ML | MV[0] MV[1] ... MV[ML-1] | SL | SV[0] SV[1] ... SV[SL-1]
```

ML: 1 byte: Length of main version
MV: ML bytes: Main version
In linear format. For example 0x03, 0x05, 0x00, 0x00 for 3.5.0.0

SL: 1 byte: Length of SVN revision
SV: SL bytes: SVN revision
In little endian format. Returns zero if it's not an SVN build.

13.4.23 CPU History (0x86)

Gets records of every instruction executed by an emulated CPU.

Minimum VICE version: 3.10

Command body:

```
MS | HC HC HC HC
```

MS: 1 byte: memspace
- 0x00: main memory
- 0x01: drive 8
- 0x02: drive 9
- 0x03: drive 10
- 0x04: drive 11

HC: 4 bytes: count of items to retrieve

Response type:

0x86: MON_RESPONSE_CPUHISTORY_GET

Response body:

```
HC HC HC HC [
    IS[0] {
      RC[0] RC[0] [
        RS[0][0] { ... }
        ...
        RS[0][RC-1] { ... }
      ]
      CL[0] CL[0] CL[0] CL[0] CL[0] CL[0] CL[0] CL[0] | IL[0] | IB[0][0] IB[0][1] ... IB[0]
    }
    ...
    IS[HC-1] { ... }
]
```

HC: 4 bytes: CPU history item count
Array: HC*(IS+1) bytes:
An array with items of structure:

[

IS: 1 bytes: Item size, excluding this byte
RC: 2 bytes: Register item count
Array: RC*(RS+1) bytes: Array items of structure:

[

RS: 1 byte: Item size, excluding this byte
{...}: RS bytes: The array item body from Section 13.5.3 [MON_RESPONSE_REGISTER_INFO], page 363

]

CL: 8 bytes: The CPU clock
IL: 1 bytes: instruction bytes length
IB: IL bytes: Instruction byte string.
This is a fixed length and must be interpreted by the client.

]

13.4.24 Palette get (0x91)

Get the colors in the current palette

Minimum VICE version: 3.6

Command body:

```
VC
```

VC: 1 byte: USE VIC-II?
Must be included, but ignored for all but the C128. If true, (>=0x01) the screen returned will be from the VIC-II. If false (0x00), it will be from the VDC.

Response type:

0x91: MON_RESPONSE_PALETTE_GET

Response body:

```
PC PC [
  IS[0]  RR[0] | GG[0] | BB[0]
  ...
  IS[PC-1]  RR[PC-1] | GG[PC-1] | BB[PC-1]
]
```

PC: 2 bytes: The palette item count
Array : PC*(IS+1): An array with items of structure:

[

IS: 1 byte: Item size, excluding this byte
RR: 1 byte: Red
GG: 1 byte: Green
BB: 1 byte: Blue

]

13.4.25 Joyport set (0xa2)

Set the simulated joyport value.

Minimum VICE version: 3.6

Command body:

```
PN PN | PV PV
```

PN: 2 bytes: The port to set the value on
PV: 2 bytes: The value to set

Response type:

0xa2: MON_RESPONSE_JOYPORT_SET

Response body:

```
Currently empty.
```

13.4.26 Userport set (0xb2)

Set the simulated userport value.

Minimum VICE version: 3.6

Command body:

```
UV UV
```

UV: 2 bytes: The value to set

Response type:

0xb2: MON_RESPONSE_USERPORT_SET

Response body:

```
Currently empty.
```

13.4.27 Exit (0xaa)

Exit the monitor until the next breakpoint.
Minimum VICE version: 3.5
Command body:
`Currently empty.`

Response type:
0xaa: MON_RESPONSE_EXIT
Response body:
`Currently empty.`

13.4.28 Quit (0xbb)

Quits VICE.
Minimum VICE version: 3.5
Command body:
`Currently empty.`

Response type:
0xbb: MON_RESPONSE_QUIT
Response body:
`Currently empty.`

13.4.29 Reset (0xcc)

Reset the system or a drive
Minimum VICE version: 3.5
Command body:
`RS`

RS: 1 byte: What to reset

- 0x00: Reset system
- 0x01: Power cycle system
- 0x08 - 0x0b: Reset drives 8 - 11

Response type:
0xcc: MON_RESPONSE_RESET
Response body:
`Currently empty.`

13.4.30 Autostart / autoload (0xdd)

Load a program then return to the monitor
Minimum VICE version: 3.5
Command body:
`RL | FI FI | FL | FN[0] FN[1] ... FN[FL-1]`

RL: 1 byte: Run after loading?
>=0x01: true, 0x00: false

FI: 2 bytes: File index
The index of the file to execute, if a disk image. 0x00 is the default value.

FL: byte 3: Length of filename
FN: FL bytes: Filename
The filename to autoload.

Response type:
0xdd: MON_RESPONSE_AUTOSTART
Response body:
`Currently empty.`

13.5 Responses

These responses are generated by many different commands, or by certain events. Events are generated with a request ID of 0xffffffff, so that they can be easily distinguished from regular requests.

13.5.1 Invalid Response (0x00)

This response type is returned for errors.
Response type:
0x00: MON_RESPONSE_INVALID
Response body:
`Usually empty`

13.5.2 Checkpoint Response (0x11)

This response is generated by hitting a checkpoint, or by many of the checkpoint commands.

See Section 13.4.3 [MON_CMD_CHECKPOINT_GET], page 347.

See Section 13.4.4 [MON_CMD_CHECKPOINT_SET], page 347.

See Section 13.4.6 [MON_CMD_CHECKPOINT_LIST], page 348.

Response type:

0x11: MON_RESPONSE_CHECKPOINT_INFO

Response body:

```
CN CN CN CN | CH | SA SA | EA EA | ST | EN | OP | TM |
    HC HC HC HC | IC IC IC IC | CE | MS
```

CN: 4 bytes: Checkpoint number
CH: 1 byte: Currently hit?
>=0x01: true, 0x00: false

SA: 2 bytes: start address
EA: 2 bytes: end address
ST: 1 byte: stop when hit
>=0x01: true, 0x00: false

EN: 1 byte: enabled
>=0x01: true, 0x00: false

OP: 1 byte: CPU operation
0x01: load, 0x02: store, 0x04: exec

TM: 1 byte: temporary
Deletes the checkpoint after it has been hit once. This is similar to "until" command, but it will not resume the emulator.

HC: 4 bytes: hit count
IC: 4 bytes: ignore count
CE: 1 byte: Has condition?
>=0x01: true, 0x00: false

MS: 1 byte: memspace
Describes which part of the computer to checkpoint:

- 0x00: main memory
- 0x01: drive 8
- 0x02: drive 9
- 0x03: drive 10
- 0x04: drive 11

13.5.3 Register Response (0x31)

Response type:

0x31: MON_RESPONSE_REGISTER_INFO

Response body:

```
RC RC [
    IS[0] { RI[0] | RV[0] RV[0] }
    ...
    IS[RC-1] { RI[RC-1] | RV[RC-1] RV[RC-1] }
]
```

RC: 2 bytes: The count of the array items
Array: RC*(IS+1): An array with items of structure:

[

IS: 1 byte: Item size, excluding this byte
RI: 1 byte: ID of the register
RV: 2 bytes: register value

]

13.5.4 JAM Response (0x61)

When the CPU jams

Response type:

0x61: MON_RESPONSE_JAM

Response body:

```
PC PC
```

PC: 2 bytes: The current program counter position

13.5.5 Stopped Response (0x62)

When the machine stops for the monitor, either due to hitting a checkpoint or stepping.

Response type:

0x62: MON_RESPONSE_STOPPED

Response body:

```
PC PC
```

PC: 2 bytes: The current program counter position

13.5.6 Resumed Response (0x63)

When the machine resumes execution for any reason.

Response type:

0x63: MON_RESPONSE_RESUMED

Response body:

`PC PC`

PC: 2 bytes: The current program counter position

13.6 Example Projects

Here's a short list of some projects using the binary monitor interface:

C64 Studio	IDE for assembly and BASIC projects, geared toward game development. `https://github.com/GeorgRottensteiner/C64Studio`
VICE Binary Monitor Bridge for .NET	A cross platform .NET 5 library that implements a bridge for communication with the VICE binary monitor. `https://github.com/MihaMarkic/vice-bridge-net`
IceBro Lite	IceBro Lite is a source-level debugger with a graphical user interface (GUI). `https://github.com/Sakrac/IceBroLite`
VS65 Debugger	Visual Studio Code extension to debug CC65 projects. `https://github.com/empathicqubit/vscode-cc65-debugger`
VS64	The VS64 extension makes it easy to build, debug, inspect and run C64 assembly code from Visual Studio Code. `https://github.com/rolandshacks/vs64`

14 c1541

VICE is provided with a complete stand-alone disk image maintenance utility, called `c1541`.

The syntax is:

```
c1541 [IMAGE1 [IMAGE2]] [COMMAND1 COMMAND2 ... COMMANDN]
```

`IMAGE1` and `IMAGE2` are disk image names that can be attached before `c1541` starts. `c1541` can handle up to two disk images at the same time by using two virtual built-in drives, numbered `8` and `9`; `IMAGE1` (if present) is always attached to drive `8`, while `IMAGE2` is attached to drive `9`.

`COMMAND`s specified on the command-line all begin with the minus sign (`-`); if present, `c1541` executes them in the same order as they are on the command line and returns a zero error code if they were successful. If any of the `COMMAND`s fails, `c1541` stops and returns a nonzero error code.

If no `COMMAND`s are specified at all, `c1541` enters interactive mode, where you can type commands manually. Commands in interactive mode are the same as commands in batch mode, but do not require a leading `-`.

As with the monitor, file name completion and command line editing with history are provided. Use the command '`quit`' or press *`C-d`* to exit.

14.1 Specifying files in c1541

When accessing CBM DOS files (i.e. files that reside on disk images), c1541 uses a special syntax that lets you access files on both drive 8 and 9. If you prepend the file name with `@8:` or `@9:`, you will specified that file is to be found or created on drive 8 and 9, respectively.

For instance,

```
@8:somefile
```

will name file named `somefile` on unit 8, while

```
@9:somefile
```

will name file named `somefile` on unit 9.

14.2 Using quotes and backslashes

You can use quotes (`"`) in a command to embed spaces into file names. For instance,

```
read some file
```

will read file `some` from the disk image and write it into the file system as `file`, while

```
read "some file"
```

will copy `some file` into the file system, with the name `some file`.

The backslash character (\) has a special meaning too: it lets you literally insert the following character no matter what it is. For example,

```
read some\ file
```

will copy file `some file` into the file system, while

```
read some\ file this\"file
```

will copy `some file` into the file system with name `this"file` (with an embedded quote).

14.3 c1541 commands and options

This is a list of the `c1541` commands. They are shown in their interactive form, without the leading `-`. Square brackets [] indicate an optional part, and `"<COMMAND>"` translates to a disk command according to CBM DOS, like `"i0"` for example.

`@ [<command>]`
: Execute specified CBM DOS command and print the current status of the drive. If no `command` is specified, just print the status.

`? [<command>]`
: Explain specified command. If no command is specified, list available ones.

`attach <diskimage> [<unit>]`
: Attach `diskimage` to `unit` (default unit is 8).

`bam [<unit>] | <track-min> <track-max> [<unit>]`
: Show the BAM of `unit`, optionally displaying only the entries for `track-min` to `track-max`

`bcopy <src-trk> <src-sec> <dst-trk> <dst-sec> [<src-unit> [<dst-unit>]]`
: Copy a block to another block, optionally specifying different source and destination units. The block is copied using all 256 bytes.

`bfill <track> <sector> <value> [<unit>]`
: Fill a block with a single value.

`block <track> <sector> [<offset> [<unit>]]`
: Show specified disk block in hex form.

`bpoke [@unit:<unit>] <track> <sector> <offset> <data ...>`
: Store one or more bytes of `data` into a block. The `data` can be specified using prefixes:

  `0b or %` binary value (‘%11111111‘)

  `&` octal value (‘&377‘)

  `0x or $` hexadecimal value (‘$ff‘)

  The <unit> is optional, and when used must use the CBM DOS notation for the unit number, for example ‘@9:‘.

`bpeek [@unit:<unit>] <track> <sector> [<start> [<end>]]`
: Print one or more bytes of `track,sector`.

`bread <filename> <track> <sector> [<unit>]`
: Read a block from a disk image and write it to the host filesystem as `filename`.

`bwrite <filename> <track> <sector> [<unit>]`
: Write data from the host filesystem using `filename` as input. At most 256 bytes are written to the disk image.

`cd <dir>` Change the current working directory to `dir`.

`chain <track> <sector> [<unit>] | <filename>`
: Show block chain starting at (`track`, `sector`), or alternatively use `filename` to show the block chain of a file in the directory. The last number shown is the number of bytes used in the final block.

`copy <source1> [<source2> ... <sourceN>] <destination>`
: Copy `source1 ... sourceN` into destination. If N > 1, `destination` must be a simple drive specifier (`@n:`). To copy a REL file, specify the <source> with a `,L` file type suffix.

`delete <file1> [<file2> ... <fileN>]`
: Delete the specified files.

`disable-libdebug-output`
: Disable output of src/lib.c's leak list. This is a debug hook for the c1541 test bench.

`exit`
: Exit (same as `quit`).

`entry [+side] <file1> [<file2> ... <fileN>]`
: Show the directory entries of the given files in full detail. If the `+side` option is present, and it is a RELative file, it also shows all side sectors.

`extract`
: Extract all the files to the file system.

`format <diskname,id> [<type> <imagename>] [<unit>]`
: If `unit` is specified, format the disk in unit `unit`. If `type` and `imagename` are specified, create a new image named `imagename`, attach it to unit 8 and format it. `type` is a disk image type, and must be either `x64`, `d64` (both VC1541/2031), `g64` (VC1541/2031 but in GCR coding), `d71` (VC1571), `g71` (VC1571 but in GCR coding), `d81` (VC1581), `d80` (CBM8050), `d82` (CBM8250/1001), or `d90` (CBM D9090). Otherwise, format the disk in the current unit, if any.

`geosdelete <file1> [<file2> ... <fileN>]`
: Delete the specified GEOS files. Please note that due to GEOS using ASCII, not PETSCII, the name should be entered as listed by the 'geoslist' command.

`geosextract [<unit>]`
: Extract all the files to the file system and GEOS Convert them.

`geoslist [<pattern>]`
`geosdir [<pattern>]`
: List GEOS files matching `pattern` (default is all files).

`geosread <source> [<destination>]`
: Read GEOS <source> from the disk image and copy it as a Convert file into <destination> in the file system. If <destination> is not specified, copy it into a file with the same name as <source>. Please note that due to GEOS using ASCII, not PETSCII, the name should be entered as listed by the 'geoslist' command.

`geoswrite <source>`
: Write GEOS Convert file <source> from the file system to a disk image.

`help [<command>]`
: Explain specified command. If no command is specified, list available ones.

`info [<unit>]`
: Display information about unit `unit` (if unspecified, use the current one).

`list [<pattern>]`
`dir [<pattern>]`
List files matching `pattern` (default is all files).

`name <diskname>[,<id>] <unit>`
Change image name.

`p00save <enable> [<unit>]`
Save P00 files to the file system.

`pwd` Print current working directory.

`quit` Exit (same as `exit`).

`read <source> [<destination>]`
Read `source` from the disk image and copy it into `destination` in the file system. If `destination` is not specified, copy it into a file with the same name as `source`. By default PRG files are copied. To copy SEQ files add `,s` (i.e. the usual DOS file specification syntax). REL files can be copied by adding `,l`. Don't specify the record length, `c1541` will determine it automatically.

`rename <oldname> <newname>`
Rename `oldname` into `newname`. The files must be on the same drive.

`tape <t64name> [<file1> ... <fileN>]`
Extract files from a T64 image.

`unit <number>`
Make unit `number` the current unit.

`unlynx <lynxname> [<unit>]`
Extract the specified Lynx image file into the specified unit (default is the current unit).

`validate [<unit>]`
Validate the disk in unit `unit`. If `unit` is not specified, validate the disk in the current unit.

`verbose ["off"]`
Enable or disable verbose output.

`version` Show C1541 version string

`write <source> [<destination>]`
Write `source` from the file system into `destination` on a disk image.

To create a REL file, you must specify the `destination` including the record length. As a special convenience, you can give it in decimal. Example: `write fsname imgname,l,100` Note that the size of the file may be rounded up to fill the last sector.

`unzip <d64name> <zipname> [<label,id>]`
Create a D64 disk image out of a set of four Zipcoded files named `1!zipname`, `2!zipname`, `3!zipname` and `4!zipname`.

14.4 Executing shell commands

If you want to execute a shell command from within `c1541`, just prepend it with an exclamation mark (`!`). For example,

```
!ls -la
```

will execute the command `ls -la`, which will show you all the files in the current directory.

14.5 c1541 examples

`c1541 -attach test.d64 -list`
: Attach `test.d64` and show directory.

`c1541 -attach test.d64 -write test.prg testfile`
: Write `test.prg` to `test.d64` as `testfile`.

`c1541 -format "name,id" d64 disk.d64`
: Create a disk image in `d64` format, format it with the `name` and `id` and save it to `disk.d64`.

`c1541 opencbm -list`
: Show directory from a 1541 drive connected via OpenCBM.

15 cartconv

The cartconv program is a cartridge conversion utility, it can convert between binary and .crt images and it can 'insert' binary and/or .crt images into the EPROM type of cartridges.

```
convert:    cartconv [-r] [-q|-v] [-t cart type] [-s cart revision] -i "input name" -o "out
print info: cartconv [-r] [-q|-v] -f "input name"
check file: cartconv [-r] [-q|-v] -c "input name"

-f <name>                    print info on file
-c --check <name>            check file
-r                           repair mode (accept broken/invalid input files)
-p                           accept non padded binaries as input
-b                           output all banks (do not optimize the .crt file)
-t <type> or <crtid>         output cart type
-s <rev>                     output cart revision/subtype
-i <name>                    input filename
-o <name>                    output filename
-n <name>                    crt cart name
-l <addr>                    load address (decimal, use 0x-prefix for hexadecimal, e.g. 0xc0
-q                           quiet
-v --verbose                 verbose
--types                      show the supported cart types
--version                    print cartconv version
--options-file <filename>    write options for reverting the conversion into a file (for tes
```

15.1 cartconv command line options

The cartconv program has the following parameters:

`-i "input name"`
: This parameter is mandatory, it should contain the name of the binary/.crt file you want to convert. For the EPROM type of cartridges this parameter can be used multiple times to insert images into the resulting file.

`-o "output name"`
: This parameter is mandatory, it should contain the name of the binary/.crt file you want to convert the input file to.

`-t carttype`
: This parameter is optional. It is only needed when converting to a .crt file. See below for the supported cartridge types.

`-s revision`
: This parameter is optional. It is only needed when converting to a .crt file. See the detailed description of the .crt format for which types support subtypes/revisions.

`-n "cart name"`
: This parameter is optional and is used as the cartridge name when creating a .crt file. By default the cartridge name will be set to the cartridge type.

`-l loadaddress`
: This parameter is optional and is used as the load-address when converting a .crt file to a .prg file, or when converting to a generic type .crt file.

`-f "input name"`
: This parameter is optional, and is meant to output information about the named file. It can't be used in conjuction with any of the other parameters.

`-c "input name"`
`--check "input name"`
: Works similar to -f, but does not output anything except on errors. On errors it will also make cartconv exit with an exitcode of -1.

`-r` This parameter is optional, it enables repair mode (accept broken input files)

`-p` This parameter is optional, when it is given cartconv will accept input files that do not match the cartridge sizes (useful for development).

`-b` This parameter is optional, when it is given cartconv will not omit banks that are empty (filled with $ff).

`-q` This parameter is optional, it disables all non-error messages

`-v`

`--verbose`
: This parameter is optional, it enables more verbose messages

`--types` This parameter is optional. It shows all supported cartridge types (see below).

`--version`
: Show cartconv version string and exit

`--options-file <filename>`
: This parameter is optional. If present, cartconv writes options for reverting the conversion into a file, this is mostly useful for the test script.

15.2 cartconv supported cartridge types

The following cartridge types are supported:

`bin` Binary .bin file (Default crt->bin)

`prg` Binary C64 .prg file with load-address

C64 cartridges:

`normal` Generic 8KiB/12KiB/16KiB .crt file (Default bin->crt)

`ulti` Ultimax mode 4KiB/8KiB/16KiB .crt file

`ap` Atomic Power .crt file

`ar2` Action Replay MK2 .crt file

`ar3` Action Replay MK3 .crt file

`ar4` Action Replay MK4 .crt file

`ar5`	Action Replay V5 .crt file
`bis`	BIS-Plus .crt file
`bb3`	Blackbox V3 .crt file
`bb4`	Blackbox V4 .crt file
`bb8`	Blackbox V8 .crt file
`bb9`	Blackbox V9 .crt file
`bdt`	BMP Data Turbo 2000 .crt file
`cap`	Capture .crt file
`comal`	Comal 80 .crt file
`dep256`	Dela EP256 .crt file, extra files can be inserted (1)(2)
`dep64`	Dela EP64 .crt file, extra files can be inserted (1)
`dep7x8`	Dela EP7x8 .crt file, extra files can be inserted (1)(2)(3)
`din`	Dinamic .crt file
`dsm`	Diashow-Maker .crt file
`easy`	EasyFlash .crt file
`ecr`	Easy Calc Result .crt file
`epyx`	Epyx FastLoad .crt file
`exos`	EXOS .crt file
`expert`	Expert Cartridge .crt file
`f64`	Formel 64 .crt file
`fc1`	The Final Cartridge .crt file
`fc3`	The Final Cartridge III .crt file
`fcp`	Final Cartridge Plus .crt file
`ff`	Freeze Frame .crt file
`ff2`	Freeze Frame MK2 .crt file
`fm`	Freeze Machine .crt file
`fp`	Fun Play .crt file
`gk`	Game Killer .crt file
`gmod2`	GMod2 .crt file
`gmod3`	GMod3 .crt file
`gs`	C64 Games System .crt file
`hyper`	Hyper-Basic .crt file
`ide64`	IDE64 .crt file

`ieee` IEEE-488 Interface .crt file

`ieeeflash64`
IEEE Flash! 64 interface .crt file

`kcs` KCS Power Cartridge .crt file

`ks` Kingsoft .crt file

`ltk` Lt. Kernal Host Adaptor .crt file

`mach5` MACH 5 .crt file

`md` Magic Desk .crt file

`mdp` Magic Desk Plus 256KiB/512KiB/1024KiB .crt file. Use `-s 0` for SRAM + 32KiB EEPROM, `-s 1` for SRAM + 8KiB EEPROM, `-s 2` for 32KiB EEPROM, `-s 3` for 8KiB EEPROM, or `-s 4` for SRAM only.

`md16` Magic Desk 16 .crt file

`mf` Magic Formel .crt file

`max` MAX Basic .crt file

`mb` Megabyter .crt file

`mikro` Mikro Assembler .crt file

`mmc64` MMC64 .crt file

`mmcr` MMC Replay .crt file

`mv` Magic Voice .crt file

`mm` MultiMAX .crt file

`ocean` Ocean .crt file

`p64` Prophet64 .crt file

`pf` Pagefox .crt file

`partner64`
Partner 64 .crt file

`pd` Profi-DOS .crt file

`rep256` REX 256KiB EPROM Cart .crt file, extra files can be inserted (1)(2)(3)

`rgcd` RGCD .crt file

`ross` ROSS .crt file

`rr` Retro Replay .crt file

`rrnet` RR-Net MK3 .crt file

`rrf` REX RAM-Floppy .crt file

`ru` REX Utility .crt file

`sdbox` SD-BOX .crt file

`s64` Snapshot 64 .crt file

`sb` Structured BASIC .crt file

`se5` Super Explode V5.0 .crt file

`sg` Super Games .crt file

`silver` Silverrock 128KiB Cartridge .crt file

`simon` Simons' BASIC .crt file

`ss4` Super Snapshot V4 .crt file

`ss5` Super Snapshot V5 .crt file

`star` Stardos .crt file

`turtle` Turtle Graphics II .crt file

`uc1` Universal Cartridge 1 .crt file

`uc15` Universal Cartridge 1.5 .crt file

`uc2` Universal Cartridge 2 .crt file

`wl` Westermann Learning .crt file

`ws` Warp Speed .crt file

`zaxxon` Zaxxon .crt file

`zipp` ZIPP-CODE 48 .crt file

- (1) insertion of 32KiB EPROM files supported.
- (2) insertion of 8KiB .crt/binary files supported.
- (3) insertion of 16KiB .crt/binary files supported.

C128 cartridges:

`c128` generic C128 cartridge ("external function ROM")

`comal128` Comal 80

`gmod128` Gmod2-C128

`md128` Magic Desk 128

`partner128`
Partner 128

`ws128` Warpspeed128

VIC20 cartridges:

`vic20` Generic 8KiB/12KiB/16KiB cartridge

`bb` Behr Bonz

`fe3` Final Expansion

`mega` Mega-Cart

`mini` Minimon

`ma` Mikro Assembler

`rabbit` Rabbit tape turbo

`se` Super Expander

`um` UltiMem

`vicfp` Vic Flash Plugin

`wn` Write Now

PLUS4 cartridges:

`plus4` Generic 4KiB/8KiB/16KiB/32KiB cartridge

`c1lo` Generic 4KiB/8KiB/16KiB C1LO cartridge

`c1hi` Generic 4KiB/8KiB/16KiB C1HI cartridge

`c2lo` Generic 4KiB/8KiB/16KiB C2LO cartridge

`c2hi` Generic 4KiB/8KiB/16KiB C2HI cartridge

`jacint` 1MB Cartridge

`magic` c264 magic cart

`multi` Plus4 multi cart

`speedy` "Speedy" freezer cart

CBM2 cartridges:

`cbm2` Generic 4KiB/8KiB/16KiB/24KiB/32KiB cartridge

15.3 cartconv examples

`cartconv -i foo.crt -o foo.bin`
Convert a .crt file to a binary file with no load-address.

`cartconv -t prg -i foo.crt -o foo.prg`
Convert a .crt file to a .prg file with default load-address.

`cartconv -t prg -l 49152 -i foo.crt -o foo.prg`
Convert a .crt file to a .prg file with 49152 as the load-address.

`cartconv -t ocean -i foo.bin -o foo.crt`
Convert a binary file to an ocean type cartridge.

`cartconv -t mdp -s 1 -i game.bin -o game.crt`
Create a Magic Desk Plus cartridge with SRAM and 8KiB EEPROM.

`cartconv -t dep64 -i dep64.bin -i eprom.prg -o foo.crt`
Inserting a 32KiB EPROM file into an dep64 type cartridge.

- step 1 : use the dep64 binary file in VICE as a generic 8KiB cartridge.
- step 2 : generate an EPROM file.

- step 3 : get the EPROM file to the host computer.
- step 4 : insert the EPROM file into the final dep64 .crt file:

`cartconv -t dep256 -i dep256.bin -i somegame.crt -o foo.crt`
: Insert an 8KiB .crt file into a dep256 type cartridge.

`cartconv -t rep256 -i rep256.bin -i foo1.crt -i foo2.crt -i foo3.crt -o foo.crt`
: Insert multiple 8KiB .crt files into a rep256 type cartridge.

`cartconv -f foo.crt`
: Get information about a .crt file.

16 petcat

The petcat program is a text conversion utility, it can convert between ASCII, PETSCII and tokenized BASIC.

16.1 petcat command line options

`-help` Output help text

`-?` Same as above

`-version` show petcat version string and exit

`-v` verbose output

`-c` controls (interpret also control codes) (default if textmode)

`-nc` no controls (suppress control codes in printout) (default if non-textmode)

`-qc` convert all non alphanumeric characters inside quotes into controlcodes

`-ic` interpret control codes case-insensitive

`-d` output raw codes in decimal

`-h` write header (default if output is stdout)

`-nh` no header (default if output is a file)

`-skip <n>` Skip <n> bytes in the beginning of input file. Ignored on P00.

`-text` Force text mode

`-C<chksum>`
add listing checksums of the specified type. (see below)

`-<version>`
use keywords for <version> instead of the v7.0 ones

`-w<version>`
tokenize using keywords on specified Basic version.

`-k<version>`
list all keywords for the specified Basic version

`-k` list all Basic versions available.

`-l` Specify load address for program (in hex, no leading chars!).

`-o <name>` Specify the output file name

`-f` Force overwritten the output file. The default depends on the BASIC version.

BASIC Versions:

`10` Basic v10.0 (C65/C64DX)

`1p` Basic v1.0 (PET)

`2` Basic v2.0 (C64/VIC20/PET)

`3`	Basic v3.5 (C16)
`40`	Basic v4.0 (PET/CBM2)
`4e`	Basic v2.0 with Basic v4.0 extension (C64)
`4v`	Basic v2.0 with Basic v4.0 extension (VIC20)
`5`	Basic v2.0 with Basic v5.0 extension (VIC20)
`65`	Basic v65.0 (Mega65)
`70`	Basic v7.0 (C128)
`71`	Basic v7.0 with Basic v7.1 extension (C128)
`a`	Basic v2.0 with AtBasic (C64)
`bk`	Basic v2.0 with Kipper Basic (C64)
`blarg`	Basic v2.0 with Blarg (C64)
`bob`	Basic v2.0 with Basic on Bails (C64)
`bsx`	Basic v2.0 with Basex (C64)
`bwarsaw`	Basic v2.0 with Warsaw Basic (C64)
`bws`	Basic v2.0 with WS Basic (C64)
`bwsf`	Basic v2.0 with WS Basic final (C64)
`drago`	Basic v2.0 with Drago basic v2.2 (C64)
`easy`	Basic v2.0 with Easy Basic (VIC20)
`eve`	Basic v2.0 with Eve Basic (C64)
`exp20`	Basic v2.0 with Expanded Basic (VIC20)
`exp64`	Basic v2.0 with Expanded Basic (C64)
`f`	Basic v2.0 with Final Cartridge III (C64)
`game`	Basic v2.0 with Game Basic (C64)
`graph`	Basic v2.0 with Graphics basic (C64)
`lightning`	Basic v2.0 with Basic Lightning (C64)
`magic`	Basic v2.0 with Magic Basic (C64)
`mighty`	Basic v2.0 with Mighty Basic by Craig Bruce (VIC20)
`pegasus`	Basic v2.0 with Pegasus Basic v4.0 (C64)
`reu`	Basic v2.0 with REU-Basic (C64)
`simon`	Basic v2.0 with Simons' Basic extension (C64)
`speech`	Basic v2.0 with Speech Basic v2.7 (C64)
`superbas`	Basic v2.0 with Super Basic (C64)

`superexp` Basic v2.0 with Super Expander (VIC20)

`supergra` Basic v2.0 with Supergrafik 64 (C64)

`sxc` Basic v2.0 with Super Expander Chip (C64)

`tt64` Basic v2.0 with The Tool 64 (C64)

`turtle` Basic v2.0 with Turtle Basic by Craig Bruce (VIC20)

`ultra` Basic v2.0 with Ultrabasic-64 (C64)

`xbasic` Basic v2.0 with Xbasic (C64)

Checksummers:

`64er-v1` 64'er Checksummer v1 + v2

`64er-v3` 64'er Checksummer v3

`dossier` Commodore Dossier Checksummer

`f64` F64Summer

16.2 petcat extra features

- If a line does not begin with a line number, a new line number is created by taking the line number of the previous line and incrementing it by two.
- Lines starting with a semicolon will be treated as comments and are ignored
- Unlike the BASIC editor, petcat can create BASIC files that contain lines numbered in arbitrary order, and also duplicate line numbers.

16.3 petcat examples

`petcat -2 -o outputfile.txt -- inputfile.prg`
: De-tokenize, Convert inputfile.prg to a text file in outputfile.txt, using BASIC V2 only

`petcat -wsimon -o outputfile.prg -- inputfile.txt`
: Tokenize, Convert inputfile.txt to a PRG file in outputfile.prg, using Simons' BASIC

`petcat -text -o outputfile.txt -- inputfile.seq`
: Convert inputfile.seq to a Ascii text file in outputfile.txt.

`petcat -text -w2 -o outputfile.seq -- inputfile.txt`
: Convert inputfile.txt to a Petscii text SEQ file in outputfile.seq.

17 The emulator file formats

This chapter gives a technical description of the various files supported by the emulators.

17.1 The raw tape image format

(This section is based on v1.1 TAP specification by Per Hakan Sundell and Markus Brenner, additional format v2 expansion by Markus Brenner, plus some editing by groepaz, with some more VICE extensions)

Designed by Per Hakan Sundell (author of the CCS64 C64 emulator) in 1997, this format attempts to duplicate the data stored on a cassette tape, bit for bit. Since it is simply a representation of the raw serial data from a tape, it should handle *any* custom tape loaders that exist.

The TAP images are generally very large, being a minimum of eight times, and up to sixteen times as large as what a raw PRG file would be. This is due to the way the data is stored, with each bit of the original file now being one byte large in the TAP file. The layout is fairly simple, with a small 14-byte header followed by file data.

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F        ASCII
      -----------------------------------------------   ----------------
0000: 43 36 34 2D 54 41 50 45 2D 52 41 57 00 00 00 00   C64-TAPE-RAW????
0010: 51 21 08 00 2F 0F 0D 31 64 1D 26 0D 07 21 0A 12   Q!??/??1d?&??!??
0020: 4A 2F 2C 34 07 18 0D 31 07 04 23 04 0D 42 0D 1E   J/,4???1??#??B??
0030: 34 04 42 0D 20 15 5E 04 0D 18 61 0D 26 29 34 0D   4?B???^???a?&)4?
0040: 23 0D 07 0A 3F 55 04 0A 13 3F 07 0D 12 2B 18 0A   #????U???????+??
```

Offset	Description
$0000-000B	File signature (`C64-TAPE-RAW` (v0 and v1) or `C16-TAPE-RAW` (v2 only))
$000C	`TAP` version (see below for description), $00 - Original layout, $01 - Updated, $02 - Halfwave Extension
$000D	Computer Platform (0 = C64, 1 = VIC-20, 2 = C16, Plus/4, 3 = PET, 4 = C5x0, 5 = C6x0, C7x0)
$000E	Video Standard (0 = PAL, 1 = NTSC, 2 = OLD NTSC, 3 = PALN)
$000F	reserved for future expansion
$0010-0013	File data size (not including this header, in LOW/HIGH format) i.e. The above example image is $00082151 bytes long.
$0014-xxxx	File data

In TAP version $00 files, each data byte in the file data area represents the length of a pulse, when the hardware will trigger again. This pulse length is determined by the following formula:

```
pulse length (in seconds) = (8 * data byte) / (clock cycles)
```

A data value of $00 represents an "overflow" condition, any pulse length which is more that 255 * 8 in length.

For example, assuming the image was made on a PAL C64, a data value of $2F (47 in decimal) would be:

```
(47 * 8) / 985248 = .00038975 seconds.
```

The number of clock cycles per second refer to the clock rate the timers used for the tape routines in the respective computer system count with, and can be derived from the given computer platform and video standard, as in the following table:

Computer platform	**Video standard**	**Timer clock speed**
0 = C64	0 = PAL	985248 Hz
	1 = NTSC	1022730 Hz
	2 = OLD NTSC	1022730 Hz
	3 = PAL N	1023440 Hz
1 = VIC-20	0 = PAL	1108405 Hz
	1 = NTSC	1022727 Hz
2 = C16 / Plus4 (*)	0 = PAL	886724 Hz
	1 = NTSC	894886 Hz
3 = PET	0 = PAL	1000000 Hz
	1 = NTSC	1000000 Hz
4 = C5x0	0 = PAL	985248 Hz
	1 = NTSC	1022730 Hz
5 = C6x0 / C7x0	0 = PAL	2000000 Hz
	1 = NTSC	2000000 Hz

(*) The TED timers always run with the slower clock rate (886724 Hz for PAL machines), so the length of one TAP pulse should be relative to this frequency.

The original format did not include the "computer platform" and "video system" field, and the number of clock cycles per second was assumed to always be 985248, as if the tape was recorded on a PAL C64.

In TAP version $01 files, the data value of $00 has been re-purposed to represent cycle exact values, which may also be greater than 255 * 8. When a $00 is encountered, three bytes will follow which are the actual time (in cycles) of a pulse, and the above formula does not apply. The three bytes are stored in LOW/HIGH format.

TAP Version 2 is an extension made by Markus Brenner for C16 tapes. It is version 1, but each value represents a halfwave, starting with a '0'->'1' transition. The time encoding doesn't change.

The actual interpretation of the serial data takes a little more work to explain. The typical ROM tape loader (and some of the turbo loaders) will initialize a timer with a specified value and start it counting down. If either the tape data changes or the timer runs out, an IRQ will occur. The loader will determine which condition caused the IRQ. If the tape data changed before the timer ran out, we have a short pulse, or a "0" bit. If the timer ran out first, we have a long pulse, or a "1" bit. Doing this continuously and we decode the entire file.

Plus4 implementation hints: When playing back a TAP on a Plus/4 emulator, you should write a routine that sets the CAS_READ line to '0' for half of the time and '1' for the other half. This should be easy to implement, as your emulator probably counts in units of clock cycles anyway :) As for writing to a TAP file, you need to watch the CAS_WRITE register (processor port, bit 0x02) and start counting whenever a 0->1 transition happens.

17.2 The T64 tape image format

(This section is based on the documentation of the C64S distribution, and contains extra information and edits added by the VICE authors.)

The `T64` File Structure was developed by Miha Peternel for use in the C64S emulator. Since the format has various drawbacks (see further below), we recommend to use other formats (D64, TAP, P00 - depending on the usecase).

17.2.1 T64 File structure

Offset	Size	Description
0	64	tape record (T64 header)
64	32*n	file records for n directory entries
64+32*n	varies	binary contents of the files

17.2.2 Tape Record (T64 header)

Offset	Size	Description
0	32	"C64S tape image file", padded with zeros
32	2	T64 format version (v1.0: $0100, v1.1: $0101, v2.0: $0200)
34	2	number of directory entries (C64S supports a maximum of 63 files only)
36	2	number of used entries
38	2	free
40	24	user description (displayed in C64S tape menu), padded with spaces ($20)

17.2.3 File record

Offset	Size	Description
0	1	entry type (see below)
1	1	CBM file type (0x82 for PRG, 0x81 for SEQ, etc). 0 indicates the special C64S "FRZ" session snapshot.
2	2	start address. 0 if the files is a C64S session snapshot.
4	2	end address. 0 if the files is a C64S session snapshot. (*)
6	2	free
8	4	offset of file contents start within T64 file
12	4	free
16	16	CBM file name, padded with spaces ($20)

(*) In the early days of emulation, an utility called "conv64" was around, which created faulty T64 files that had an end address of $c3c6 regardless of the actual file size. Since these files are still around, VICE tries to detect and fix this when such T64 file is attached.

Valid entry types are:

Code	Explanation
0	free entry
1	normal tape file
2	tape file with header: header is saved just before file data (added in v1.1)
3	memory snapshot v0.9, uncompressed (C64S specific)
4	tape block (added in v2.0)
5	digitized stream (added in v2.0)

VICE only supports file type `1`. This also seems to be the only type that is in wide use "in the wild". All other types are unsupported in VICE, and practically non existent.

Notes:

- The actual CBM tape format has info on whether a file is "relocatable" (BASIC programs, tape header type 1) or "binary" (tape header type 3). Since the T64 format does not contain this info, it is not really suited for actual tape emulation (like what VICE does - C64S originally only used them as containers for loading single files). VICE will always use "binary" by default, unless "load to basic start (tape)" is selected in the autostart options, which will make it switch to "BASIC" type only for the first file that is being loaded. Note that originally C64S did not support multi-load programs using T64 at all!
- Handling of empty directory entries (ie deleting a file) from a T64 was poorly handled by C64S - the directory entry must be completely removed.
- While in theory all CBM filetypes can be used, in practise only PRG and SEQ will actually work.
- Since file names are padded with spaces ($20) rather than shifted spaces ($A0) like on disk, the file names can't have trailing spaces.
- Zero length files are not allowed (minimum length is 2 bytes).

17.3 The G64 GCR-encoded disk image format

17.3.1 The original format

(This section was contributed by Peter Schepers and slightly edited by Ettore Perazzoli.)

This format was defined in 1998 as a cooperative effort between several emulator people, mainly Per Håkan Sundell, author of the CCS64 C64 emulator, Andreas Boose of the VICE CBM emulator team and Joe Forster/STA, the author of Star Commander. It was the first real public attempt to create a format for the emulator community which removed almost all of the drawbacks of the other existing image formats, namely `D64`.

The intention behind `G64` is not to replace the widely used `D64` format, as `D64` works fine with the vast majority of disks in existence. It is intended for those small percentage of programs which demand to work with the 1541 drive in a non-standard way, such as reading or writing data in a custom format. The best example is with speeder software such as Action Cartridge in Warp Save mode or Vorpal which write track/sector data in another format other than standard GCR. The other obvious example is copy-protected software which looks for some specific data on a track, like the disk ID, which is not stored in a standard `D64` image.

`G64` has a deceptively simply layout for what it is capable of doing. We have a signature, version byte, some predefined size values, and a series of offsets to the track data and speed zones. It is what's contained in the track data areas and speed zones which is really at the heart of this format.

Each track entry in simply the raw stream of GCR data, just what a read head would see when a diskette is rotating past it. How the data gets interpreted is up to the program trying to access the disk. Because the data is stored in such a low-level manner, just about anything can be done. Most of the time I would suspect the data in the track would be

standard sectors, with SYNC, GAP, header, data and checksums. The arrangement of the data when it is in a standard GCR sector layout is beyond the scope of this document.

Since it is a flexible format in both track count and track byte size, there is no "standard" file size. However, given a few constants like 42 tracks and halftracks, a track size of 7928 bytes and no speed offset entries, the typical file size will a minimum of 333744 bytes.

Below is a dump of the header, broken down into its various parts. After that will be an explanation of the track offset and speed zone offset areas, as they demand much more explanation.

```
Addr  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
----  -----------------------------------------------
0000: 47 43 52 2D 31 35 34 31 00 54 F8 1E .. .. .. ..
```

Offset	Description
$0000-0007	File signature (`GCR-1541`)
$0008	`G64` version (presently only $00 defined)
$0009	Number of tracks in image (usually $54, decimal 84)
$000A-000B	Size of each stored track in bytes (usually 7928, or $1EF8) in LO/HI format.

An obvious question here is "why are there 84 tracks defined when a normal `D64` disk only has 35 tracks?" Well, by definition, this image includes all half-tracks, so there are actually 42 tracks and 42 half tracks. The 1541 stepper motor can access up to 42 tracks and the in-between half-tracks. Even though using more than 35 tracks is not typical, it was important to define this format from the start with what the 1541 is capable of doing, and not just what it typically does.

At first, the defined track size value of 7928 bytes may seem to be arbitrary, but it is not. It is determined by the fastest write speed possible (speed zone 0), coupled with the average rotation speed of the disk (300 rpm). After some math, the answer that actually comes up is 7692 bytes. Why the discrepency between the actual size of 7692 and the defined size of 7928? Simply put, not all drives rotate at 300 rpm. Some can be faster or slower, so a upper safety margin of +3% was built added, in case some disks rotate slower and can write more data. After applying this safety factor, and some rounding-up, 7928 bytes per track was arrived at.

Also note that this upper limit of 7928 bytes per track really only applies to 1541 and compatible disks. If this format were applied to another disk type like the SFD1001, this value would be higher.

Below is a dump of the first section of a `G64` file, showing the offsets to the data portion for each track and half-track entry. Following that is a dump of the speed zone offsets.

```
Addr  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
----  -----------------------------------------------
0000: .. .. .. .. .. .. .. .. .. .. .. .. AC 02 00 00
0010: 00 00 00 00 A6 21 00 00 00 00 00 00 A0 40 00 00
0020: 00 00 00 00 9A 5F 00 00 00 00 00 00 94 7E 00 00
0030: 00 00 00 00 8E 9D 00 00 00 00 00 00 88 BC 00 00
0040: 00 00 00 00 82 DB 00 00 00 00 00 00 7C FA 00 00
0050: 00 00 00 00 76 19 01 00 00 00 00 00 70 38 01 00
0060: 00 00 00 00 6A 57 01 00 00 00 00 00 64 76 01 00
```

```
0070: 00 00 00 00 5E 95 01 00 00 00 00 00 58 B4 01 00
0080: 00 00 00 00 52 D3 01 00 00 00 00 00 4C F2 01 00
0090: 00 00 00 00 46 11 02 00 00 00 00 00 40 30 02 00
00A0: 00 00 00 00 3A 4F 02 00 00 00 00 00 34 6E 02 00
00B0: 00 00 00 00 2E 8D 02 00 00 00 00 00 28 AC 02 00
00C0: 00 00 00 00 22 CB 02 00 00 00 00 00 1C EA 02 00
00D0: 00 00 00 00 16 09 03 00 00 00 00 00 10 28 03 00
00E0: 00 00 00 00 0A 47 03 00 00 00 00 00 04 66 03 00
00F0: 00 00 00 00 FE 84 03 00 00 00 00 00 F8 A3 03 00
0100: 00 00 00 00 F2 C2 03 00 00 00 00 00 EC E1 03 00
0110: 00 00 00 00 E6 00 04 00 00 00 00 00 E0 1F 04 00
0120: 00 00 00 00 DA 3E 04 00 00 00 00 00 D4 5D 04 00
0130: 00 00 00 00 CE 7C 04 00 00 00 00 00 C8 9B 04 00
0140: 00 00 00 00 C2 BA 04 00 00 00 00 00 BC D9 04 00
0150: 00 00 00 00 B6 F8 04 00 00 00 00 00 .. .. .. ..
```

Offset	**Description**
$000C-000F	Offset to stored track 1.0 ($000002AC, in LO/HI format, see below for more)
$0010-0013	Offset to stored track 1.5 ($00000000)
$0014-0017	Offset to stored track 2.0 ($000021A6)
...	
$0154-0157	Offset to stored track 42.0 ($0004F8B6)
$0158-015B	Offset to stored track 42.5 ($00000000)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
      -----------------------------------------------
0150: .. .. .. .. .. .. .. .. .. .. .. .. 03 00 00 00
0160: 00 00 00 00 03 00 00 00 00 00 00 00 03 00 00 00
0170: 00 00 00 00 03 00 00 00 00 00 00 00 03 00 00 00
0180: 00 00 00 00 03 00 00 00 00 00 00 00 03 00 00 00
0190: 00 00 00 00 03 00 00 00 00 00 00 00 03 00 00 00
01A0: 00 00 00 00 03 00 00 00 00 00 00 00 03 00 00 00
01B0: 00 00 00 00 03 00 00 00 00 00 00 00 03 00 00 00
01C0: 00 00 00 00 03 00 00 00 00 00 00 00 03 00 00 00
01D0: 00 00 00 00 03 00 00 00 00 00 00 00 03 00 00 00
01E0: 00 00 00 00 02 00 00 00 00 00 00 00 02 00 00 00
01F0: 00 00 00 00 02 00 00 00 00 00 00 00 02 00 00 00
0200: 00 00 00 00 02 00 00 00 00 00 00 00 02 00 00 00
0210: 00 00 00 00 02 00 00 00 00 00 00 00 01 00 00 00
0220: 00 00 00 00 01 00 00 00 00 00 00 00 01 00 00 00
0230: 00 00 00 00 01 00 00 00 00 00 00 00 01 00 00 00
0240: 00 00 00 00 01 00 00 00 00 00 00 00 00 00 00 00
0250: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0260: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0270: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0280: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0290: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

```
02A0: 00 00 00 00 00 00 00 00 00 00 00 00 .. .. .. ..
```

Offset	Description
$015C-015F	Speed zone entry for track 1 ($03, in LO/HI format, see below for more)
$0160-0163	Speed zone entry for track 1.5 ($03)
. . .	
$02A4-02A7	Speed zone entry for track 42 ($00)
$02A8-02AB	Speed zone entry for track 42.5 ($00)

Starting here at $02AC is the first track entry (from above, it is the first entry for track 1.0)

The track offsets (from above) require some explanation. When one is set to all 0's, no track data exists for this entry. If there is a value, it is an absolute reference into the file (starting from the beginning of the file). From the track 1.0 entry we see it is set for $000002AC. Going to that file offset, here is what we see. . .

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
      -----------------------------------------------
02A0: .. .. .. .. .. .. .. .. .. .. .. .. 0C 1E FF FF
02B0: FF FF FF 52 54 B5 29 4B 7A 5E 95 55 55 55 55 55
02C0: 55 55 55 55 55 55 FF FF FF FF FF 55 D4 A5 29 4A
02D0: 52 94 A5 29 4A 52 94 A5 29 4A 52 94 A5 29 4A 52
```

Offset	Description
$02AC-02AD	Actual size of stored track (7692 or $1E0C, in LO/HI format)
$02AE-02AE+$1E0C	Track data

Following the track data is filler bytes. In this case, there are 368 bytes of unused space. This space can contain anything, but for the sake of those wishing to compress these images for storage, they should all be set to the same value. In the sample I used, these are all set to $FF.

Below is a dump of the end of the track 1.0 data area. Note the actual track data ends at address $20B9, with the rest of the block being unused, and set to $FF.

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
      -----------------------------------------------
1FE0: 52 94 A5 29 4A 52 94 A5 29 4A 52 94 A5 29 4A 52
1FF0: 94 A5 29 4A 52 94 A5 29 4A 52 94 A5 29 4A 52 94
2000: A5 29 4A 52 94 A5 29 4A 52 94 A5 29 4A 52 94 A5
2010: 29 4A 52 94 A5 29 4A 52 94 A5 29 4A 52 94 A5 29
2020: 4A 52 94 A5 29 4A 52 94 A5 29 4A 52 94 A5 29 4A
2030: 55 55 55 55 55 55 FF FF FF FF FF FF FF FF FF FF
2040: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2050: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2060: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2070: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2080: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2090: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
20A0: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
```

```
20B0: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
20C0: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
20D0: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
20E0: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
20F0: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2100: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2110: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2120: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2130: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2140: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2150: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2160: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2170: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2180: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
2190: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
21A0: FF FF FF FF FF FF .. .. .. .. .. .. .. .. .. ..
```

The speed offset entries can be a little more complex. The 1541 has four speed zones defined, which means the drive can write data at four distinct speeds. On a normal 1541 disk, these zones are as follows:

Track Range	**Speed Zone**
1-17	3 (highest writing speed)
18-24	2
25-30	1
31 and up	0 (lowest writing speed)

Note that you can, through custom programming of the 1541, change the speed zone of any track to something different (change the 3 to a 0) and write data differently. From the dump of the speed offset entries above, we see that all the entries are in the range of 0-3. If any entry is less than 4, this is not considered a speed offset but defines the whole track to be recorded at that one speed.

In the example I had, there were no offsets defined, so no speed zone dump can be shown. However, I can define what should be there. You will have a block of data, 1982 bytes long. Each byte is encoded to represent the speed of 4 bytes in the track offset area, and is broken down as follows:

```
Speed entry $FF:  in binary %11111111
                               |'|'|'|'
                               | | | |
                               | | | +- 4'th byte speed (binary 11, 3 dec)
                               | | +--- 3'rd byte speed (binary 11, 3 dec)
                               | +----- 2'nd byte speed (binary 11, 3 dec)
                               +------- 1'st byte speed (binary 11, 3 dec)
```

It was very smart thinking to allow for two speed zone settings, one in the offset block and another defining the speed on a per-byte basis. If you are working with a normal disk, where each track is one constant speed, then you don't need the extra blocks of information hanging around the image, wasting space.

What may not be obvious is the flexibility of this format to add tracks and speed offset zones at will. If a program decides to write a track out with varying speeds, and no speed offset exist, a new block will be created by appending it to the end of the image, and the offset pointer for that track set to point to the new block. If a track has no offset yet, meaning it doesn't exist (like a half-track), and one needs to be added, the same procedure applies. The location of the actual track or speed zone data is not important, meaning they do not have to be in any particular order since they are all referenced by the offsets at the beginning of the image.

17.3.2 An extension for double sided disks (Commodore VIC 1571)

Given that a Commodore VIC 1541/1570/1571 can read/write at most 42 tracks per side, it is quite natural to use the half-tracks from 1 to 84 for the first side and the following 84 half-tracks (i. e. half-track 85 to 168) for the second side.

In order for this to work, byte 9 of the image (i. e. the maximum number of tracks) usually contains 168 for double sided disks.

To make identifying such images easy, the file signature in the first 8 bytes of such files is `GCR-1571`).

17.3.3 Extra mastering info (SPS extension)

G64s created by the DTC tool from Kryoflux dumps contain extra info for each track, usually at offset $02ac right after the speedzone entries.

First comes the extra info tag:

Offset	Description
$00-$02	extra info tag (**EXT**) $45,$58,$54
$03	extra info version ($01)

Followed by 16 bytes of extra info for each track in the image:

Offset	Description
$00	write splice position
$04	write area size
$08	bitcell size in nanoseconds (eg 3500 means 3.5us)
$0c	track fill value
$0d	n/a
$0e	format code
$0f	format extension

A zero value in any of these fields means that the respective default/standard value applies.

Format codes:

ID	Description
0	Unknown format
1	GCR Data
2	CBM DOS
3	CBM DOS Extended
4	MicroProse
5	RapidLok
6	Datasoft

7	Vorpal
8	V-MAX!
9	Teque
10	TDP
11	Big Five
12	OziSoft

Format Extensions:

ID	**Description**
0	Unknown protection
1	Datasoft with Weak bits
2	CBM DOS with Cyan loader, Weak bits
3	CBM DOS with Datasoft, Weak bits
4	RapidLok Key
5	Data Duplication
6	Melbourne House
7	Melbourne House, Weak bits
8	PirateBusters v1.0
9	PirateBusters v2.0, Track A
10	PirateBusters v2.0, Track B
11	PirateSlayer
12	CBM DOS, XEMAG

17.4 The P64 NRZI flux pulse disk image format

This section is taken from "P64 file format specification" by Benjamin 'BeRo' Rosseaux.

All values are in little endian order !

17.4.1 P64 Header Layout

```
       0   1   2   3   4   5   6   7   8   9   A   B   C   D   E   F
     +-------------------------------------------------------------------+
0000: |'P'|'6'|'4'|'-'|'1'|'5'|'4'|'1'|    Version    |     Flags     |
     +---------------+---------------+-------------------------------+
0010: |     Size      | CRC32Checksum |
     +-------------------------------+
```

Version: File format version, current is 0x00000000

Size Size of the following whole chunk content stream

Flags: Bit 0 = Write protect Bit 1 = Number of sides (0 for single sided, 1 for double sided) Bit 2-31 = Reserved, all set to 0 when creating a file, preserve existing value when updating

CRC32CheckSum: CRC32 checksum of the following whole chunk content stream

17.4.2 P64 Chunk Header Layout

```
       0   1   2   3   4   5   6   7   8   9   A   B   C   D   E   F
     +-----------------------------------------------+
0000: |Chunk Signature|      Size     | CRC32Checksum |
     +-----------------------------------------------+
```

Chunk signature: Signature of chunk

Size: Size of the chunk data

CRC32CheckSum: CRC32 checksum of the chunk data

17.4.3 P64 Chunk 'HTPx' Layout

| x = half track index byte | +———————+ Bit 7 of the half track index byte contains the side.

Track 18 = Half track 36 = Half track index byte decimal value 36

Half track NRZI transition flux pulse data chunk block

```
        0   1   2   3   4   5   6   7   8   9   A   B   C   D   E   F
      +-------------------------------------------------------------------+
0000: |  Count pulses |     Size      | ..... Range-encoded data .... |
      +-------------------------------------------------------------------+
```

Count pulses: Count of the NRZI transition flux pulses in half track

Size: Size of the range-encoded data

17.4.4 'HTPx' Range encoded data format

Hint: For a working C implememtation see p64.c and p64.h

The range coder is a FPAQ0-style range coder combined with 12-bit 0-order models, one model per byte with one bit per byte processing.

Sub stream	Count of models	Size per model	Total value bits
Position	4	65536	32
Strength	4	65536	32
Position flag	1	2	1
Strenth flag	1	2	1
Total models	10		

All initial model state values are initialized with zero.

All initial model probability values are initialized with 2048.

These model probability values will be updating in a adaptive way on the fly and not precalculated before the encoding and even not loaded before the decoding, see pseudo code below.

16000000 Hz / 5 rotations per second at 300 RPM = maximal 3200000 flux pulses

So NRZI transition flux pulse positions are in the 0 .. 3199999 value range, which is also a exact single rotation, where each time unit is a cycle at 16 MHz with 300 RPM as a mapping for the ideal case.

The NRZI transition flux pulse stength are in the 0x00000000 .. 0xffffffff value range, where 0xffffffff indices a strong flux pulse, that always triggers, and 0x00000001 indices a weak

flux pulse, that almost never triggers, and 0x00000000 indices a flux pulse, that absolutly never triggers.

For 32-bit values, the model sub streams are subdivided byte wide in a little-endian manner, and each byte is processed bitwise with model probability shifting of 4 bits, just as:

Pascal-Style pseudo code:

```
procedure WriteDWord(Model, Value : longword);
var ByteValue, ByteIndex, Context, Bit : longword;
begin
  for ByteIndex := 0 to 3 do begin
    ByteValue := (Value shr (ByteIndex shl 3)) and $ff;
    Context := 1;
    for Bit := 7 downto 0 do begin
      Context := (Context shl 1) or RangeCoderEncodeBit(
                    RangeCoderProbabilities[
                     RangeCoderProbabilityOffsets[Model + ByteIndex] +
                     (((RangeCoderProbabilityStates[Model + ByteIndex]
                     shl 8) or Context) and $ffff)], 4, (ByteValue shr
                     Bit) and 1);
    end;
    RangeCoderProbabilityStates[Model+ByteIndex] := ByteValue;
  end;
end;
```

And for 1-bit flag values it is much simpler, but also with model probability shifting of 4 bits, just as:

Pascal-Style pseudo code:

```
procedure WriteBit(Model, Value : longword);
begin
   RangeCoderProbabilityStates[Model] :=
     RangeCoderEncodeBit(RangeCoderProbabilities[
       RangeCoderProbabilityOffsets[Model] +
         RangeCoderProbabilityStates[Model]], 4, Value and 1);
end;
```

The position and strength values are delta-encoded. If a value is equal to the last previous value, then the value will not encoded, instead, a flag for this will encoded. First the position value will encoded, then the stength value. If the last position delta is 0, then it is a track stream end marker.

Pascal-Style pseudo code:

```
LastPosition := 0;
PreviousDeltaPosition := 0

LastStrength := 0;

for PulseIndex := 0 to PulseCount - 1 do begin

  DeltaPosition := Pulses[PulseIndex].Position - LastPosition;
```

```
  if PreviousDeltaPosition <> DeltaPosition then begin
    PreviousDeltaPosition := DeltaPosition;
    WriteBit(ModelPositionFlag, 1)
    WriteDWord(ModelPosition, DeltaPosition);
  end else begin
    WriteBit(ModelPositionFlag, 0);
  end;
  LastPosition := Pulses[PulseIndex].Position;

  if LastStrength <> Pulses[PulseIndex].Strength then begin
    WriteBit(ModelStrengthFlag, 1)
    WriteDWord(ModelStrength, Pulses[PulseIndex].Strength - LastStrength);
  end else begin
    WriteBit(ModelStrengthFlag, 0);
  end;
  LastStrength := Pulses^[PulseIndex].Strength;

end;

// End code
WriteBit(ModelPositionFlag, 1);
WriteDWord(ModelPosition, 0);
```

The decoding is simply just in the another direction way.

Pseudo code for a FPAQ0-style carryless range coder:

Pascal-Style pseudo code:

```
procedure RangeCoderInit; // At encoding and decoding start
begin
  RangeCode := 0;
  RangeLow := 0;
  RangeHigh := $ffffffff;
end;

procedure RangeCoderStart; // At decoding start
var Counter : longword;
begin
  for Counter := 1 to 4 do begin
   RangeCode := (RangeCode shl 8) or ReadByteFromInput;
  end;
end;

procedure RangeCoderFlush; // At encoding end
var Counter : longword;
begin
  for Counter := 1 to 4 do begin
    WriteByteToOutput(RangeHigh shr 24);
    RangeHigh := RangeHigh shl 8;
```

```
  end;
end;

procedure RangeCoderEncodeNormalize;
begin
  while ((RangeLow xor RangeHigh) and $ff000000) = 0 do begin
   WriteByteToOutput(RangeHigh shr 24);
   RangeLow := RangeLow shl 8;
   RangeHigh := (RangeHigh shl 8) or $ff;
  end;
end;

function RangeCoderEncodeBit(var Probability : longword; Shift,
                             BitValue : longword) : longword;
begin
  RangeMiddle := RangeLow + (((RangeHigh - RangeLow) shr 12) *
                              Probability);
  if BitValue <> 0 then begin
    inc(Probability, ($fff - Probability) shr Shift);
    RangeHigh := RangeMiddle;
  end else begin
    dec(Probability, Probability shr Shift);
    RangeLow := RangeMiddle + 1;
  end;
  RangeCoderEncodeNormalize;
  result := BitValue;
end;

procedure RangeCoderDecodeNormalize;
begin
  while ((RangeLow xor RangeHigh) and $ff000000) = 0 do begin
    RangeLow := RangeLow shl 8;
    RangeHigh := (RangeHigh shl 8) or $ff;
    RangeCode := (RangeCode shl 8) or ReadByteFromInput;
  end;
end;

function RangeCoderDecodeBit(var Probability : longword;
                             Shift : longword) : longword;
begin
  RangeMiddle := RangeLow + (((RangeHigh - RangeLow) shr 12) *
                             Probability);
  if RangeCode <= RangeMiddle then begin
    inc(Probability, ($fff - Probability) shr Shift);
    RangeHigh := RangeMiddle;
    result := 1;
  end else begin
```

```
    dec(Probability, Probability shr Shift);
    RangeLow := RangeMiddle + 1;
    result := 0;
  end;
  RangeCoderDecodeNormalize;
end;
```

The probability may be never zero! But that can't happen here with this adaptive model in this P64 file format, since the adaptive model uses a shift factor of 4 bits and initial probabilities value of 2048, so the probability has a value range from 15 up to 4080 here. If you do want to use the above range coder routines for other stuff with other probability models, then you must to ensure that the probability output value is never zero, for example with "probability |= (probability < 1); " in C.

17.4.5 P64 Chunk 'DONE' Layout

This is the last empty chunk for to signalize that the correct file end is reached.

17.5 The D64 disk image format

(This section was contributed by Peter Schepers and slightly edited by Marco van den Heuvel. Added 42 track info by groepaz)

First and foremost we have D64, which is basically a sector-for-sector copy of a 1540/1541 disk. There are several versions of these which I will cover shortly. The standard D64 is a 174848 byte file comprised of 256 byte sectors arranged in 35 tracks with a varying number of sectors per track for a total of 683 sectors. Track counting starts at 1, not 0, and goes up to 35. Sector counting starts at 0, not 1, for the first sector, therefore a track with 21 sectors will go from 0 to 20.

The original media (a 5.25" disk) has the tracks laid out in circles, with track 1 on the very outside of the disk (closest to the sides) to track 35 being on the inside of the disk (closest to the inner hub ring). Commodore, in their infinite wisdom, varied the number of sectors per track and data densities across the disk to optimize available storage, resulting in the chart below. It shows the sectors/track for a standard D64. Since the outside diameter of a circle is the largest (versus closer to the center), the outside tracks have the largest amount of storage.

Track	**Sectors/track**	**# Sectors**
1-17	21	357
18-24	19	133
25-30	18	108
31-35	17	85
36-40(*)	17	85
41-42(*)	17	34

Track	**#Sect**	**#SectorsIn**	**D64 Offset**
1	21	0	$00000
2	21	21	$01500
3	21	42	$02A00
4	21	63	$03F00
5	21	84	$05400

6	21	105	$06900
7	21	126	$07E00
8	21	147	$09300
9	21	168	$0A800
10	21	189	$0BD00
11	21	210	$0D200
12	21	231	$0E700
13	21	252	$0FC00
14	21	273	$11100
15	21	294	$12600
16	21	315	$13B00
17	21	336	$15000
18	19	357	$16500
19	19	376	$17800
20	19	395	$18B00
21	19	414	$19E00
22	19	433	$1B100
23	19	452	$1C400
24	19	471	$1D700
25	18	490	$1EA00
26	18	508	$1FC00
27	18	526	$20E00
28	18	544	$22000
29	18	562	$23200
30	18	580	$24400
31	17	598	$25600
32	17	615	$26700
33	17	632	$27800
34	17	649	$28900
35	17	666	$29A00
36(*)	17	683	$2AB00
37(*)	17	700	$2BC00
38(*)	17	717	$2CD00
39(*)	17	734	$2DE00
40(*)	17	751	$2EF00
41(*)	17	768	$30000
42(*)	17	785	$31100

(*) Tracks 36-40 apply to 40- and 42-track images only. (*) Tracks 41-42 apply to 42-track images only.

The directory track should be contained totally on track 18. Sectors 1-18 contain the entries and sector 0 contains the BAM (Block Availability Map) and disk name/ID. Since the directory is only 18 sectors large (19 less one for the BAM), and each sector can contain only 8 entries (32 bytes per entry), the maximum number of directory entries is 18 * 8 = 144. The first directory sector is always 18/1, even though the t/s pointer at 18/0 (first two bytes) might point somewhere else. It then follows the same chain structure as a normal file, using a sector interleave of 3. This makes the chain links go 18/1, 18/4, 18/7 etc.

Note that you can extend the directory off of track 18, but only when reading the disk or image. Attempting to write to a directory sector not on track 18 will cause directory corruption. Each directory sector has the following layout (18/1 partial dump):

```
00: 12 04 81 11 00 4E 41 4D 45 53 20 26 20 50 4F 53 <- notice the T/S link
10: 49 54 A0 A0 A0 00 00 00 00 00 00 00 00 00 15 00 <- to 18/4 ($12/$04)
20: 00 00 84 11 02 41 44 44 49 54 49 4F 4E 41 4C 20 <- and how its not here
30: 49 4E 46 4F A0 11 0C FE 00 00 00 00 00 00 61 01 <- ($00/$00)
```

The first two bytes of the sector ($12/$04) indicate the location of the next track/sector of the directory (18/4). If the track is set to $00, then it is the last sector of the directory. It is possible, however unlikely, that the directory may *not* be competely on track 18 (some disks do exist like this). Just follow the chain anyhow.

When the directory is done, the track value will be $00. The sector link should contain a value of $FF, meaning the whole sector is allocated, but the actual value doesn't matter. The drive will return all the available entries anyways.

This is a breakdown of a standard directory sector:

Bytes	**Description**
$00-$1F	First directory entry
$20-$3F	Second dir entry
$40-$5F	Third dir entry
$60-$7F	Fourth dir entry
$80-$9F	Fifth dir entry
$A0-$BF	Sixth dir entry
$C0-$DF	Seventh dir entry
$E0-$FF	Eighth dir entry

This is a breakdown of a standard directory entry:

Bytes	**Description**
$00-$01	Track/Sector location of next directory sector ($00 $00 if not the first entry in the sector)
$02	File type
$03-$04	Track/sector location of first sector of file
$05-$14	16 character filename (in PETASCII, padded with $A0)
$15-$16	Track/Sector location of first side-sector block (REL file only)
$17	REL file record length (REL file only, max. value 254)
$18-$1D	Unused (except with GEOS disks)
$1E-$1F	File size in sectors, low/high byte order ($1E+$1F*256). The approx. filesize in bytes is <= #sectors * 254

The file type field is used as follows:

Bits	**Description**
0-3	The actual file type
4	Unused
5	Used only during SAVE-@ replacement
6	Locked flag (Set produces ">" locked files)
7	Closed flag (Not set produces "*", or "splat" files)

The actual file type can be one of the following:

Binary	Decimal	File type
0000	0	DEL
0001	1	SEQ
0010	2	PRG
0011	3	USR
0100	4	REL

Values 5-15 are illegal, but if used will produce very strange results. The 1541 is inconsistent in how it treats these bits. Some routines use all 4 bits, others ignore bit 3, resulting in values from 0-7.

Files, on a standard 1541, are stored using an interleave of 10. Assuming a starting track/sector of 17/0, the chain would run 17/0, 17/10, 17/20, 17/8, 17/18, etc.

17.5.1 Non-Standard & Long Directories

Most Commdore floppy disk drives use a single dedicated directory track where all filenames are stored. This limits the number of files stored on a disk based on the number of sectors on the directory track. There are some disk images that contain more files than would normally be allowed. This requires extending the directory off the default directory track by changing the last directory sector pointer to a new track, allocating the new sectors in the BAM, and manually placing (or moving existing) file entries there. The directory of an extended disk can be read and the files that reside there can be loaded without problems on a real drive. However, this is still a very dangerous practice as writing to the extended portion of the directory will cause directory corruption in the non-extended part. Many of the floppy drives core ROM routines ignore the track value that the directory is on and assume the default directory track for operations.

To explain: assume that the directory has been extended from track 18 to track 19/6 and that the directory is full except for a few slots on 19/6. When saving a new file, the drive DOS will find an empty file slot at 19/6 offset $40 and correctly write the filename and a few other things into this slot. When the file is done being saved the final file information will be written to 18/6 offset $40 instead of 19/6 causing some directory corruption to the entry at 18/6. Also, the BAM entries for the sectors occupied by the new file will not be saved and the new file will be left as a SPLAT (*) file.

Attempts to validate the disk will result in those files residing off the directory track to not be allocated in the BAM, and could also send the drive into an endless loop. The default directory track is assumed for all sector reads when validating so if the directory goes to 19/6, then the validate code will read 18/6 instead. If 18/6 is part of the normal directory chain then the validate routine will loop endlessly.

17.5.2 BAM layout

The layout of the BAM area (sector 18/0) is a bit more complicated...

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
     -----------------------------------------------
00: 12 01 41 00 12 FF F9 17 15 FF FF 1F 15 FF FF 1F
10: 15 FF FF 1F 12 FF F9 17 00 00 00 00 00 00 00 00
20: 00 00 00 00 0E FF 74 03 15 FF FF 1F 15 FF FF 1F
30: 0E 3F FC 11 07 E1 80 01 15 FF FF 1F 15 FF FF 1F
```

```
40: 15 FF FF 1F 15 FF FF 1F 0D C0 FF 07 13 FF FF 07
50: 13 FF FF 07 11 FF CF 07 13 FF FF 07 12 7F FF 07
60: 13 FF FF 07 0A 75 55 01 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 01 08 00 00 03 02 48 00
80: 11 FF FF 01 11 FF FF 01 11 FF FF 01 11 FF FF 01
90: 53 48 41 52 45 57 41 52 45 20 31 20 20 A0 A0 A0
A0: A0 A0 56 54 A0 32 41 A0 A0 A0 A0 00 00 00 00 00
B0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Bytes	**Description**
$00-$01	Track/Sector location of the first directory sector (should be set to 18/1 but it doesn't matter, and don't trust what is there, always go to 18/1 for first directory entry)
$02	Disk DOS version type (see note below) $41 ("A")
$03	Unused
$04-$8F	BAM entries for each track, in groups of four bytes per track, starting on track 1 (see below for more details)
$90-$9F	Disk Name (padded with $A0)
$A0-$A1	Filled with $A0
$A2-$A3	Disk ID
$A4	Usually $A0
$A5-$A6	DOS type, usually "2A"
$A7-$AA	Filled with $A0
$AB	Unused ($00)
$AC-$BF	For DOLPHIN DOS track 36-40 BAM entries, otherwise unused ($00)
$C0-$D3	For SPEED DOS track 36-40 BAM entries, otherwise unused ($00)
$D4-$FF	Unused ($00)

Note: The BAM entries for SPEED, DOLPHIN and ProLogic DOS use the same layout as standard BAM entries. One of the interesting things from the BAM sector is the byte at offset $02, the DOS version byte. If it is set to anything other than $41 or $00, then we have what is called "soft write protection". Any attempt to write to the disk will return the "DOS Version" error code 73 ,"CBM DOS V 2.6 1541". The 1541 is simply telling you that it thinks the disk format version is incorrect. This message will normally come up when you first turn on the 1541 and read the error channel. If you write a $00 or a $41 into 1541 memory location $00FF (for device 0), then you can circumvent this type of write-protection, and change the DOS version back to what it should be.

The BAM entries require a bit (no pun intended) more of a breakdown. Take the first entry at bytes $04-$07 ($12 $FF $F9 $17). The first byte ($12) is the number of free sectors on that track. Since we are looking at the track 1 entry, this means it has 18 (decimal) free sectors. The next three bytes represent the bitmap of which sectors are used/free. Since it is 3 bytes (8 bits/byte) we have 24 bits of storage. Remember that at most, each track only has 21 sectors, so there are a few unused bits.

Bytes	Data	Description
$04-$07	$12 $FF $F9 $17	Track 1 BAM
$08-$0B	$15 $FF $FF $FF	Track 2 BAM
$0C-$0F	$15 $FF $FF $1F	Track 3 BAM
...	...	...
$8C-$8F	$11 $FF $FF $01	Track 35 BAM

These entries must be viewed in binary to make any sense. We will use the first entry (track 1) at bytes 04-07:

```
FF=11111111, F9=11111001, 17=00010111
```

In order to make any sense from the binary notation, flip the bits around.

```
           111111 11112222
01234567 89012345 67890123
-------------------------
11111111 10011111 11101000
^                     ^
sector 0           sector 20
```

Since we are on the first track, we have 21 sectors, and only use up to the bit 20 position. If a bit is on (1), the sector is free. Therefore, track 1 has sectors 9, 10 and 19 used, all the rest are free. Any leftover bits that refer to sectors that don't exist, like bits 21-23 in the above example, are set to allocated.

Each filetype has its own unique properties, but most follow one simple structure. The first file sector is pointed to by the directory and follows a t/s chain, until the track value reaches $00. When this happens, the value in the sector link location indicates how much of the sector is used. For example, the following chain indicates a file 6 sectors long, and ends when we encounter the $00/$34 chain. At this point the last sector occupies from bytes $02-$34.

1	2	3	4	5	6
—-	—-	—-	—-	—-	—-
17/0	17/10	17/20	17/1	17/11	0/52
(11/00)	(11/0A)	(11/14)	(11/01)	(11/0B)	(0/34)

17.5.3 Variations on the D64 layout

These are some variations of the D64 layout:

1. Standard 35 track layout but with 683 error bytes added on to the end of the file. Each byte of the error info corresponds to a single sector stored in the D64, indicating if the sector on the original disk contained an error. The first byte is for track 1/0, and the last byte is for track 35/16.

2. A 40 track layout, following the same layout as a 35 track disk, but with 5 extra tracks. These contain 17 sectors each, like tracks 31-35. Some of the PC utilities do allow you to create and work with these files. This can also have error bytes attached like variant #1.

3. A 42 track layout, with two extra tracks of 17 sectors each. This is extremely uncommon, since real drives often have problems with accessing these tracks, software that uses them is very rare.

4. The Commodore 128 allowed for "auto-boot" disks. With this, t/s 1/0 holds a specific byte sequence which the computer recognizes as boot code.

Below is a small chart detailing the standard file sizes of D64 images, 35, 40 or 42 tracks, with or without error bytes.

Disk type	**Size**
35 track, no errors	174848
35 track, 683 error bytes	175531
40 track, no errors	196608
40 track, 768 error bytes	197376
42 track, no errors	205312
42 track, 802 error bytes	206114

The following table (provided by Wolfgang Moser) outlines the differences between the standard 1541 DOS and the various "speeder" DOS's that exist. The 'header 7/8' category is the 'fill bytes' as the end of the sector header of a real 1541 disk.

Disk format	**tracks**	**header 7/8**	**Dos type**	**Diskdos type**	**vs.**
Original CBM DOS v2.6	35	$0f $0f	"2A"	$41/'A'	
*SpeedDOS+	40	$0f $0f	"2A"	$41/'A'	
Professional DOS Initial	35	$0f $0f	"2A"	$41/'A'	
Professional DOS Version 1/Prototype	40	$0f $0f	"2A"	$41/'A'	
ProfDOS Release	40	$0f $0f	"4A"	$41/'A'	
Dolphin-DOS 2.0/3.0	35	$0f $0f	"2A"	$41/'A'	
Dolphin-DOS 2.0/3.0	40	$0d $0f	"2A"	$41/'A'	
PrologicDOS 1541	35	$0f $0f	"2A"	$41/'A'	
PrologicDOS 1541	40	$0f $0f	"2P"	$50/'P'	
ProSpeed 1571 2.0	35	$0f $0f	"2A"	$41/'A'	
ProSpeed 1571 2.0	40	$0f $0f	"2P"	$50/'P'	

*Note: There are also clones of SpeedDOS that exist, such as RoloDOS and DigiDOS. Both are just a change of the DOS startup string.

The location of the extra BAM information in sector 18/0, for 40 track images, will be different depending on what standard the disks have been formatted with. SPEED DOS stores them from $C0 to $D3, DOLPHIN DOS stores them from $AC to $BF and PrologicDOS stored them right after the existing BAM entries from $90-A3. PrologicDOS also moves the disk label and ID forward from the standard location of $90 to $A4. 64COPY and Star Commander let you select from several different types of extended disk formats you want to create/work with.

All three of the speeder DOS's mentioned above don't alter the standard sector interleave of 10 for files and 3 for directories. The reason is that they use a memory cache installed in the drive which reads the entire track in one pass. This alleviates the need for custom interleave values. They do seem to alter the algorithm that finds the next available free sector so that the interleave value can deviate from 10 under certain circumstances, but I don't know why they would bother.

Below is a HEX dump of a Speed DOS BAM sector. Note the location of the extra BAM info from $C0-D3.

```
00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
------------------------------------------------
```

```
0070: 12 FF FF 03 12 FF FF 03 12 FF FF 03 11 FF FF 01
0080: 11 FF FF 01 11 FF FF 01 11 FF FF 01 11 FF FF 01
0090: A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0
00A0: A0 A0 30 30 A0 32 41 A0 A0 A0 A0 00 00 00 00 00
00B0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00C0: 11 FF FF 01 11 FF FF 01 11 FF FF 01 11 FF FF 01
00D0: 11 FF FF 01 00 00 00 00 00 00 00 00 00 00 00 00
```

Below is a HEX dump of a Dolphin DOS BAM sector. Note the location of the extra BAM info from $AC-BF.

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
      -----------------------------------------------
0070: 12 FF FF 03 12 FF FF 03 12 FF FF 03 11 FF FF 01
0080: 11 FF FF 01 11 FF FF 01 11 FF FF 01 11 FF FF 01
0090: A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0
00A0: A0 A0 30 30 A0 32 41 A0 A0 A0 A0 00 11 FF FF 01
00B0: 11 FF FF 01 11 FF FF 01 11 FF FF 01 11 FF FF 01
00C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00D0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Below is a HEX dump of a PrologicDOS BAM sector. Note that the disk name and ID are now located at $A4 instead of starting at $90.

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
      -----------------------------------------------
0070: 12 FF FF 03 12 FF FF 03 12 FF FF 03 11 FF FF 01
0080: 11 FF FF 01 11 FF FF 01 11 FF FF 01 11 FF FF 01
0090: 11 FF FF 01 11 FF FF 01 11 FF FF 01 11 FF FF 01
00A0: 11 FF FF 01 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0
00B0: A0 A0 A0 A0 A0 A0 30 30 A0 32 50 A0 A0 A0 A0 00
00C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00D0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

17.5.4 Error codes

Here is the meaning of the error bytes added onto the end of any extended D64. The CODE is the same as that generated by the 1541 drive controller... it reports these numbers, not the error code we usually see when an error occurs.

Some of what comes below is taken from Immers/Neufeld book "Inside Commodore DOS". Note the descriptions are not completely accurate as to what the drive DOS is actually doing to seek/read/decode/write sectors, but serve as simple examples only. The "type" field is where the error usually occurs, whether it's searching for any SYNC mark, any header ID, any valid header, or reading a sector.

Code	Error	Type	D64	Description
$01	00	N/A	*	No error.
$02	20	Seek	*	Header block not found / Header descriptor byte not found
$03	21	Seek	*	No SYNC sequence found
$04	22	Read	*	Data descriptor byte not found

$05	23	Read	*	Checksum error in data block
$06	24	Write		Write verify on format (never occurs on 1541)
$07	25	Write		Write verify error
$08	26	Write		Write protect on
$09	27	Seek	*	Checksum error in header block
$0A	28	Write		Write error (never occurs on 1541)
$0B	29	Seek	*	Disk sector ID mismatch
$0F	74	Read		Drive Not Ready (no disk in drive or no device 1)

Codes $0 and $C to $E are unused and never occur.

These first errors are "seek" errors, where the disk controller is simply reading headers and looking at descriptor bytes, checksums, format ID's and reporting what errors it sees. These errors do *not* necessarily apply to the exact sector being looked for. This fact makes duplication of these errors very unreliable.

Code : $03 Error : 21 Type : Seek Message : No SYNC sequence found.

Each sector data block and header block are preceeded by SYNC marks. If *no* sync sequence is found within 20 milliseconds (only ~1/10 of a disk rotation!) then this error is generated. This error used to mean the entire track is bad, but it does not have to be the case. Only a small area of the track needs to be without a SYNC mark and this error will be generated.

Converting this error to a D64 is very problematic because it depends on where the physical head is on the disk when a read attempt is made. If it is on valid header/sectors then it won't occur. If it happens over an area without SYNC marks, it will happen.

Code : $02 Error : 20 Type : Seek Message : Header descriptor byte not found (HEX $08, GCR $52)

Each sector is preceeded by an 8-byte GCR header block, which starts with the value $52 (GCR). If this value is not found after 90 attempts, this error is generated.

Basically, what a track has is SYNC marks, and possibly valid data blocks, but no valid header descriptors.

Code : $09 Error : 27 Type : Seek Message : Checksum error in header block

The header block contains a checksum value, calculated by XOR'ing the TRACK, SECTOR, ID1 and ID2 values. If this checksum is wrong, this error is generated.

Code : $0B Error : 29 Type : Seek Message : Disk sector ID mismatch

The ID's from the header block of the currently read sector are compared against the ones from the low-level header of 18/0. If there is a mismatch, this error is generated.

Code : $02 Error : 20 Type : Seek Message : Header block not found

This error can be reported again when searching for the correct header block. An image of the header is built and searched for, but not found after 90 read attempts. Note the difference from the first occurance. The first one only searches for a valid ID, not the whole header.

Note that error 20 occurs twice during this phase. The first time is when a header ID is being searched for, the second is when the proper header pattern for the sector being searched for is not found.

From this point on, all the errors apply to the specific sector you are looking for. If a read passed all the previous checks, then we are at the sector being searched for.

Note that the entire sector is read before these errors are detected. Therefore the data, checksum and off bytes are available.

Code : $04 Error : 22 Type : Read Message : Data descriptor byte not found (HEX $07, GCR $55)

Each sector data block is preceeded by the value $07, the "data block" descriptor. If this value is not there, this error is generated. Each encoded sector has actually 260 bytes. First is the descriptor byte, then follows the 256 bytes of data, a checksum, and two "off" bytes.

Code : $05 Error : 23 Type : Read Message : Checksum error in data block

The checksum of the data read of the disk is calculated, and compared against the one stored at the end of the sector. If there's a discrepancy, this error is generated.

Code : $0F Error : 74 Type : Read Message : Drive Not Ready (no disk in drive or no device 1)

These errors only apply when writing to a disk. I don't see the usefulness of having these as they cannot be present when only *reading* a disk.

Code : $06 Error : 24 Type : Write Message : Write verify (on format)

Code : $07 Error : 25 Type : Write Message : Write verify error

Once the GCR-encoded sector is written out, the drive waits for the sector to come around again and verifies the whole 325-byte GCR block. Any errors encountered will generate this error.

Code : $08 Error : 26 Type : Write Message : Write protect on

Self explanatory. Remove the write-protect tab, and try again.

Code : $0A Error : 28 Type : Write Message : Write error

In actual fact, this error never occurs, but it is included for completeness.

This is not an error at all, but it gets reported when the read of a sector is ok.

Code : $01 Error : 00 Type : N/A Message : No error.

Self explanatory. No errors were detected in the reading and decoding of the sector.

The advantage with using the 35 track D64 format, regardless of error bytes, is that it can be converted directly back to a 1541 disk by either using the proper cable and software on the PC, or send it down to the C64 and writing it back to a 1541. It is the best documented format since it is also native to the C64, with many books explaining the disk layout and the internals of the 1541.

17.6 The X64 disk image format

(This section was contributed by Peter Schepers and slightly edited by Marco van den Heuvel.)

This file type, created by Teemu Rantanen, was used on the X64 emulator (a UNIX-based emulator) which has been superceeded by VICE. Both VICE and X64 support the X64 file standard, with VICE also supporting the regular D64 and T64 files.

Note that this ancient format is deprecated and subject for removal. It never got any momentum in the emulation community, and VICE never supported it for anything but 1541 disks.

X64 is not a specific type of file, but rather encompasses *all* known C64 disk types (hard disk, floppies, etc). An X64 is created by prepending a 64-byte header to an existing image (1541, 1571, etc) and setting specific bytes which describe what type of image follows. This header has undergone some revision, and this description is based on the 1.02 version, which was the last known at the time of writing.

The most common X64 file you will see is the D64 variety, typically 174912 bytes long (174848 for the D64 and 64 bytes for the header, assuming no error bytes are appended). The header layout (as used in 64COPY) is as follows:

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
      -----------------------------------------------
0000: 43 15 41 64 01 02 01 23 00 00 00 00 00 00 00 00
0010: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0020: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0040: XX XX XX <- standard C64 image starts here....
```

Bytes	**Description**
$00-$03	This is the "Magic header" ($43 $15 $41 $64)
$04	Header version major ($01)
$05	Header version minor ($01, now its up to $02)
$06	Device type represented
$07	Maximum tracks in image (only in version 1.02 or greater) 1540/41/70: 35 1571: 35 1581: 80 (Logical single-sided disk)
$08	Number of disk sides in image. This value must be $00 for all 1541 and 1581 formats. $00=No second side $01=Second side
$09	Error data flag.
$0A-$1F	Unused, set to $00
$20-$3E	Disk image description (in ASCII or ISO Latin/1)
$3F	Always set to $00
$40-	Standard C64 file begins here.

The device types are:

Value	**Drive type**
$00	1540 See note below. . .
$01	1541 (Default)
$02	1542
$03	1551
$04	1570
$05	1571
$06	1572
$08	1581
$10	2031 or 4031
$11	2040 or 3040
$12	2041

$18	4040
$20	8050
$21	8060
$22	8061
$30	SFD-1001
$31	8250
$32	8280

The first four bytes used for the device type at position $06 ($00 to $03) are functionally the same, and are compatible with older version of X64 files. Some old X64 files might have $00 for the device type (instead of $01), but it makes no real difference.

As most instances of X64 files will be strictly 1541 images, bytes $08-$3F are set to zero, and some versions of the X64 emulator don't use bytes $08-$3F.

17.7 The D71 disk image format

(This section was contributed by Peter Schepers and slightly edited by Marco van den Heuvel.)

Similar to the D64 (1541), the 1571 drive can operate in either single-sided (1541 compatible) mode or double-sided (1571) mode. In this section I will be dealing with the double-sided mode only. For the breakdown of the single-sided mode, see the D64 section.

The D71 has 70 tracks, double that of the 1541, with a DOS file size of 349696 bytes. If the error byte block (1366 bytes) is attached, this makes the file size 351062 bytes. The track range and offsets into the D71 files are as follows:

Track	**Sec/trk**	**# Sectors**
1-17 (side 0)	21	357
18-24 (side 0)	19	133
25-30 (side 0)	18	108
31-35 (side 0)	17	85
36-52 (side 1)	21	357
53-59 (side 1)	19	133
60-65 (side 1)	18	108
66-70 (side 1)	17	85

Track	**#Sect**	**#SectorsIn**	**D71 Offset**
1	21	0	$00000
2	21	21	$01500
3	21	42	$02A00
4	21	63	$03F00
5	21	84	$05400
6	21	105	$06900
7	21	126	$07E00
8	21	147	$09300
9	21	168	$0A800
10	21	189	$0BD00
11	21	210	$0D200
12	21	231	$0E700
13	21	252	$0FC00

14	21	273	$11100
15	21	294	$12600
16	21	315	$13B00
17	21	336	$15000
18	19	357	$16500
19	19	376	$17800
20	19	395	$18B00
21	19	414	$19E00
22	19	433	$1B100
23	19	452	$1C400
24	19	471	$1D700
25	18	490	$1EA00
26	18	508	$1FC00
27	18	526	$20E00
28	18	544	$22000
29	18	562	$23200
30	18	580	$24400
31	17	598	$25600
32	17	615	$26700
33	17	632	$27800
34	17	649	$28900
35	17	666	$29A00
36	21	683	$2AB00
37	21	704	$2C000
38	21	725	$2D500
39	21	746	$2EA00
40	21	767	$2FF00
41	21	788	$31400
42	21	809	$32900
43	21	830	$33E00
44	21	851	$35300
45	21	872	$36800
46	21	893	$37D00
47	21	914	$39200
48	21	935	$3A700
49	21	956	$3BC00
50	21	977	$3D100
51	21	998	$3E600
52	21	1019	$3FB00
53	19	1040	$41000
54	19	1059	$42300
55	19	1078	$43600
56	19	1097	$44900
57	19	1116	$45C00
58	19	1135	$46F00
59	19	1154	$48200
60	18	1173	$49500

61	18	1191	$4A700
62	18	1209	$4B900
63	18	1227	$4CB00
64	18	1245	$4DD00
65	18	1263	$4EF00
66	17	1281	$50100
67	17	1298	$51200
68	17	1315	$52300
69	17	1332	$53400
70	17	1349	$54500

The directory structure is the same as a D64/1541. All the same filetypes apply, the directory still only holds 144 files per disk and should only exist on track 18.

The first two bytes of the sector ($12/$04 or 18/4) indicate the location of the next track/sector of the directory. If the track value is set to $00, then it is the last sector of the directory. It is possible, however unlikely, that the directory may *not* be competely on track 18 (some disks do exist like this). Just follow the chain anyhow.

When the directory is done, the track value will be $00. The sector link should contain a value of $FF, meaning the whole sector is allocated, but the actual value doesn't matter. The drive will return all the available entries anyways. This is a breakdown of a standard directory sector and entry:

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 12 04 82 11 00 4A 45 54 20 53 45 54 20 57 49 4C
10: 4C 59 A0 A0 A0 00 00 00 00 00 00 00 00 00 2B 00
20: 00 00 82 0F 01 4A 53 57 20 31 A0 A0 A0 A0 A0 A0
30: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 BF 00
40: 00 00 82 06 03 53 4F 4E 20 4F 46 20 42 4C 41 47
50: 47 45 52 A0 A0 00 00 00 00 00 00 00 00 00 AE 00
60: 00 00 82 15 0D 50 4F 54 54 59 20 50 49 47 45 4F
70: 4E A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 A2 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Bytes	**Description**
$00-$1F	First directory entry
$20-$3F	Second dir entry
$40-$5F	Third dir entry
$60-$7F	Fourth dir entry
$80-$9F	Fifth dir entry
$A0-$BF	Sixth dir entry
$C0-$DF	Seventh dir entry

$E0-$FF	Eighth dir entry

This is a breakdown of a standard directory entry:

Bytes	Description
$00-$01	Track/Sector location of next directory sector ($00/$FF if its the last sector)
$02	File type
$03-$04	Track/sector location of first sector of file
$05-$14	16 character filename (in PETASCII, padded with $A0)
$15-$16	Track/Sector location of first side-sector block (REL file only)
$17	REL file record length (REL file only, max. value 254)
$18-$1D	Unused (except with GEOS disks)
$1E-$1F	File size in sectors, low/high byte order ($1E+$1F*256). The approx. filesize in bytes is <= #sectors * 254

The file type field is used as follows:

Bits	Description
0-3	The actual file type
4	Unused
5	Used only during SAVE-@ replacement
6	Locked flag (Set produces ">" locked files)
7	Closed flag (Not set produces "*", or "splat" files)

The actual file type can be one of the following:

Binary	Decimal	File type
0000	0	DEL
0001	1	SEQ
0010	2	PRG
0011	3	USR
0100	4	REL

Values 5-15 are illegal, but if used will produce very strange results. The 1571 is inconsistent in how it treats these bits. Some routines use all 4 bits, others ignore bit 3, resulting in values from 0-7.

When the 1571 is in is native ("1571") mode, files are stored with a sector interleave of 6, rather than 10 which the 1541 (and the 1571 in "1541" mode) uses. The directory still uses an interleave of 3.

17.7.1 Non-Standard & Long Directories

Most Commodore floppy disk drives use a single dedicated directory track where all filenames are stored. This limits the number of files stored on a disk based on the number of sectors on the directory track. There are some disk images that contain more files than would normally be allowed. This requires extending the directory off the default directory track by changing the last directory sector pointer to a new track, allocating the new sectors in the BAM, and manually placing (or moving existing) file entries there. The directory of an extended disk can be read and the files that reside there can be loaded without problems on a real drive. However, this is still a very dangerous practice as writing to the extended portion of the directory will cause directory corruption in the non- extended part. Many

of the floppy drives core ROM routines ignore the track value that the directory is on and assume the default directory track for operations.

To explain: assume that the directory has been extended from track 18 to track 19/6 and that the directory is full except for a few slots on 19/6. When saving a new file, the drive DOS will find an empty file slot at 19/6 offset $40 and correctly write the filename and a few other things into this slot. When the file is done being saved the final file information will be written to 18/6 offset $40 instead of 19/6 causing some directory corruption to the entry at 18/6. Also, the BAM entries for the sectors occupied by the new file will not be saved and the new file will be left as a SPLAT (*) file.

Attempts to validate the disk will result in those files residing off the directory track to not be allocated in the BAM, and could also send the drive into an endless loop. The default directory track is assumed for all sector reads when validating so if the directory goes to 19/6, then the validate code will read 18/6 instead. If 18/6 is part of the normal directory chain then the validate routine will loop endlessly.

17.7.2 Bam layout

The BAM is somewhat different as it now has to take 35 new tracks into account. In order to do this, most of the extra BAM information is stored on track 53/0, and the remaining sectors on track 53 are marked in the BAM as allocated. This does mean that except for one allocated sector on track 53, the rest of the track is unused and wasted. (Track 53 is the equivalent to track 18, but on the flip side of the disk). Here is a dump of the first BAM sector. . .

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 12 01 41 80 12 FF F9 17 15 FF FF 1F 15 FF FF 1F
10: 15 FF FF 1F 15 FF FF 1F 15 FF FF 1F 15 FF FF 1F
20: 15 FF FF 1F 15 FF FF 1F 15 FF FF 1F 15 FF FF 1F
30: 15 FF FF 1F 15 FF FF 1F 15 FF FF 1F 15 FF FF 1F
40: 15 FF FF 1F 15 FF FF 1F 11 FC FF 07 13 FF FF 07
50: 13 FF FF 07 13 FF FF 07 13 FF FF 07 13 FF FF 07
60: 13 FF FF 07 12 FF FF 03 12 FF FF 03 12 FF FF 03
70: 12 FF FF 03 12 FF FF 03 12 FF FF 03 11 FF FF 01
80: 11 FF FF 01 11 FF FF 01 11 FF FF 01 11 FF FF 01
90: A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0 A0
A0: A0 A0 30 30 A0 32 41 A0 A0 A0 A0 00 00 00 00 00
B0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0: 00 00 00 00 00 00 00 00 00 00 00 00 00 15 15 15
E0: 15 15 15 15 15 15 15 15 15 15 15 15 15 15 00 13
F0: 13 13 13 13 13 12 12 12 12 12 12 11 11 11 11 11
```

Bytes	Description
$00-$01	Track/Sector location of the first directory sector (should be set to 18/1 but it doesn't matter, and don't trust what is there, always go to 18/1 for first directory entry)

$02	Disk DOS version type (see note below) $41 ('A') = 1541
$03	Double-sided flag $00 - Single sided disk $80 - Double sided disk
$04-8F	BAM entries for each track, in groups of four bytes per track, starting on track 1.
$90-$9F	Disk Name (padded with $A0)
$A0-$A1	Filled with $A0
$A2-$A3	Disk ID
$A4	Usually $A0
$A5-$A6	DOS type, usually "2A"
$A7-$AA	Filled with $A0
$AB-$DC	Not used ($00's)
$DD-$FF	Free sector count for tracks 36-70 (1 byte/track).

The "free sector" entries for tracks 36-70 are likely included here in the first BAM sector due to some memory restrictions in the 1571 drive. There is only enough memory available for one BAM sector, but in order to generate the "blocks free" value at the end of a directory listing, the drive needs to know the extra track "free sector" values. It does make working with the BAM a little more difficult, though.

These are the values that would normally be with the 4-byte BAM entry, but the rest of the entry is contained on 53/0.

Note: If the DOS version byte is set to anything other than $41 or $00, then we have what is called "soft write protection". Any attempt to write to the disk will return the "DOS Version" error code 73. The 1571 is simply telling you that it thinks the disk format version is incorrect.

The BAM entries require some explanation. Take the first entry at bytes $04-$07 ($12 $FF $F9 $17). The first byte ($12) is the number of free sectors on that track. Since we are looking at the track 1 entry, this means it has 18 (decimal) free sectors.

The next three bytes represent the bitmap of which sectors are used/free. Since it is 3 bytes (8 bits/byte) we have 24 bits of storage. Remember that at most, each track only has 21 sectors, so there are a few unused bits. These entries must be viewed in binary to make any sense. We will use the first entry (track 1) at bytes 04-07:

```
FF=11111111, F9=11111001, 17=00010111
```

In order to make any sense from the binary notation, flip the bits around.

```
            111111 11112222
01234567 89012345 67890123
-------------------------
11111111 10011111 11101000
^                    ^
sector 0           sector 20
```

Since we are on the first track, we have 21 sectors, and only use up to the bit 20 position. If a bit is on (1), the sector is free. Therefore, track 1 has sectors 9,10 and 19 used, all the rest are free.

In order to complete the BAM, we must check 53/0.

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: FF FF 1F FF FF 1F FF FF 1F FF FF 1F FF FF 1F FF
10: FF 1F FF FF 1F FF FF 1F FF FF 1F FF FF 1F FF FF
20: 1F FF FF 1F FF FF 1F FF FF 1F FF FF 1F FF FF 1F
30: FF FF 1F 00 00 00 FF FF 07 FF FF 07 FF FF 07 FF
40: FF 07 FF FF 07 FF FF 07 FF FF 03 FF FF 03 FF FF
50: 03 FF FF 03 FF FF 03 FF FF 03 FF FF 01 FF FF 01
60: FF FF 01 FF FF 01 FF FF 01 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Each track from 36-70 has 3 byte entries, starting at address $00.

```
Byte: $00-$02: $FF $FF $1F - BAM map for track 36
      $03-$05: $FF $FF $1F - BAM map for track 37
      ...
      $33-$35: $00 $00 $00 - BAM map for track 53
      ...
      $66-$68: $FF $FF $01 - BAM map for track 70
      $69-$FF:             - Not used
```

You can break down the entries for tracks 36-70 the same way as track 1, just combine the free sector bytes from 18/0 and the BAM usage from 53 to get the full 4-byte entry.

Just like a D64, you can attach error bytes to the file, for sector error information. This block is 1366 bytes long, 1 byte for each of the 1366 sectors in the image. With the error bytes, the file size is 351062 bytes.

17.8 The D81 disk image format

(This section was contributed by Peter Schepers and slightly edited by Marco van den Heuvel.)

Like D64 and D71, this is a byte for byte copy of a physical 1581 disk. It consists of 80 tracks, 40 sectors each (0 to 39) for a size of 819200 bytes, or 3200 sectors. If the error byte block is attached, this makes the file size 822400 bytes.

There are three sectors on the directory track used for disk internals (header and BAM), leaving 37 sectors for filename entries, thus allowing for 296 files (37 * 8) to be stored at the root level of the disk.

The actual physical layout on the disk is quite different from what the user sees, but this is unimportant to the scope of this section. One important difference from the D64 and D71

is all the sector interleaves are now 1 for both files and directory storage (rather than 3 for directory and 10 for file on a D64/D71). This is due to the built-in buffering in the 1581. When reading a sector, the whole track will be buffered in memory, and any sectors being modified will be done in memory. Once it has to be written, the whole track will be written out in one step.

The track range and offsets into the D81 files are as follows:

Track	**#Sect**	**#SectorsIn**	**D81 Offset**
1	40	0	$00000
2	40	40	$02800
3	40	80	$05000
4	40	120	$07800
5	40	160	$0A000
6	40	200	$0C800
7	40	240	$0F000
8	40	280	$11800
9	40	320	$14000
10	40	360	$16800
11	40	400	$19000
12	40	440	$1B800
13	40	480	$1E000
14	40	520	$20800
15	40	560	$23000
16	40	600	$25800
17	40	640	$28000
18	40	680	$2A800
19	40	720	$2D000
20	40	760	$2F800
21	40	800	$32000
22	40	840	$34800
23	40	880	$37000
24	40	920	$39800
25	40	960	$3C000
26	40	1000	$3E800
27	40	1040	$41000
28	40	1080	$43800
29	40	1120	$46000
30	40	1160	$48800
31	40	1200	$4B000
32	40	1240	$4D800
33	40	1280	$50000
34	40	1320	$52800
35	40	1360	$55000
36	40	1400	$57800
37	40	1440	$5A000
38	40	1480	$5C800
39	40	1520	$5F000

40	40	1560	$61800
41	40	1600	$64000
42	40	1640	$66800
43	40	1680	$69000
44	40	1720	$6B800
45	40	1760	$6E000
46	40	1800	$70800
47	40	1840	$73000
48	40	1880	$75800
49	40	1920	$78000
50	40	1960	$7A800
51	40	2000	$7D000
52	40	2040	$7F800
53	40	2080	$82000
54	40	2120	$84800
55	40	2160	$87000
56	40	2200	$89800
57	40	2240	$8C000
58	40	2280	$8E800
59	40	2320	$91000
60	40	2360	$93800
61	40	2400	$96000
62	40	2440	$98800
63	40	2480	$9B000
64	40	2520	$9D800
65	40	2560	$A0000
66	40	2600	$A2800
67	40	2640	$A5000
68	40	2680	$A7800
69	40	2720	$AA000
70	40	2760	$AC800
71	40	2800	$AF000
72	40	2840	$B1800
73	40	2880	$B4000
74	40	2920	$B6800
75	40	2960	$B9000
76	40	3000	$BB800
77	40	3040	$BE000
78	40	3080	$C0800
79	40	3120	$C3000
80	40	3160	$C5800

The header sector is stored at 40/0, and contains the disk name, ID and DOS version bytes, but the BAM is no longer contained here (like the D64).

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 28 03 44 00 31 35 38 31 20 55 54 49 4C 49 54 59
```

```
10: 20 56 30 31 A0 A0 47 42 A0 33 44 A0 A0 00 00 00
20: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
40: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Bytes	**Description**
$00-$01	Track/Sector location of the first directory sector (should be set to 40/3 but it doesn't matter, and don't trust what is there, always go to 40/3 for first directory entry)
$02	Disk DOS version type (see note below) $44 ('D')=1581
$03	$00
$04-$13	16 character Disk Name (padded with $A0)
$14-$15	$A0
$16-$17	Disk ID
$18	$A0
$19	DOS Version ("3")
$1A	Disk version ("D")
$1B-$1C	$A0
$1D-$FF	Unused (usually $00)

The following might be set if the disk is a GEOS format (this info is based on the D64 layout, and might not prove to be true)

Bytes	**Description**
$AB-$AC	Border sector (GEOS only, else set to $00)
$AD-$BC	GEOS ID string ("geos FORMAT V1.x" GEOS only, else $00)
$BD-$FF	Unused (usually $00)

Note: If the DOS version byte is changed to anything other than a $44 (or $00), then we have what is called "soft write protection". Any attempt to write to the disk will return the "DOS Version" error code 73. The drive is simply telling you that it thinks the disk format version is incompatible.

The directory track should be contained totally on track 40. Sectors 3-39 contain the entries and sector 1 and 2 contain the BAM (Block Availability Map). Sector 0 holds the disk name and ID. The first directory sector is always 40/3, even though the t/s pointer at 40/0 (first two bytes) might point somewhere else. It goes linearly up the sector count, 3-4-5-6-etc. Each sector holds up to eight entries.

The first two bytes of the sector ($28/$04) indicate the location of the next track/sector of the directory (40/4). If the track is set to $00, then it is the last sector of the directory. It

is possible, however unlikely, that the directory may *not* be competely on track 40. Just follow the chain anyhow.

When the directory is done (track=$00), the sector should contain an $FF, meaning the whole sector is allocated. Theactual value doesn't matter as all the entries will be returned anyways. Each directory sector has the following layout:

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 28 04 81 2B 00 53 43 52 45 45 4E 20 20 33 A0 A0
10: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 02 00
20: 00 00 81 2B 01 53 43 52 45 45 4E 20 20 34 A0 A0
30: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 03 00
40: 00 00 81 2B 02 53 43 52 45 45 4E 20 20 35 A0 A0
50: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 07 00
60: 00 00 81 2B 08 53 43 52 45 45 4E 20 20 36 A0 A0
70: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 08 00
80: 00 00 81 2B 14 53 43 52 45 45 4E 20 20 37 A0 A0
90: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 07 00
A0: 00 00 81 24 00 53 43 52 45 45 4E 20 20 38 A0 A0
B0: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 0B 00
C0: 00 00 82 24 04 46 49 4C 45 34 32 39 33 36 39 30
D0: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 07 00
E0: 00 00 82 24 06 46 49 4C 45 32 35 37 38 38 31 35
F0: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 05 00
```

Bytes	**Description**
$00-$1F	First directory entry
$20-$3F	Second dir entry
$40-$5F	Third dir entry
$60-$7F	Fourth dir entry
$80-$9F	Fifth dir entry
$A0-$BF	Sixth dir entry
$C0-$DF	Seventh dir entry
$E0-$FF	Eighth dir entry

This is a breakdown of a standard directory entry:

Bytes	**Description**
$00-$01	Track/Sector location of next directory sector
$02	File type
$03-$04	Track/sector location of first sector of file or partition
$05-$14	16 character filename (in PETASCII, padded with $A0)
$15-$16	Track/Sector location of first SUPER SIDE SECTOR block (REL file only)
$17	REL file record length (REL file only)
$18-$1B	Unused (except with GEOS disks)
$1C-$1D	(Used during an SAVE or OPEN, holds the new t/s link)
$1E-$1F	File or partition size in sectors, low/high byte order ($1E+$1F*256). The approx. file size in bytes is <= #sectors * 254

The file type field is used as follows:

Bits	Description
0-3	The actual file type
4	Unused
5	Used only during SAVE-@ replacement
6	Locked flag (Set produces ">" locked files)
7	Closed flag (Not set produces "*", or "splat" files)

The actual file type can be one of the following:

Binary	Decimal	File type
0000	0	DEL
0001	1	SEQ
0010	2	PRG
0011	3	USR
0100	4	REL
0101	5	CBM (partition or sub-directory)

Values 6-15 are illegal, but if used will produce very strange results.

17.8.1 Non-Standard & Long Directories

Most Commdore floppy disk drives use a single dedicated directory track where all filenames are stored. This limits the number of files stored on a disk based on the number of sectors on the directory track. There are some disk images that contain more files than would normally be allowed. This requires extending the directory off the default directory track by changing the last directory sector pointer to a new track, allocating the new sectors in the BAM, and manually placing (or moving existing) file entries there. The directory of an extended disk can be read and the files that reside there can be loaded without problems on a real drive. However, this is still a very dangerous practice as writing to the extended portion of the directory will cause directory corruption in the non-extended part. Many of the floppy drives core ROM routines ignore the track value that the directory is on and assume the default directory track for operations.

17.8.2 BAM layout

The BAM is located on 40/1 (for side 0, tracks 1-40) and 40/2 (for side 1, tracks 41-80). Each entry takes up six bytes, one for the "free sector" count and five for the allocation bitmap.

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 28 02 44 BB 47 42 C0 00 00 00 00 00 00 00 00 00
10: 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
20: FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF
30: FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF
40: 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
50: FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF
60: FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF
70: 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
80: FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF
90: FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF
A0: 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
```

```
B0: FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF
C0: FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF
D0: 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
E0: FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF
F0: FF FF FF FF 28 FF FF FF FF FF 24 F0 FF 2D FF FE
```

```
Bytes:
$00-$01: Track/sector of next bam sector (40/2)
    $02: Version # ('D')
    $03: One's complement of version# ($BB)
$04-$05: Disk ID bytes (same as 40/0 Disk ID)
    $06: I/O byte
         bit 7 set - Verify on
         bit 7 clear - Verify off
         bit 6 set - Check header CRC
         bit 6 clear - Don't check header CRC
    $07: Auto-boot-loader flag
$08-$0F: Reserved for future (set to $00)
$10-$15: BAM entry for track 1 (track 41, side 1)
$16-$1B: BAM entry for track 2 (track 42, side 1)
         ...
$46-$4B: BAM entry for track 10 (track 50, side 1)
         ...
$82-$87: BAM entry for track 20 (track 60, side 1)
         ...
$BE-$C3: BAM entry for track 30 (track 70, side 1)
         ...
$FA-$FF: BAM entry for track 40 (track 80, side 1)
```

The BAM entries require some explanation, so lets look at the track 40 entry at bytes $FA-FF ($24 $F0 $FF $2D $FF $FE). The first byte ($24, or 36 decimal) is the number of free sectors on that track. The next five bytes represent the bitmap of which sectors are used/free. Since it is five bytes (8 bits/byte) we have 40 bits of storage. Since this format has 40 sectors/track, the whole five bytes are used.

```
 F0: .. .. .. .. .. .. .. .. .. .. 24 F0 FF 2D FF FE
```

The last five bytes of any BAM entry must be viewed in binary to make any sense. We will once again use track 40 as our reference:

```
 F0=11110000, FF=11111111, 2D=00101101, FF=11111111, FE=11111110
```

In order to make any sense from the binary notation, flip the bits around.

```
                   111111 11112222 22222233 33333333
 Sector 01234567 89012345 67890123 45678901 23456789
        ------------------------- -------- --------
        00001111 11111111 10110100 11111111 01111111
```

Note that if a bit is on (1), the sector is free. Therefore, track 40 has sectors 0-3, 17, 20, 22, 23 and 32 used, all the rest are free.

The second BAM (for side 1) contains the entries for tracks 41-80.

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
```

```
    ------------------------------------------------
00: 00 FF 44 BB 47 42 C0 00 00 00 00 00 00 00 00 00
10: 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
20: FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF
30: FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF
40: 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
50: FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF
60: FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF
70: 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
80: FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF
90: FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF
A0: 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
B0: FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF
C0: FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF
D0: 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
E0: FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF 28 FF
F0: FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF FF FF
```

It is laid out exactly as the side 0 BAM except for one difference. The track/sector reference for the next sector should be set to $00/$FF, indicating there is no next sector.

17.8.3 REL files

The REL filetype requires some extra explaining. It was designed to make access to data *anywhere* on the disk very fast. Take a look at this directory entry. . .

```
 00: 00 FF 84 27 00 41 44 44 49 54 49 4F 4E 41 4C 20
 10: 49 4E 46 4F A0 27 02 FE 00 00 00 00 00 00 D2 0B
```

The third byte ($84) indicates this entry is a REL file and that the three normally empty entries at offset $15, $16 and $17 are now used as they are explained above. It's the track/sector chain that this entry points to, called the SUPER SIDE SECTOR, which is of interest here (in this case, 39/2). The SUPER SIDE SECTOR is very different from the D64 format. If you check the D64 entry for a REL file and do the calculations, you will find that the maximum file size of the REL file is 720 data sectors. With the new SUPER SIDE SECTOR, you can now have 126 groups of these SIDE SECTORS chains, allowing for file sizes up to (theoretically) 90720 sectors, or about 22.15 Megabytes.

Here is a dump of the beginning of the SUPER SIDE SECTOR. . .

```
 00: 27 01 FE 27 01 15 09 03 0F 38 16 4A 1C 00 00 00
 10: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
 20: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

```
Bytes:
$00-$01: Track/sector of first side sector in group 0
    $02: Always $FE
$03-$04: Track/sector of first side sector in group 0 (again)
         ...
$FD-$FE: Track/sector of first side sector in group 125
    $FF: Unused (likely $00)
```

The side sector layout is the same as the D64/1571.

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 12 0A 00 FE 15 09 12 0A 0F 0B 0C 0C 09 0D 06 0E
10: 15 07 15 08 15 0A 15 0B 15 0C 15 0D 15 0E 15 0F
20: 15 10 15 11 15 12 15 13 15 14 15 15 15 16 15 17
30: 15 18 15 19 15 1A 15 1B 15 1C 15 1D 15 1E 15 1F
40: 15 20 15 21 15 22 15 23 15 24 15 25 15 26 15 27
50: 14 00 14 01 14 02 14 03 14 04 14 05 14 06 14 07
60: 14 08 14 09 14 0A 14 0B 14 0C 14 0D 14 0E 14 0F
70: 14 10 14 11 14 12 14 13 14 14 14 15 14 16 14 17
80: 14 18 14 19 14 1A 14 1B 14 1C 14 1D 14 1E 14 1F
90: 14 20 14 21 14 22 14 23 14 24 14 25 14 26 14 27
A0: 13 00 13 01 13 02 13 03 13 04 13 05 13 06 13 07
B0: 13 08 13 09 13 0A 13 0B 13 0C 13 0D 13 0E 13 0F
C0: 13 10 13 11 13 12 13 13 13 14 13 15 13 16 13 17
D0: 13 18 13 19 13 1A 13 1B 13 1C 13 1D 13 1E 13 1F
E0: 13 20 13 21 13 22 13 23 13 24 13 25 13 26 13 27
F0: 12 00 12 01 12 02 12 03 12 04 12 05 12 06 12 07

Bytes:
    $00: Track location of next side-sector ($00 if last sector)
    $01: Sector location of next side-sector
    $02: Side-sector block number (first sector is $00, the next is
         $01, then $02, etc)
    $03: REL file RECORD size (from directory entry)
$04-$0F: Track/sector locations of the six other side-sectors. Note
         the first entry is this very sector we have listed here.
         The next is the next t/s listed at the beginning of the
         sector. All of this information must be correct. If one of
         these chains is $00/$00, then we have no more side sectors.
         Also, all of these (up to six) side sectors must have the
         same values in this range.
$10-$FF: T/S chains of *each* sector of the data portion. When we
         get a $00/$00, we are at the end of the file.
```

17.8.4 1581 Partitions and Sub-directories

At the beginning of this section it was stated that the 1581 can hold 296 entries "at the root level". The 1581 also has the ability to partition areas of the disk. Under the right conditions these can become sub-directories, acting as a small diskette, complete with its own directory and BAM. When you are inside of a sub-directory, no other files except those in that directory are visible, or can be affected.

To the 1581, this file will show up as a "CBM" filetype in a directory. All this does is tell the disk that a file, starting at X/Y track/sector and Z sectors large exists. Doing a validate will not harm these files as they have a directory entry, and are fully allocated in the BAM.

There are two main uses for partitions. One is to simply allocate a section of the disk to be used for direct-access reads/writes, and lock it away from being overwritten after a VALIDATE. The second is as a sub-directory, basically a small "disk within a disk".

In order to use a partition as a sub-directory, it must adhere to the following four rules:

1. If must start on sector 0
2. It's size must be in multiples of 40 sectors
3. It must be a minimum of 120 sectors long (3 tracks)
4. If must not start on or cross track 40, which limits the biggest directory to 1600 sectors (tracks 1-39).

This is a dump of a sub-directory entry:

```
00: 00 FF 85 29 00 50 41 52 54 49 54 49 4F 4E 20 31
10: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 40 06
```

It is a partition starting on track 41/0, extends for 1600 sectors, and has been formatted as a sub-directory. Note that when a partition is created, the area being allocated is not touched in any way. If you want it set up as a sub-directory, you must issue the FORMAT command to the 1581 to create the central directory and BAM. Also note that from the directory entry you can't tell whether it is a sub-directory or not, just that it fits the sub-directory parameters.

The BAM track for the sub-directory exists on the first track of the partition, and has the same layout as the disk BAM on track 40. The biggest difference is the "disk name" is what what given when the partition was formatted rather than what the actual disk name is. Also, except for the free sectors in the partition area, all other sectors in the BAM will be allocated.

If the partition size doesn't match the above rules for a sub-directory, it will simply exist as a "protected" area of the disk, and can't be used as a sub-directory. Either way, it still shows up as a "CBM" type in a directory listing. Below is a dump of a 10-sector partition starting on track 5/1, which does not qualify as a sub-directory. . .

```
00: 00 00 85 05 01 53 4D 41 4C 4C 50 41 52 54 20 32
10: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 0A 00
```

The master BAM shows the entry for this partition on track 5. . .

```
00: 28 02 44 BB 43 44 C0 00 00 00 00 00 00 00 00 00
10: 23 C1 FF FF FF FF 28 FF FF FF FF FF 28 FF FF FF
20: FF FF 28 FF FF FF FF FF 1E 01 F8 FF FF FF 28 FF
                           ^^^^^^^^^^^^^^^^^
```

The breakdown of the BAM shows the allocation for this track, with sectors 1-10 allocated, as it should be.

```
10000000 00011111 11111111 11111111 11111111
^        ^        ^        ^        ^
0        10       20       30       39
```

Partitions and sub-directories share one very important trait. When created, the sub-directory entry simply has the starting track/sector and the size of the partition in sectors. Partitions are created linearly, meaning if one starts on 30/1 and is of size 15 sectors, then the sector range from 1 through 15 on track 30 will be allocated. If a partition size crosses a track boundary, the allocation will continue on the next track starting on sector 0, and going up.

The section allocated will *not* have a track/sector chain like a file would, but rather is dependant on the directory entry to keep it from being overwritten. You can store whatever you want to in the allocated area.

17.8.5 AUTO-BOOT LOADER

If byte $07 in the BAM is set, then when the drive is reset (and other circumstances) it will look for a USR file called "COPYRIGHT CBM 86". This file will then be loaded into the drive RAM and executed.

The format for this auto-loader file is fairly basic. It starts with a two-byte load address, a size byte, program data, and a checksum at the end.

```
Bytes:
       $00-$01: Load address, low/high format
           $02: Size of program (SZ) (smaller than 256 bytes)
$03-($03+SZ-1): Program data
        $03+SZ: Checksum byte
```

17.9 The D80 disk image format

(This section was contributed by Peter Schepers and slightly edited by Marco van den Heuvel.)

This is a sector-for-sector copy of an 8050 floppy disk. The file size for an 8050 image is 533248 bytes. It is comprised of 256-byte sectors arranged across 77 tracks, with a varying number of sectors per track for a total of 2083 sectors. Track counting starts at 1 (not 0) and sector counting starts at 0 (not 1), therefore a track with 29 sectors will go from 0 to 28.

The original media (a 5.25" disk) has the tracks laid out in circles, with track 1 on the very outside of the disk (closest to the sides) to track 77 being on the inside of the disk (closest to the inner hub ring). Commodore, in their infinite wisdom, varied the number of sectors per track and data densities across the disk to optimize available storage, resulting in the chart below. It shows the sectors/track for a D80. Since the outside diameter of a circle is the largest (versus closer to the center), the outside tracks have the largest amount of storage.

Track Range	**Sectors/track**	**# Sectors**
1-39	29	1131
40-53	27	378
54-64	25	275
65-77	23	299

Track	**#Sect**	**#SectorsIn**	**D8x Offset**
1	29	0	$00000
2	29	29	$01D00
3	29	58	$03A00
4	29	87	$05700
5	29	116	$07400
6	29	145	$09100
7	29	174	$0AE00
8	29	203	$0CB00
9	29	232	$0E800
10	29	261	$10500
11	29	290	$12200

12	29	319	$13F00
13	29	348	$15C00
14	29	377	$17900
15	29	406	$19600
16	29	435	$1B300
17	29	464	$1D000
18	29	493	$1ED00
19	29	522	$20A00
20	29	551	$22700
21	29	580	$24400
22	29	609	$26100
23	29	638	$27E00
24	29	667	$29B00
25	29	696	$2B800
26	29	725	$2D500
27	29	754	$2F200
28	29	783	$30F00
29	29	812	$32C00
30	29	841	$34900
31	29	870	$36600
32	29	899	$38300
33	29	928	$3A000
34	29	957	$3BD00
35	29	986	$3DA00
36	29	1015	$3F700
37	29	1044	$41400
38	29	1073	$43100
39	29	1102	$44E00
40	27	1131	$46B00
41	27	1158	$48600
42	27	1185	$4A100
43	27	1212	$4BC00
44	27	1239	$4D700
45	27	1266	$4F200
46	27	1293	$50D00
47	27	1320	$52800
48	27	1347	$54300
49	27	1374	$55E00
50	27	1401	$57900
51	27	1428	$59400
52	27	1455	$5AF00
53	27	1482	$5CA00
54	25	1509	$5E500
55	25	1534	$5FE00
56	25	1559	$61700
57	25	1584	$63000
58	25	1609	$64900

59	25	1634	$66200
60	25	1659	$67B00
61	25	1684	$69400
62	25	1709	$6AD00
63	25	1734	$6C600
64	25	1759	$6DF00
65	23	1784	$6F800
66	23	1807	$70F00
67	23	1830	$72600
68	23	1853	$73D00
69	23	1876	$75400
70	23	1899	$76B00
71	23	1922	$78200
72	23	1945	$79900
73	23	1968	$7B000
74	23	1991	$7C700
75	23	2014	$7DE00
76	23	2037	$7F500
77	23	2060	$80C00

The BAM (Block Availability Map) is on track 38. The D80 is only 77 tracks and so the BAM is contained on 38/0 and 38/3. The BAM interleave is 3.

The directory is on track 39, with 39/0 contains the header (DOS type, disk name, disk ID's) and sectors 1-28 contain the directory entries. Both files and the directory use an interleave of 1. Since the directory is only 28 sectors large (29 less one for the header), and each sector can contain only 8 entries (32 bytes per entry), the maximum number of directory entries is 28 * 8 = 224. The first directory sector is always 39/1. It then follows a chain structure using a sector interleave of 1 making the links go 39/1, 39/2, 39/3 etc.

When reading a disk, you start with 39/0 (disk label/ID) which points to 38/0 (BAM0), 38/3 (BAM1), and finally to 39/1 (first dir entry sector). When writing a file to a blank disk, it will start at 38/1 because 38/0 is already allocated.

Below is a dump of the header sector 39/0:

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 26 00 43 00 00 00 73 61 6D 70 6C 65 20 64 38 30
10: A0 A0 A0 A0 A0 A0 A0 A0 65 72 A0 32 43 A0 A0 A0
20: A0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
...
F0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Bytes	**Description**
$00-$01	T/S pointer to first BAM sector (38/0)
$02	$43 'C' is for DOS format version
$03	Reserved
$04-$05	Unused
$06-$16	Disk name, padded with 0xA0 ("sample d80")
$17	0xA0

$18-$19	Disk ID bytes "er"
$1A	0xA0
$1B-$1C	DOS version bytes "2C"
$1D-$20	0xA0
$21-$FF	Unused

Below is a dump of the first directory sector, 39/1

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 27 02 82 26 01 54 45 53 54 A0 A0 A0 A0 A0 A0 A0
10: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 01 00
20: 00 00 82 26 02 54 45 53 54 32 A0 A0 A0 A0 A0 A0
30: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 01 00
40: 00 00 82 26 04 54 45 53 54 33 A0 A0 A0 A0 A0 A0
50: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 05 00
60: 00 00 82 26 0B 54 45 53 54 34 A0 A0 A0 A0 A0 A0
70: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 09 00
80: 00 00 82 26 14 54 45 53 54 35 A0 A0 A0 A0 A0 A0
90: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 0C 00
A0: 00 00 82 28 00 54 45 53 54 36 A0 A0 A0 A0 A0 A0
B0: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 01 00
C0: 00 00 82 28 01 54 45 53 54 37 A0 A0 A0 A0 A0 A0
D0: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 01 00
E0: 00 00 82 28 02 54 45 53 54 38 A0 A0 A0 A0 A0 A0
F0: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 01 00
```

The first two bytes of the directory sector ($27/$02) indicate the location of the next track/sector of the directory (39/2). If the track is set to $00, then it is the last sector of the directory.

When the directory is done, the track value will be $00. The sector link should contain a value of $FF, meaning the whole sector is allocated, but the actual value doesn't matter. The drive will return all the available entries anyways. This is a breakdown of a standard directory sector:

Bytes	**Description**
$00-$1F	First directory entry
$20-$3F	Second dir entry
$40-$5F	Third dir entry
$60-$7F	Fourth dir entry
$80-$9F	Fifth dir entry
$A0-$BF	Sixth dir entry
$C0-$DF	Seventh dir entry
$E0-$FF	Eighth dir entry

This is a breakdown of a standard directory entry:

Bytes	**Description**
$00-$01	Track/Sector location of next directory sector ($00 $00 if not the first entry in the sector)
$02	File type

$03-$04	Track/sector location of first sector of file
$05-$14	16 character filename (in PETASCII, padded with $A0)
$15-$16	Track/Sector location of first side-sector block (REL file only)
$17	REL file record length (REL file only, max. value 254)
$18-$1D	Unused
$1E-$1F	File size in sectors, low/high byte order ($1E+$1F*256). The approx. filesize in bytes is <= #sectors * 254

The file type field is used as follows:

Bits	Description
0-3	The actual file type
4	Unused
5	Used only during SAVE-@ replacement
6	Locked flag (Set produces ">" locked files)
7	Closed flag (Not set produces "*", or "splat" files)

The actual file type can be one of the following:

Binary	Decimal	File type
0000	0	DEL
0001	1	SEQ
0010	2	PRG
0011	3	USR
0100	4	REL

Values 5-15 are illegal, but if used will produce very strange results.

17.9.1 Non-Standard & Long Directories

Most Commdore floppy disk drives use a single dedicated directory track where all filenames are stored. This limits the number of files stored on a disk based on the number of sectors on the directory track. There are some disk images that contain more files than would normally be allowed. This requires extending the directory off the default directory track by changing the last directory sector pointer to a new track, allocating the new sectors in the BAM, and manually placing (or moving existing) file entries there. The directory of an extended disk can be read and the files that reside there can be loaded without problems on a real drive. However, this is still a very dangerous practice as writing to the extended portion of the directory will cause directory corruption in the non-extended part. Many of the floppy drives core ROM routines ignore the track value that the directory is on and assume the default directory track for operations.

17.9.2 BAM layout

The BAM only occupies up to four sectors on track 38, so the rest of the track is empty and is available for file storage. Below is a dump of the first BAM block, 38/0. A D80 will only contain two BAM sectors, 38/0 and 38/3. Each entry takes 5 bytes, 1 for the free count on that track, and 4 for the BAM bits.

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 26 03 43 00 01 33 1D FF FF FF 1F 1D FF FF FF 1F
10: 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D
```

```
20: FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF
30: FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF
40: FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF
50: 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F
60: 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D
70: FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF
80: FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF
90: FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF
A0: 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F
B0: 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1B
C0: F6 FF FF 1F 1B FC FF FF 1F 1B FF FF FF 07 1B FF
D0: FF FF 07 1B FF FF FF 07 1B FF FF FF 07 1B FF FF
E0: FF 07 1B FF FF FF 07 1B FF FF FF 07 1B FF FF FF
F0: 07 1B FF FF FF 07 1B FF FF FF 07 1B FF FF FF 07
```

Bytes	**Description**
$00-$01	T/S pointer to second BAM sector (38/3)
$02	DOS version byte (0x43='C')
$03	Reserved
$04	Lowest track covered by this BAM (0x01=1)
$05	Highest+1 track covered by this BAM (0x33=51)
$06-$0A	BAM for track 1. The first byte shows the "blocks free" for this track, the remaining 4 show the BAM for the track.
$0B-$0F	BAM for track 2
...	...
$FB-$FF	BAM for track 50

Being bit-based, the BAM entries need some explanation. The first track entry in the above BAM sector is at offset 06, "1D FF FF FF 1F". The first number is how many blocks are free on this track ($1D=29) and the remainder is the bit representation of the usage map for the track. These entries must be viewed in binary to make any sense. First convert the values to binary:

```
FF=11111111, FF=11111111, FF=11111111, 1F=00011111
```

In order to make any sense from the binary notation, flip the bits around.

```
           111111 11112222 222222
01234567 89012345 67890123 456789...
------------------------- ---------
11111111 11111111 11111111 11111000
^                             ^
sector 0                    sector 28
```

Since we are on the first track, we have 29 sectors, and only use up to the bit 28 position. If a bit is on (1), the sector is free. Therefore, track 1 is clean, all sectors are free. Any leftover bits that refer to sectors that don't exist, like bits 29-31 in the above example, are set to allocated.

Second BAM block 38/3.

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
```

```
00: 27 01 43 00 33 4E 1B FF FF FF 07 1B FF FF FF 07
10: 1B FF FF FF 07 19 FF FF FF 01 19 FF FF FF 01 19
20: FF FF FF 01 19 FF FF FF 01 19 FF FF FF 01 19 FF
30: FF FF 01 19 FF FF FF 01 19 FF FF FF 01 19 FF FF
40: FF 01 19 FF FF FF 01 19 FF FF FF 01 17 FF FF 7F
50: 00 17 FF FF 7F 00 17 FF FF 7F 00 17 FF FF 7F 00
60: 17 FF FF 7F 00 17 FF FF 7F 00 17 FF FF 7F 00 17
70: FF FF 7F 00 17 FF FF 7F 00 17 FF FF 7F 00 17 FF
80: FF 7F 00 17 FF FF 7F 00 17 FF FF 7F 00 00 00 00
90: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Bytes	**Description**
$00-$01	T/S pointer to second BAM sector (39/1)
$02	DOS version byte (0x43='C')
$03	Reserved
$04	Lowest track covered by this BAM (0x33=51)
$05	Highest+1 track covered by this BAM (0x43=78)
$06-$0A	BAM for track 51. The first byte shows the "blocks free" for this track, the remaining 4 show the BAM for the track.
$0B-$0F	BAM for track 52
...	...
$88-$8C	BAM for track 77
$8D-$FF	Not used

17.10 The D82 disk image format

(This section was contributed by Peter Schepers and slightly edited by Marco van den Heuvel.)

This is a sector-for-sector copy of an 8250 floppy disk. The file size for an 8250 image is 1066496 bytes. It is comprised of 256-byte sectors arranged across 154 tracks, with a varying number of sectors per track for a total of 4166 sectors. Track counting starts at 1 (not 0) and sector counting starts at 0 (not 1), therefore a track with 29 sectors will go from 0 to 28.

The original media (a 5.25" disk) has the tracks laid out in circles, with track 1 on the very outside of the disk (closest to the sides) to track 77 being on the inside of the disk (closest to the inner hub ring). Commodore, in their infinite wisdom, varied the number of sectors per track and data densities across the disk to optimize available storage, resulting in the chart below. It shows the sectors/track for a D82. Since the outside diameter of a circle is the largest (versus closer to the center), the outside tracks have the largest amount of storage.

Track Range	**Sectors/track**	**# Sectors**

1-39	29	1131
40-53	27	378
55-64	25	275
65-77	23	299
78-116	29	1131
117-130	27	378
131-141	25	275
142-154	23	299

Track	**#Sect**	**#SectorsIn**	**D82 Offset**
1	29	0	$000000
2	29	29	$001D00
3	29	58	$003A00
4	29	87	$005700
5	29	116	$007400
6	29	145	$009100
7	29	174	$00AE00
8	29	203	$00CB00
9	29	232	$00E800
10	29	261	$010500
11	29	290	$012200
12	29	319	$013F00
13	29	348	$015C00
14	29	377	$017900
15	29	406	$019600
16	29	435	$01B300
17	29	464	$01D000
18	29	493	$01ED00
19	29	522	$020A00
20	29	551	$022700
21	29	580	$024400
22	29	609	$026100
23	29	638	$027E00
24	29	667	$029B00
25	29	696	$02B800
26	29	725	$02D500
27	29	754	$02F200
28	29	783	$030F00
29	29	812	$032C00
30	29	841	$034900
31	29	870	$036600
32	29	899	$038300
33	29	928	$03A000
34	29	957	$03BD00
35	29	986	$03DA00
36	29	1015	$03F700
37	29	1044	$041400

38	29	1073	$043100
39	29	1102	$044E00
40	27	1131	$046B00
41	27	1158	$048600
42	27	1185	$04A100
43	27	1212	$04BC00
44	27	1239	$04D700
45	27	1266	$04F200
46	27	1293	$050D00
47	27	1320	$052800
48	27	1347	$054300
49	27	1374	$055E00
50	27	1401	$057900
51	27	1428	$059400
52	27	1455	$05AF00
53	27	1482	$05CA00
54	25	1509	$05E500
55	25	1534	$05FE00
56	25	1559	$061700
57	25	1584	$063000
58	25	1609	$064900
59	25	1634	$066200
60	25	1659	$067B00
61	25	1684	$069400
62	25	1709	$06AD00
63	25	1734	$06C600
64	25	1759	$06DF00
65	23	1784	$06F800
66	23	1807	$070F00
67	23	1830	$072600
68	23	1853	$073D00
69	23	1876	$075400
70	23	1899	$076B00
71	23	1922	$078200
72	23	1945	$079900
73	23	1968	$07B000
74	23	1991	$07C700
75	23	2014	$07DE00
76	23	2037	$07F500
77	23	2060	$080C00
78	29	2083	$082300
79	29	2112	$084000
80	29	2141	$085D00
81	29	2170	$087A00
82	29	2199	$089700
83	29	2228	$08B400
84	29	2257	$08D100

85	29	2286	$08EE00
86	29	2315	$090600
87	29	2344	$092800
88	29	2373	$094500
89	29	2402	$096200
90	29	2431	$097F00
91	29	2460	$099C00
92	29	2489	$09B900
93	29	2518	$09D600
94	29	2547	$09F300
95	29	2576	$0A1000
96	29	2605	$0A2D00
97	29	2634	$0A4A00
98	29	2663	$0A6700
99	29	2692	$0A8400
100	29	2721	$0AA100
101	29	2750	$0ABE00
102	29	2779	$0ADB00
103	29	2808	$0AF800
104	29	2837	$0B1500
105	29	2866	$0B3200
106	29	2895	$0B4F00
107	29	2924	$0B6C00
108	29	2953	$0B8900
109	29	2982	$0BA600
110	29	3011	$0BC300
111	29	3040	$0BE000
112	29	3069	$0BFD00
113	29	3098	$0C1A00
114	29	3137	$0C3700
115	29	3156	$0C5400
116	29	3185	$0C7100
117	27	3214	$0C8E00
118	27	3241	$0CA900
119	27	3268	$0CC400
120	27	3295	$0CDF00
121	27	3322	$0CFA00
122	27	3349	$0D1500
123	27	3376	$0D3000
124	27	3403	$0D4B00
125	27	3430	$0D6600
126	27	3457	$0D8100
127	27	3484	$0D9C00
128	27	3511	$0DB700
129	27	3538	$0DD200
130	27	3565	$0DED00
131	25	3592	$0E0800

132	25	3617	$0E2100
133	25	3642	$0E3A00
134	25	3667	$0E5300
135	25	3692	$0E6C00
136	25	3717	$0E8500
137	25	3742	$0E9E00
138	25	3767	$0EB700
139	25	3792	$0ED000
140	25	3817	$0EE900
141	25	3842	$0F0200
142	23	3867	$0F1B00
143	23	3890	$0F3200
144	23	3913	$0F4900
145	23	3936	$0F6000
146	23	3959	$0F7700
147	23	3982	$0F8E00
148	23	4005	$0FA500
149	23	4028	$0FBC00
150	23	4051	$0FD300
151	23	4074	$0FEA00
152	23	4097	$100100
153	23	4120	$101800
154	23	4143	$102F00

The BAM (Block Availability Map) is on track 38. The D82 is 154 tracks and so the BAM is contained on 38/0, 38/3, 38/6 and 38/9. The BAM interleave is 3.

The directory is on track 39, with 39/0 contains the header (DOS type, disk name, disk ID's) and sectors 1-28 contain the directory entries. Both files and the directory use an interleave of 1. Since the directory is only 28 sectors large (29 less one for the header), and each sector can contain only 8 entries (32 bytes per entry), the maximum number of directory entries is 28 * 8 = 224. The first directory sector is always 39/1. It then follows a chain structure using a sector interleave of 1 making the links go 39/1, 39/2, 39/3 etc.

When reading a disk, you start with 39/0 (disk label/ID) which points to 38/0 (BAM0), 38/3 (BAM1), 38/6 (BAM2), 38/9 (BAM3, and finally to 39/1 (first dir entry sector). When writing a file to a blank disk, it will start at 38/1 because 38/0 is already allocated.

Below is a dump of the header sector 39/0:

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 26 00 43 00 00 00 73 61 6D 70 6C 65 20 64 38 30
10: A0 A0 A0 A0 A0 A0 A0 A0 65 72 A0 32 43 A0 A0 A0
20: A0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
...
F0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Bytes	**Description**
$00-$01	T/S pointer to first BAM sector (38/0)
$02	$43 'C' is for DOS format version

$03	Reserved
$04-$05	Unused
$06-$16	Disk name, padded with 0xA0 ("sample d82")
$17	0xA0
$18-$19	Disk ID bytes "er"
$1A	0xA0
$1B-$1C	DOS version bytes "2C"
$1D-$20	0xA0
$21-$FF	Unused

Below is a dump of the first directory sector, 39/1

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 27 02 82 26 01 54 45 53 54 A0 A0 A0 A0 A0 A0 A0
10: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 01 00
20: 00 00 82 26 02 54 45 53 54 32 A0 A0 A0 A0 A0 A0
30: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 01 00
40: 00 00 82 26 04 54 45 53 54 33 A0 A0 A0 A0 A0 A0
50: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 05 00
60: 00 00 82 26 0B 54 45 53 54 34 A0 A0 A0 A0 A0 A0
70: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 09 00
80: 00 00 82 26 14 54 45 53 54 35 A0 A0 A0 A0 A0 A0
90: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 0C 00
A0: 00 00 82 28 00 54 45 53 54 36 A0 A0 A0 A0 A0 A0
B0: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 01 00
C0: 00 00 82 28 01 54 45 53 54 37 A0 A0 A0 A0 A0 A0
D0: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 01 00
E0: 00 00 82 28 02 54 45 53 54 38 A0 A0 A0 A0 A0 A0
F0: A0 A0 A0 A0 A0 00 00 00 00 00 00 00 00 00 01 00
```

The first two bytes of the directory sector ($27/$02) indicate the location of the next track/sector of the directory (39/2). If the track is set to $00, then it is the last sector of the directory.

When the directory is done, the track value will be $00. The sector link should contain a value of $FF, meaning the whole sector is allocated, but the actual value doesn't matter. The drive will return all the available entries anyways. This is a breakdown of a standard directory sector:

Bytes	**Description**
$00-$1F	First directory entry
$20-$3F	Second dir entry
$40-$5F	Third dir entry
$60-$7F	Fourth dir entry
$80-$9F	Fifth dir entry
$A0-$BF	Sixth dir entry
$C0-$DF	Seventh dir entry
$E0-$FF	Eighth dir entry

This is a breakdown of a standard directory entry:

Bytes	Description
$00-$01	Track/Sector location of next directory sector ($00 $00 if not the first entry in the sector)
$02	File type
$03-$04	Track/sector location of first sector of file
$05-$14	16 character filename (in PETASCII, padded with $A0)
$15-$16	Track/Sector location of first side-sector block (REL file only)
$17	REL file record length (REL file only, max. value 254)
$18-$1D	Unused
$1E-$1F	File size in sectors, low/high byte order ($1E+$1F*256). The approx. filesize in bytes is <= #sectors * 254

The file type field is used as follows:

Bits	Description
0-3	The actual file type
4	Unused
5	Used only during SAVE-@ replacement
6	Locked flag (Set produces ">" locked files)
7	Closed flag (Not set produces "*", or "splat" files)

The actual file type can be one of the following:

Binary	Decimal	File type
0000	0	DEL
0001	1	SEQ
0010	2	PRG
0011	3	USR
0100	4	REL

Values 5-15 are illegal, but if used will produce very strange results.

17.10.1 Non-Standard & Long Directories

Most Commdore floppy disk drives use a single dedicated directory track where all filenames are stored. This limits the number of files stored on a disk based on the number of sectors on the directory track. There are some disk images that contain more files than would normally be allowed. This requires extending the directory off the default directory track by changing the last directory sector pointer to a new track, allocating the new sectors in the BAM, and manually placing (or moving existing) file entries there. The directory of an extended disk can be read and the files that reside there can be loaded without problems on a real drive. However, this is still a very dangerous practice as writing to the extended portion of the directory will cause directory corruption in the non-extended part. Many of the floppy drives core ROM routines ignore the track value that the directory is on and assume the default directory track for operations.

17.10.2 BAM layout

The BAM only occupies up to four sectors on track 38, so the rest of the track is empty and is available for file storage. Below is a dump of the first BAM block, 38/0. A D82 will contain four BAM sectors, 38/0, 38/3, 38/6 and 38/9. Each entry takes 5 bytes, 1 for the free count on that track, and 4 for the BAM bits.

```
00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
```

```
    -------------------------------------------
00: 26 03 43 00 01 33 1D FF FF FF 1F 1D FF FF FF 1F
10: 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D
20: FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF
30: FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF
40: FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF
50: 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F
60: 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D
70: FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF
80: FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF
90: FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF
A0: 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F
B0: 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1B
C0: F6 FF FF 1F 1B FC FF FF 1F 1B FF FF FF 07 1B FF
D0: FF FF 07 1B FF FF FF 07 1B FF FF FF 07 1B FF FF
E0: FF 07 1B FF FF FF 07 1B FF FF FF 07 1B FF FF FF
F0: 07 1B FF FF FF 07 1B FF FF FF 07 1B FF FF FF 07
```

Bytes	**Description**
$00-$01	T/S pointer to second BAM sector (38/3)
$02	DOS version byte (0x43='C')
$03	Reserved
$04	Lowest track covered by this BAM (0x01=1)
$05	Highest+1 track covered by this BAM (0x33=51)
$06-$0A	BAM for track 1. The first byte shows the "blocks free" for this track, the remaining 4 show the BAM for the track.
$0B-$0F	BAM for track 2
...	...
$FB-$FF	BAM for track 50

Being bit-based, the BAM entries need some explanation. The first track entry in the above BAM sector is at offset 06, "1D FF FF FF 1F". The first number is how many blocks are free on this track ($1D=29) and the remainder is the bit representation of the usage map for the track. These entries must be viewed in binary to make any sense. First convert the values to binary:

```
FF=11111111, FF=11111111, FF=11111111, 1F=00011111
```

In order to make any sense from the binary notation, flip the bits around.

```
           111111 11112222 222222
01234567 89012345 67890123 456789...
------------------------- ---------
11111111 11111111 11111111 11111000
^                               ^
sector 0                  sector 28
```

Since we are on the first track, we have 29 sectors, and only use up to the bit 28 position. If a bit is on (1), the sector is free. Therefore, track 1 is clean, all sectors are free. Any leftover bits that refer to sectors that don't exist, like bits 29-31 in the above example, are set to allocated.

Second BAM block 38/3

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 26 06 43 00 33 65 1B FF FF FF 07 1B FF FF FF 07
10: 1B FF FF FF 07 19 FF FF FF 01 19 FF FF FF 01 19
20: FF FF FF 01 19 FF FF FF 01 19 FF FF FF 01 19 FF
30: FF FF 01 19 FF FF FF 01 19 FF FF FF 01 19 FF FF
40: FF 01 19 FF FF FF 01 19 FF FF FF 01 17 FF FF 7F
50: 00 17 FF FF 7F 00 17 FF FF 7F 00 17 FF FF 7F 00
60: 17 FF FF 7F 00 17 FF FF 7F 00 17 FF FF 7F 00 17
70: FF FF 7F 00 17 FF FF 7F 00 17 FF FF 7F 00 17 FF
80: FF 7F 00 17 FF FF 7F 00 17 FF FF 7F 00 1D FF FF
90: FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF
A0: 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F
B0: 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D
C0: FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF
D0: FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF
E0: FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF
F0: 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F
```

Bytes	**Description**
$00-$01	T/S pointer to third BAM sector (38/6)
$02	DOS version byte (0x43='C')
$03	Reserved
$04	Lowest track covered by this BAM (0x33=51)
$05	Highest+1 track covered by this BAM (0x65=101)
$06-$0A	BAM for track 51. The first byte shows the "blocks free" for this track, the remaining 4 show the BAM for the track.
$0B-$0F	BAM for track 52
...	...
$FB-$FF	BAM for track 100

Third BAM block 38/6

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 26 09 43 00 65 97 1D FF FF FF 1F 1D FF FF FF 1F
10: 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D
20: FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF
30: FF FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF
40: FF 1F 1D FF FF FF 1F 1D FF FF FF 1F 1D FF FF FF
50: 1F 1D FF FF FF 1F 1B FF FF FF 07 1B FF FF FF 07
60: 1B FF FF FF 07 1B FF FF FF 07 1B FF FF FF 07 1B
70: FF FF FF 07 1B FF FF FF 07 1B FF FF FF 07 1B FF
80: FF FF 07 1B FF FF FF 07 1B FF FF FF 07 1B FF FF
90: FF 07 1B FF FF FF 07 1B FF FF FF 07 19 FF FF FF
A0: 01 19 FF FF FF 01 19 FF FF FF 01 19 FF FF FF 01
B0: 19 FF FF FF 01 19 FF FF FF 01 19 FF FF FF 01 19
C0: FF FF FF 01 19 FF FF FF 01 19 FF FF FF 01 19 FF
```

```
D0: FF FF 01 17 FF FF 7F 00 17 FF FF 7F 00 17 FF FF
E0: 7F 00 17 FF FF 7F 00 17 FF FF 7F 00 17 FF FF 7F
F0: 00 17 FF FF 7F 00 17 FF FF 7F 00 17 FF FF 7F 00
```

Bytes	Description
$00-$01	T/S pointer to fourth BAM sector (38/9)
$02	DOS version byte (0x43='C')
$03	Reserved
$04	Lowest track covered by this BAM (0x65=101)
$05	Highest+1 track covered by this BAM (0x97=151)
$06-$0A	BAM for track 101. The first byte shows the "blocks free" for this track, the remaining 4 show the BAM for the track.
$0B-$0F	BAM for track 102
...	...
$FB-$FF	BAM for track 150

Fourth BAM block 38/9

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 27 01 43 00 97 9B 17 FF FF 7F 00 17 FF FF 7F 00
10: 17 FF FF 7F 00 17 FF FF 7F 00 00 00 00 00 00 00
20: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
40: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Bytes	Description
$00-$01	T/S pointer to first directory sector (39/1)
$02	DOS version byte (0x43='C')
$03	Reserved
$04	Lowest track covered by this BAM (0x97=151)
$05	Highest+1 track covered by this BAM (0x9B=155)
$06-$0A	BAM for track 151. The first byte shows the "blocks free" for this track, the remaining 4 show the BAM for the track.
$0B-$0F	BAM for track 152
...	...
$15-$19	BAM for track 154
$1A-$FF	Not used

17.11 The D90 disk image format

The D90 image is bit-for-bit copy of the hard drives in the D9090 and D9060. The specifications are as follows:

Specifications	D9060	D9090
Hard Disk Mechanism	Tandom TM602S	Tandom TM603S
Cylinders	153	153
Heads	4	6
Sectors	32	32
Sector Size	256	256
Disk Interface	ST-506	ST-506

Unformatted Capacity	6.4 MB	9.6 MB
Formatted Capacity	5.01 MB	7.52 MB
Available Physical Sectors	19584	29376
Usable CBM Blocks	19441	29162

Note that the unformatted capacity is simply the total raw storage and makes no accommodation for the management of the information. A disk needs to be low-level formatted to include sync bits, sector identifiers, error checking, and correcting bits to reliably find and hold data. This results in an overall loss of about 22% of space, which wasn't unusual for ST-506 MFM hard disks at the time. Modern drives use more advanced storage techniques to reduce this overhead. Earlier published information stating these drives didn't use their full capacity was incorrect.

It can be seen that the only difference between this two mechanisms was the number of heads. In fact the only difference between the two D9090/60 models is the hard drive mechanism and the setting of the J14 configuration jumper which reflects which head count to use. Jumper J13 is connected to the FDC, but in the latest ROM it has no function.

The slight difference between the physical sectors and available blocks is due to the overhead of the CBM filing system (BAM, directory sectors, and bad block list).

The D9090/60 hard disk is the only Commodore drive which uses a "track 0". Sector 0 of this track contains the track and sector information for the header, directory, beginning of the BAM, and the beginning of the bad sector list.

Below is a dump of the configuration sector (track 0, sector 0):

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 00 01 00 FF 4C 0A 4C 14 01 00 49 44 00 00 00 00
10: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
...
```

Bytes	**Description**
$00-$01	T/S pointer to bad blocks list
$02	Always 0x00
$03	Always 0xff (DOS version?)
$04-$05	T/S pointer to first directory entry
$06-$07	T/S pointer to header
$08-$08	T/S pointer to first BAM sector
$0A-$0B	Disk ID, "ID" in this example
$0C-$FF	Always 0x00

This sector is the same for both the D9090 and D9060 as the two hard drives had the same number of cylinders. The the header and directory are placed in the middle of the disk (track 76) to minimize access times. The on-board DOS was designed to accommodate for different cylinder configurations so it was easy to install larger disks by only modifying the FDC ROM. The number of sectors can not exceed 32, and the number of heads can not exceed 7 as they would cause unhandled numeric overflow in the DOS. This variation in configuration values makes it difficult to determine them based solely on the D90 file size. At this point only the stock image sizes of 5,013,504, and 7,520,256 will be attachable in VICE.

Regardless of the size, access to the drive is translated from CHS (cylinder, head, sector) to LBA (logical block address) in the FDC. Although the drive mechanism interface is ST-506, the AM2910 bridges the SASI communication from the FDC.

Since most hard disks of that era had some type of physical defect, and no defect management system, a bad blocks list is included in the DOS. This list begins at track 0, sector 1 as seen from the configuration sector. Below is a dump of an empty bad blocks list (track 0, sector 1):

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
...
```

The list begins as a typical T/S linking sector, however, it is terminated with a T/S of $FF/$FF unlike the typical $00/$xx normally seen in other standard files. The following is a more practical example:

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 00 02 10 20 FF 02 30 40 FF 08 50 60 FF 14 70 80
...
```

The actual information starts at offset $02. "Bad" blocks are simply marked as "allocated" in the BAM so the DOS doesn't try to put any new data in them. This file is processed by setting the "working" track to 0. Starting at the beginning (offset $02), a byte is read. If this value is not $FF, it is interpreted as the "working" sector. The "working" track/sector is marked as bad (used). We continue on by reading the next byte as the "working" sector and marking it until a $FF is encountered. In the above example, track $0/$10 and $0/$20 would be marked as bad. Once the $FF is found, the next byte is the new "working" track if it is not $FF; if it is, the list is finished. Otherwise we continue processing the list just as we did when the "working" track was 0. In the above example, track $2/$30, $2/$40, $8/$50, $8/$60, $14/70, and $14/$80 would be marked as bad. Note that the list in this example would continue onto track 0, sector 2 at offset $2 as the T/S link is set. This list is processed during a "validation" as the current BAM is erased and rebuilt. It also appears to be processed after a format, but it is not certain if the DOS generates the list or if it was generated in factory by a diagnostic system prior to the drives distribution.

The BAM (Block Availability Map) generally begins on track 1, sector 0, and continues on as necessary based on the disk configuration. Below is a dump of that first BAM sector with a configuration of 32 sectors per head, and 6 heads:

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 09 00 FF FF 00 08 00 00 00 00 00 00 00 00 00 00
10: 1E FC FF FF FF 20 FF FF FF FF 20 FF FF FF FF 20
20: FF FF FF FF 20 FF FF FF FF 20 FF FF FF FF 1F FE
30: FF FF FF 20 FF FF FF FF 20 FF FF FF FF 20 FF FF
...
F0: FF 20 FF FF FF FF 20 FF FF FF FF 20 FF FF FF FF
```

Bytes	**Description**
$00-$01	T/S pointer to next BAM sector ($9/$0)

$02-$03	T/S pointer to previous BAM sector ($FF/$FF)
$04	Lowest track covered by this BAM ($0)
$05	Highest+1 track covered by this BAM ($8)
$06	Possible start of bitmap data
$10-$14 (first entry based on this example)	BAM for track 0, head 0. The first byte shows the "blocks free" for this head, the remaining 4 show the BAM for this track and head.
$24-$08	BAM for track 0, head 1.
...	...
$2E-$32	BAM for track 1, head 0.
...	...
$FB-$FF	BAM for track 7, head 5.

If the T/S pointer is $FF/$FF, it means the end of the BAM. So in this case, there is no previous BAM sector, whereas in the last BAM sector, bytes $01-$02 will be $FF/$FF indicating that there is no next BAM sector.

Bytes $4 and $5 show the range of which tracks this BAM sector applies to. There are 250 bytes to hold this information. The size of each bitmap entry is calculated by "ceiling(sectors_per_head/8)+1"; the "1" is to include the block count. This value is then multipled by the head number and divided into 250. The remainder is added to 6 (the offset for the start of bitmap data). For this example, the bitmap entry size would be "ceiling(32/8)+1" which is 5 bytes. The offset is "(250%(5*6))+6" which is 16 or $10. The goal of this calculation is to place the maximum number of whole tracks inside each BAM. In this case, "int(250/(5*6))" is 8, so we can store 8 tracks per BAM sector. Therefore the lowest track is 0, and the highest is 0 + 8. The next BAM sector will handle tracks 8 to 15 (16 - 1). The position of the BAMs will start at track 1, and the second will be at track 9 (8 + 1), the third will be at track 17 (2 * 8 + 1), and so on. For this example with 153 tracks, we will need at least 20 (153 divided by 8) BAM sectors. BAM sectors are always placed on sector 0. The header and directory will never start on sector 0, so we don't have to worry if one of the BAM sectors will end up on track 76 for example.

Being bit-based, the BAM entries need some explanation. The first track (track 0) entry in the above BAM sector is at offset $10, "1E FC FF FF FF". The first number is how many blocks are free on this track and head ($1E=30) and the remainder is the bit representation of the usage map for the track. These entries must be viewed in binary to make any sense. First convert the values to binary:

```
FC=11111100, FF=11111111, FF=11111111, FF=11111111
```

In order to make any sense from the binary notation, flip the bits around.

```
           111111 11112222 22222233
01234567 89012345 67890123 45678901
------------------------- ---------
00111111 11111111 11111111 11111111
^                                 ^
sector 0                  sector 31
```

If a bit is on (1), the sector is free. We can see all but sectors 0 and 1 are free here as it holds the configuration sector, and one sector for the bad block list respectively.

At offset $24, "1F FE FF FF FF" describes the allocation at track 1, sector 0. The first value is the number of free blocks ($1F=31) and the next 4 indicate which sectors are used (or available); in this case sector 0 is allocated as it is used by the first BAM sector.

Below is the dump of the last BAM sector (track 152, sector 0):

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: FF FF 91 00 98 99 00 00 00 00 00 00 00 00 00 00
10: 1F FE FF FF FF 20 FF FF FF FF 20 FF FF FF FF 20
20: FF FF FF FF 20 FF FF FF FF 20 FF FF FF FF 00 00
30: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
...
```

Here we can see it only covers tracks 152 ($98) to 152 ($99-1). So only 6 entries are valid, and the rest are all zeros. The next T/S pointer is $FF/$FF indicating this is the last BAM sector.

When the blocks free is calculated, only track 0 is excluded. Other Commodore drives exclude the dedicated directory track.

The directory is generally on track 76 (152 divided by 2), with sector 20 containing the header (disk name, disk ID's). The directory entries begin at sector 10. The drives interleave is 10 sectors and it appears the directory entry is allocated before the header on a format. Once the directory grows, the interleave becomes 3, as on most Commodore drives. Unlike most Commodore drives, the directory is not limited to track 76, nor is file data limited to be else where. Once track 76 is filled with directory entries, it will look in other tracks to place more entries. Filling the disk to capacity will also place data into track 76. Be warned though as the directory listing can exceed the amount of memory available on the Commodore machine which is "loading" the directory. It is wise to use a DOS wedge which displays the directory as it is being read but doesn't store it in memory.

The D9090/60 emulation in VICE was tested to see how many one block files could be created on a D9090 image. The disk was filled with 25922 files which required 3241 blocks for the directory entries.

Below is a dump of the header at track 76, sector 20:

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
    -----------------------------------------------
00: 4C 0A 00 00 00 00 54 45 53 54 A0 A0 A0 A0 A0 A0
10: A0 A0 A0 A0 A0 A0 A0 A0 49 44 A0 33 41 A0 A0 A0
20: A0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
...
F0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

This is very similar to the D80/D82 header in placement but without the DOS version value. Also note that the DOS version bytes are "3A". For the D90, it is believed that the $FF at offset $3 in the configuration sector dictates the DOS version.

The directory entry generally starts at track 76, sector 10. It has the identical layout as all other Commodore drives. There are no files specific to the D90 image other than super side sectors for larger REL files.

17.12 The DHD disk image format

The DHD image is bit-for-bit copy of a Creative Micro Designs (CMD) Hard Drive (HD). CMD designed their DOS so that it could co-exist with other operating systems from other computer platforms. At the time, SCSI HDs were very expensive, making this a likely scenario for some. In this section, a block refers to a SCSI HD data block which is 512 bytes. A SCSI HD refers to the location of a blocks on the disk as logical block address (LBA). A sector refers to a commodore disk sector which is 256 bytes. SCSI, being a smarter disk system (compared to earlier ones), translates the LBAs into physical head, sector, cylinder coordinates internally thus simplifying the host's interface. So, for example, a 40 MB HD of that era would have had 82332 (or $1419C) LBAs. As HD technology improved, the number of sectors per cylinder became variable depending on the cylinder which allowed for greater capacity. The host therefore has no information about the HD geometry other than the total number of LBAs. Most operating systems place their partition tables or boot code near the beginning of the HD. Because of this variability, CMD designed their DOS to begin at any location in increments of 128 blocks. It was therefore possible to configure foreign partitions on various operating system so that the CMD data was ignored. The starting location of the CMD whole disk partition will be referred to as X blocks.

Upon reset, the boot ROM looks for the CMD whole partition by examining LBA X+2 (starting at X=0) and checking for a special signature in the block. If it does not find it, X will increase by 128, and it will try again. It will do this until it finds the signature, or until it gets a read error from the HD signaling the end of disk space. The device will then switch to installation mode.

The following is an example configuration block of a 40 MB drive:

```
X *     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F        ASCII
$200 +  -----------------------------------------------   ----------------
000400: 94 6C 00 0E 7D E7 00 00 00 00 00 00 00 00 00 00
000410: 20 20 20 20 31 2E 39 32 30 33 2F 32 32 2F 39 36   ....1.9203/22/96
000420: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000430: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
000440: 03 02 00 7E C9 55 00 00 00 00 00 00 00 00 00 00
000450: 20 20 20 20 32 2E 30 30 30 33 2F 32 32 2F 39 36   ....2.0003/22/96
000460: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
...
000500: 00 FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
000510: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
000530: FF FF FF FF FF FF FF FF 00 01 FF FF FF FF FF FF
000540: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
000570: 00 41 FF FF FF FF FF FF FF FF FF FF FF FF FF FF
000580: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
0005A0: FF FF FF FF FF FF FF FF 00 9C FF FF FF FF FF FF
0005B0: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
0005E0: 00 0C 01 01 0C 80 01 00 00 00 00 00 00 00 00 00
0005F0: 43 4D 44 20 48 44 20 20 8D 03 88 8E 02 88 EA 60   CMD HD.........
...
000E00: < start of first OS, n=0, CMD DOS, see $402-$403 >
007E00: < start of second OS, n=1, GEOS overlay, see $442-$443 >
```

```
010000: < start of partition table, see $5E6-$5E7 >
```

The first 256 bytes of the configuration block appear to be a list of loadable operating systems for the drive. Each entry, "n", uses 64 bytes, so there can be at most 4. The first is the standard CMD DOS, while the second is an overlay for GEOS. The boot ROM explicitly loads the first (n=0).

The CMD DOS supports up to 56 connected HDs: 7 devices with 8 logical units (LUNs) each. For that era, most HDs only supported a single LUN (0), whereas more exotic array devices would allow for multiple LUNs. The relative location of the disk, "d", in the whole drive map, is stored in the next 224 bytes. For example, the first disk would start at location 0. The next disk following the first, would start at LBA $01419C (the size of the first disk plus its starting point in the map). The 24-bit values used here for the placement are adequate since a fully populated CMD HD can only access 255 16 MB partitions, which his just under 4 GiB.

The last portion of the configuration block holds the device parameters (device number, partition location, default partition) as well as the CMD HD signature.

Bytes	**Description**
(X*$200)+($400-$4FF)	Operating System Table
(X*$200+$400+n*$40)+$00	Destination memory page
(X*$200+$400+n*$40)+$01	Number of pages
(X*$200+$400+n*$40)+$02-$03	Location of data offset from X*$200 in sectors (256 bytes); MSB format
(X*$200+$400+n*$40)+$04-$05	Checksum of data; MSB format
(X*$200+$400+n*$40)+$06-$0F	All 0x00
(X*$200+$400+n*$40)+$10-$17	ASCII of version number
(X*$200+$400+n*$40)+$18-$1F	ASCII of date
(X*$200+$400+n*$40)+$20-$3F	All 0x00
(X*$200+$500)+d+$00	SCSI ID of HD in bits 7-4, LUN in bits 3-0; $FF means not present
(X*$200+$500)+d+$38	MSB of disk location in drive map
(X*$200+$500)+d+$70	Middle byte of disk location in drive map
(X*$200+$500)+d+$A8	LSB of disk location in drive map
(X*$200+$500)+$E0	0x00 (unknown)
(X*$200+$500)+$E1	Device number (12 or 0x0C by default)
(X*$200+$500)+$E2	Default partition number (1 by default)
(X*$200+$500)+$E3	Default partition number (1 by default)
(X*$200+$500)+$E4	Device number (12 or 0x0C by default)
(X*$200+$500)+$E5	0x80: 2 to the power of the SCSI ID of host (which is always 7)
(X*$200+$500)+$E6-$E7	Location of CMD partition table offset from X*$200 in sectors (256 bytes); MSB format
(X*$200+$500)+$E8-$EF	All 0x00
(X*$200+$500)+$F0-$F7	0x43 0x4D 0x44 0x20 0x48 0x44 0x20 0x20: First half of signature

(X*$200+$500)+$F8-$FF	0x8D 0x03 0x88 0x8E 0x02 0x88 0xEA 0x60: Second half of signature; decodes to STA $8803, STX $8802, NOP, RTS

The CMD partition table is a special disk image with one track and 32 sectors. It is very similar the standard directory structure of most CBM drives. The first two bytes of the sector indicate the location of the next track/sector of the partition table; it is usually in sequence. If the next track is set to $00 (the next sector will be $ff), then it is the last sector of the partition table.

The standard partition table sectors:

Bytes	**Description**
$00-$1F	First partition entry for this sector
$20-$3F	Second partition entry for this sector
$40-$5F	Third partition entry for this sector
$60-$7F	Fourth partition entry for this sector
$80-$9F	Fifth partition entry for this sector
$A0-$BF	Sixth partition entry for this sector
$C0-$DF	Seventh partition entry for this sector
$E0-$FF	Eighth partition entry for this sector

The standard partition entry:

Bytes	**Description**
$00-$01	Track/Sector location of next partition sector ($00 $00 if not the first entry in the sector)
$02	Partition type: 0x00=none, 0x01=native, 0x02=1541, 0x03=1571, 0x04=1581, 0x05=1581CPM, 0x06=Print Queue, 0x07=Foreign, 0xFF=System. Only partition 0 can be a system type, and type 6 isn't known to be implemented on any CMD DOS.
$03-$04	0x00 0x00
$05-$14	16 character partition name (in PETASCII, padded with $A0)
$15-$17	Location of the partition (in blocks) in MSB format. Partition 0, the system partition, always starts at X*$200.
$18-$1D	All 0x00
$1E-$1F	Size of partition (in blocks) in MSB format. Partition 0, the system partition is always 0x90 blocks, which includes blocks from X*$200 to the end of the partition table.

When a partition is deleted, its entry is removed and all others stay in their existing place. The partition data following the deleted partition is moved back to fill in the gap from the deletion; this can be very time consuming. So, when deleting a partition, it is suggested to start with the one with the highest start LBA.

The data at the starting LBA is simply the associated disk image of the partition type. It follows the formats (D64, D71, and D81) documented previously.

17.13 The P00 image format

(This section was contributed by Peter Schepers and slightly edited by Marco van den Heuvel.)

These files were created for use in the PC64 emulator, written by Wolfgang Lorenz. Each one has the same layout with the filetype being stored in the DOS extension (i.e. Pxx is a PRG, Sxx is a SEQ, Uxx is a USR and Rxx is a RELative file), and the header is only 26 bytes long.

This is a dump of a Pxx file (PRG)...

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
      -----------------------------------------------
0000: 43 36 34 46 69 6C 65 00 43 52 49 53 49 53 20 4D
0010: 4F 55 4E 54 41 49 4E 00 00 00
```

Bytes	Description
$00-$06	ASCII string "C64File"
$07	Always $00
$08-$17	Filename in PETASCII, padded with $00 (not $A0, like a D64)
$18	Always $00
$19	REL file record size ($00 if not a REL file)
$1A-??	Program data

The 'xx' in the extension of the file is usually 00, except when we have two DOS filenames which would be the same, but the C64 filenames are different! If we have two C64 filenames which are the same, they *cannot* co-exist in the same directory. If we have two files which do convert down to be the same DOS filename, the extension is incremented until an unused one is found (x01, x02, x03, up to x99). We can have up to 99 different C64 files with the same corresponding DOS names as that's all the extension will hold (from P00 to P99).

Each PC64 file only has one entry, there are no multi-file containers allowed. This could result in a large number of these files in a directory, even for only a few programs, as each C64 file will result in a PC64 file entry. The best use for a PC64 file is a single-file program, one which does not load anything else.

17.14 The CRT cartridge image format

This chapter is based on CRT.txt (rev1.14) compiled by Peter Schepers, with additional contributions from Per Hakan Sundell, Markus Brenner, Marco Van Den Heuvel, Groepaz.

Cartridge files were introduced in the CCS64 emulator, written by Per Hakan Sundell, and use the ".CRT" file extension. This format was created to handle the various ROM cartridges that exist, such as Action Replay, the Power cartridge, and the Final Cartridge.

The file format was extended to support other CBM machines by the VICE team in version 2.0

Normal game cartridges can load into several different memory ranges (on the C64 this is $8000-9FFF, $A000-BFFF or $E000-FFFF). Newer utility and freezer cartridges were less intrusive, hiding themselves until called upon, and still others used bank-switching techniques to allow much larger ROMs than normal. Because of these "stealthing" and bank-switching methods, a special cartridge format was necessary, to let the emulator know where the cartridge should reside, the control line states to enable it and any special hardware features it uses.

17.14.1 Header contents

For example, here is a dump of a sample 8KiB generic C64 cartridge, "Attack Of The Mutant Camels"...

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F        ASCII
      -----------------------------------------------   ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20   C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 00 00 01 00 00 00 00 00 00   ...@............
0020: 41 54 54 41 43 4B 20 4F 46 20 54 48 45 20 4D 55   ATTACK OF THE MU
0030: 54 41 4E 54 20 43 41 4D 45 4C 53 00 00 00 00 00   TANT CAMELS.....
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00   CHIP.. ....... .
0050: D3 9B BC FE C3 C2 CD 38 30 EA EA EA A9 01 85 13   .......80.......
0060: 4C B3 9B A9 08 85 5A 88 D0 FD C6 5A D0 F9 60 D0   L.....Z....Z..‘.
```

```
$0000-$000F - 16-byte cartridge signature (padded with space characters)
              "C64 CARTRIDGE   " - indicates a C64 cartridge
              "C128 CARTRIDGE  " - indicates a C128 cartridge (added in v2.0)
              "CBM2 CARTRIDGE  " - indicates a CBMII cartridge (added in v2.0)
              "VIC20 CARTRIDGE " - indicates a VIC20 cartridge (added in v2.0)
              "PLUS4 CARTRIDGE " - indicates a C16/PLUS4 cartridge (added in v2.0)

$0010-$0013 - File header length (in high/low format, counting from offset $0000).
              The default (and also the minimum) value is $40, which should always
              be used for compatibility reasons. Some cartridge images exist which
              use a value of $00000020, which is wrong.

$0014-$0015 - Cartridge version (high/low, presently 02.00)
              $01 $00 - original v1.0 specs
              $01 $01 - VICE v1.1 specs (adds CRT sub type/hardware revision)
              $02 $00 - VICE v2.0 specs (introduces VIC20, PLUS4, C128, CBM2)

$0016-$0017 - Cartridge hardware type ($0000, high/low) - see below for details

      $0018 - Cartridge port EXROM line status (C64 only)
      $0019 - Cartridge port GAME line status (C64 only)

              Determines the mode the cartridge starts in:

              exrom / game
              0       0    - 16k Game
              0       1    -  8k Game
              1       0    -  ultimax
              1       1    -  ram/off

              Emulators should only use this field with "generic" C64 cartridges
              (type 0), for all others the startup mode is implied by the type
              of the cartridge. It should still be filled correctly in the crt
              file, but a lot of files with wrong values exist "in the wild".
```

```
      $001A - Cartridge Hardware Revision/Subtype (usually 0) (added in v1.01)

$001B-$001F - Reserved for future use

$0020-$003F - 32-byte cartridge  name  (uppercase,  padded with null characters)

$0040- xxxx - Cartridge contents (called CHIP PACKETS, as there can be more than
              one per CRT file). See below for a breakdown of the CHIP format.
```

17.14.2 CHIP Contents

The following is the contents of the CHIP packet, from position $0040 on in the CRT file. Note I have re-adjusted the starting address to be $0000, since we are now looking at a file contained in the .CRT file, and all size references are from where it starts.

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0010: D3 9B BC FE C3 C2 CD 38 30 EA EA EA A9 01 85 13  .......80.......
0020: 4C B3 9B A9 08 85 5A 88 D0 FD C6 5A D0 F9 60 D0  L.....Z....Z..‘.
0030: F2 60 A9 04 85 49 A9 00 85 48 A2 00 A5 48 9D 40  .‘...I...H...H.@
0040: 03 A5 49 9D 60 03 A5 48 18 69 28 85 48 A5 49 69  ..I.‘..H.i(.H.Ii
0050: 00 85 49 E8 E0 18 D0 E4 60 A6 03 A4 02 BD 40 03  ..I.....‘.....@.
...
$0000-$0003 - Contained ROM signature "CHIP" (note there can be more than one
              CHIP image in a .CRT file)

$0004-$0007 - Total packet length (high/low format)
              Length of ROM image size and header combined, this should
              always be equal to "ROM image size" + $10

$0008-$0009 - Chip type

              0 - ROM
              1 - RAM
              2 - Flash ROM
              3 - EEPROM

              This is informal only. A RAM chunk may be empty, but doesn’t
              have to be.

$000A-$000B - Bank number ($0000 - normal cartridge)

              Number of the bank this CHIP package appears in. This should be
              a logical bank number, not the value stored into some banking
              register which might have its bit-order rearranged.
```

```
$000C-$000D - Starting load address (high/low format)

              The load address tells the loader which part of the given bank
              is to be used for this chunk. It is only really important in
              formats where actually more than one CHIP chunk exists for each
              bank - most notable the "generic" (type 0) cartridges.

$000E-$000F - ROM image size in bytes  (high/low format, typically $2000 or $4000)

$0010- xxxx - ROM data
```

17.14.3 C64 Cartridge Specifics

CRT ID	Cartridge Type
0	generic cartridge
1	Action Replay
2	KCS Power Cartridge
3	Final Cartridge III
4	Simons' BASIC
5	Ocean type 1*
6	Expert Cartridge
7	Fun Play, Power Play
8	Super Games
9	Atomic Power
10	Epyx Fastload
11	Westermann Learning
12	Rex Utility
13	Final Cartridge I
14	Magic Formel
15	C64 Game System, System 3
16	Warp Speed
17	Dinamic**
18	Zaxxon, Super Zaxxon (SEGA)
19	Magic Desk, Domark, HES Australia
20	Super Snapshot V5
21	Comal-80
22	Structured BASIC
23	Ross
24	Dela EP64
25	Dela EP7x8
26	Dela EP256
27	Rex EP256
28	Mikro Assembler
29	Final Cartridge Plus
30	Action Replay 4
31	Stardos
32	EasyFlash

33	EasyFlash Xbank
34	Capture
35	Action Replay 3
36	Retro Replay (Subtype 1: Nordic Replay)
37	MMC64
38	MMC Replay
39	IDE64
40	Super Snapshot V4
41	IEEE-488
42	Game Killer
43	Prophet64
44	EXOS
45	Freeze Frame
46	Freeze Machine
47	Snapshot64
48	Super Explode V5.0
49	Magic Voice
50	Action Replay 2
51	MACH 5
52	Diashow-Maker
53	Pagefox
54	Kingsoft
55	Silverrock 128K Cartridge
56	Formel 64
57	RGCD (Subtype 1: Hucky)
58	RR-Net MK3
59	EasyCalc
60	GMod2
61	MAX Basic
62	GMod3
63	ZIPP-CODE 48
64	Blackbox V8
65	Blackbox V3
66	Blackbox V4
67	REX RAM-Floppy
68	BIS-Plus
69	SD-BOX
70	MultiMAX
71	Blackbox V9
72	Lt. Kernal Host Adaptor
73	RAMLink
74	H.E.R.O.
75	IEEE Flash! 64
76	Turtle Graphics II
77	Freeze Frame MK2
78	Partner 64
79	Hyper-BASIC

80 Universal Cartridge 1
81 Universal Cartridge 1.5
82 Universal Cartridge 2
83 BMP Data Turbo 2000
84 Profi-DOS
85 Magic Desk 16
86 Protovision "Megabyter"
87 Magic Desk Plus (Subtype 0: SRAM + 32KiB EEPROM, 1: SRAM + 8KiB EEPROM, 2: 32KiB EEPROM, 3: 8KiB EEPROM or 4: SRAM only)

(*Note: Ocean type 1 includes Navy Seals, Robocop 2 & 3, Shadow of the Beast, Toki, Terminator 2 and more)

(**Note: Dinamic includes Narco Police and more)

17.14.3.1 0 - generic cartridge

generic cartridges can contain one or two 8k CHIP packets, one for ROML and one for ROMH. ROML start address is always $8000, ROMH start address is either $a000 or $e000 depending on the GAME/EXROM config.

The following is a chart taken from the "Commodore Programmers Reference Guide". It details the state of various areas of memory depending on the state of the control lines.

Legend: L - ROML (low) H - ROMH (high) G - GAME E - EXROM

```
Addr        LHGE   LHGE   LHGE   LHGE   LHGE   LHGE   LHGE   LHGE   LHGE
Range
            1111   101X   1000   011X   001X   1110   0100   1100   XX01
          default                 00X0                             Ultimax
-------------------------------------------------------------------------
E000-FFFF Kernal  RAM    RAM   Kernal  RAM   Kernal Kernal Kernal ROMH(*)
D000-DFFF IO/CHR IO/CHR IO/RAM IO/CHR  RAM   IO/CHR IO/CHR IO/CHR   I/O
C000-CFFF  RAM    RAM    RAM    RAM    RAM    RAM    RAM    RAM     -
A000-BFFF BASIC   RAM    RAM    RAM    RAM   BASIC   ROMH   ROMH    -
8000-9FFF  RAM    RAM    RAM    RAM    RAM    ROML   RAM    ROML  ROML(*)
4000-7FFF  RAM    RAM    RAM    RAM    RAM    RAM    RAM    RAM     -
1000-3FFF  RAM    RAM    RAM    RAM    RAM    RAM    RAM    RAM     -
0000-0FFF  RAM    RAM    RAM    RAM    RAM    RAM    RAM    RAM    RAM
```

(*) Internal memory does not respond to write accesses in these areas

From the above chart, the following table can be built. It shows standard cartridges, either 8KiB or 16KiB in size, and the memory ranges they load into.

```
Type     Size   Game   EXRom  Low Bank  High Bank
         in K   Line   Line    (ROML)    (ROMH)
-----------------------------------------------
Normal    8KiB    hi     lo     $8000      ----
Normal    16KiB   lo     lo     $8000     $A000
Ultimax   8KiB    lo     hi     $E000      ----
```

The ROMH and ROML lines are CPU-controlled status lines, used to bank in/out RAM, ROM or I/O, depending on what is needed at the time.

generic 8K GAME cartridge

Size	8KiB
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 00 00 01 00 00 00 00 00 00  ...@............
0020: 41 54 54 41 43 4B 20 4F 46 20 54 48 45 20 4D 55  ATTACK OF THE MU
0030: 54 41 4E 54 20 43 41 4D 45 4C 53 00 00 00 00 00  TANT CAMELS.....
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: D3 9B BC FE C3 C2 CD 38 30 EA EA EA A9 01 85 13  .......80.......
```

generic 16K GAME cartridge

This sample is a dump of "Adventure Creator", a 16KiB standard cartridge.

Size	16KiB
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 00 00 00 00 00 00 00 00 00  ...@............
0020: 41 64 76 65 6E 74 75 72 65 20 43 72 65 61 74 6F  Adventure Creato
0030: 72 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  r...............
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 09 80 81 EA C3 C2 CD 38 30 A2 00 78 D8 8E 11 D0  .......80..x....
```

ULTIMAX cartridge

Ultimax cartridges typically are situated in the $E000-FFFF (8KiB) ROM address range. There are some cartridges which only use 4KiB of the 8KiB allocation. If the cartridge is 16KiB in size, then it will reside in both $8000-9FFF and $E000-FFFF.

This sample below is a dump of "Music Machine", a 4KiB ULTIMAX mode cartridge. It is still identified as a "generic cartridge" according to the ID.

Size	4KiB (ULTIMAX mode)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$F000-F7FF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 00 01 00 00 00 00 00 00 00  ...@............
0020: 4D 55 53 49 43 20 4D 41 43 48 49 4E 45 00 00 00  MUSIC MACHINE...
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 10 10 00 00 00 00 F0 00 10 00  CHIP............
0050: 3C 66 C3 C3 66 3C FF FF 18 3C 66 7E 66 66 66 00  <f..f<...<f~fff.
```

17.14.3.2 1 - Action Replay

Size	32KiB (4 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF (all modules)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 01 00 01 00 00 00 00 00 00  ...@............
0020: 41 63 74 69 6F 6E 20 52 65 70 6C 61 79 20 56 00  Action Replay V.
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 0C 80 C3 C2 CD 38 30 4C 60 80 4C 63 80 4C  .......80L‘.Lc.L
```

This cart has 32KiB of ROM, and 8KiB of RAM. The bank switching is done by writing to the I/O-1 range as follows:

```
bit  meaning
---  -------
7    extra ROM bank selector (A15) (unused)
6    1 = resets FREEZE-mode (turns back to normal mode)
5    1 = enable RAM at ROML ($8000-$9FFF) &
         I/O-2 ($DF00-$DFFF = $9F00-$9FFF)
4    ROM bank selector high (A14)
3    ROM bank selector low  (A13)
2    1 = disable cartridge (turn off $DE00)
1    1 = /EXROM high
0    1 = /GAME low
```

Additionally the RAM or ROM can be available through a window in the I/O-2 range.

17.14.3.3 2 - KCS Power Cartridge

Size	16KiB (2 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	module #1 - $8000-9FFF module #2 - $A000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 20 01 00 00 02 00 00 00 00 00 00 00 00  ... ............
0020: 4B 43 53 20 50 6F 77 65 72 20 43 61 72 74 72 69  KCS Power Cartri
0030: 64 67 65 00 00 00 00 00 00 00 00 00 00 00 00 00  dge.............
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 5E FE C3 C2 CD 38 30 78 D8 A2 FF 9A A9 27  ..^....80x.....’
...
2050: 43 48 49 50 00 00 20 10 00 00 00 00 A0 00 20 00  CHIP.. ....... .
2060: 97 E3 16 A1 FF FF FF 20 13 A0 A5 01 09 01 85 01  ....... ........
```

17.14.3.4 3 - Final Cartridge III

Size	64KiB (4 banks of 16KiB each)
EXROM	inactive (hi) (1)
GAME	inactive (hi) (1)
Load address	$8000-BFFF (all modules)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 03 00 00 00 00 00 00 00 00  ...@............
0020: 46 69 6E 61 6C 20 43 61 72 74 72 69 64 67 65 20  Final Cartridge
0030: 49 49 49 20 31 39 38 37 00 00 00 00 00 00 00 00  III 1987........
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 09 80 5E FE C3 C2 CD 38 30 4C 4C 80 4C 55 95 4C  ..^....80LL.LU.L
...
4050: 43 48 49 50 00 00 40 10 00 00 00 01 80 00 40 00  CHIP..@.......@.
4060: 01 02 00 81 5D 81 61 81 99 81 D8 81 0B 82 33 82  ....].a.......3.
...
8060: 43 48 49 50 00 00 40 10 00 00 00 02 80 00 40 00  CHIP..@.......@.
8070: 20 43 80 20 52 80 A9 4E 20 05 DE 20 FD BF AD 39   C. R..N .. ...9
...
C070: 43 48 49 50 00 00 40 10 00 00 00 03 80 00 40 00  CHIP..@.......@.
C080: A2 06 BD DD 85 95 05 CA 10 F8 AE A0 02 E8 EC A2  ................
```

A total of 64 KiB of ROM memory is organized into four $4000 banks located at $8000-$BFFF.

The banks are arranged in the following way:

```
Bank 0:  BASIC, Monitor, Disk-Turbo
Bank 1:  Notepad, BASIC (Menu Bar)
Bank 2:  Desktop, Freezer/Print
Bank 3:  Freezer, Compression
```

The cartridges uses the entire I/O-1 and I/O-2 range. Bank switching is done by writing the bank number plus $40 into memory location $DFFF. For instance, to select bank 2, $DFFF is set to $42.

The CRT file contains four CHIP blocks, each block with a start address of $8000, length $4000 and the bank number in the bank field. In the cartridge header, both EXROM ($18) and GAME ($19) are set to 1 to enable the 16 KiB ROM configuration.

The registers are arranged in the following way:

One register at $DFFF:

```
bit  meaning
---  -------
7      Hide this register (1 = hidden)
6      NMI line  (0 = low = active) *1)
5      GAME line  (0 = low = active) *2)
4      EXROM line (0 = low = active)
2-3    unassigned (usually set to 0)
```

```
0-1    number of bank to show at $8000
```

1) if either the freezer button is pressed, or bit 6 is 0, then an NMI is generated

2) if the freezer button is pressed, GAME is also forced low

The rest of I/O-1/I/O-2 contain a mirror of the last 2 pages of the currently selected ROM bank (also at $dfff, contrary to what some other documents say)

17.14.3.5 4 - Simons' Basic

Size	16KiB (2 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	module #1 - $8000-9FFF module #2 - $A000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 04 00 00 00 00 00 00 00 00  ...@............
0020: 53 69 6D 6F 6E 27 73 20 42 61 73 69 63 00 00 00  Simon's Basic...
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 52 81 52 81 C3 C2 CD 38 30 41 4C 52 81 20 2C 81  R.R....80ALR. ,.
...
2050: 43 48 49 50 00 00 20 10 00 00 00 00 A0 00 20 00  CHIP.. ....... .
2060: 20 A4 A6 99 9E CB A0 05 A5 A8 91 20 A4 A6 99 A2   .......... ....
```

Simons' BASIC permanently uses 16 KiB ($4000) bytes of cartridge memory from $8000-$BFFF. However, through some custom bank-switching logic the upper area ($A000-$BFFF) may be disabled so Simons' BASIC may use it as additional RAM. Writing a value of $01 to address location $DE00 banks in ROM, $00 disables ROM and enables RAM.

The CRT file contains two CHIP blocks of length $2000 each, the first block having a start address of $8000, the second block $A000. In the cartridge header, EXROM ($18) is set to 0, GAME ($19) is set to 1 to indicate the RESET/power-up configuration of 8 KiB ROM.

17.14.3.6 5 - Ocean type 1

Size	32KiB, 128KiB, 256KiB or 512KiB sizes (4, 16, 32 or 64 banks of 8KiB)
EXROM	active (lo) (0)
GAME	active (lo) (0) inactive (hi) (1) for 512KiB variant (Terminator 2)
Load address	Banks 00-15 - $8000-9FFF Banks 16-31 - $A000-BFFF (except Terminator 2)

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
       -----------------------------------------------  ----------------
00000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
00010: 00 00 00 40 01 00 00 05 00 00 00 00 00 00 00 00  ...@............
00020: 53 48 41 44 4F 57 20 4F 46 20 54 48 45 20 42 45  SHADOW OF THE BE
00030: 41 53 54 00 00 00 00 00 00 00 00 00 00 00 00 00  AST.............
```

```
00040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
00050: 09 80 83 81 C3 C2 CD 38 30 4C 83 81 4C 76 82 80  .......80L..Lv..
 ...
02050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
02060: 59 6D 00 56 AD 00 55 AE F0 00 01 A0 FE 00 01 F8  Ym.V..U.........
 ...
20140: 43 48 49 50 00 00 20 10 00 00 00 10 A0 00 20 00  CHIP.. ....... .
20150: 0A 9A 55 FF 9B 69 57 FE AA 65 96 FE 65 0F D6 D9  ..U..iW..e..e...
```

Here is a list of the known OCEAN cartridges:

```
    Batman The Movie    (128 KiB)
    Battle Command      (128 KiB)
    Double Dragon       (128 KiB)
    Navy Seals          (128 KiB)
    Pang                (128 KiB)
    Robocop 3           (128 KiB)
    Space Gun           (128 KiB)
    Toki                (128 KiB)
    Chase H.Q. II       (256 KiB)
    Robocop 2           (256 KiB)
    Shadow of the Beast (256 KiB)
    Terminator 2        (512 KiB)
```

The 32KiB type of cart has 4 banks of 8KiB ($2000), banked in at $8000-$9FFF.

The 128KiB type of cart has 16 banks of 8KiB ($2000), banked in at $8000-$9FFF.

The 256KiB type of cart has 32 banks of 8KiB ($2000), 16 banked in at $8000-$9FFF, and 16 banked in at $A000-$BFFF.

The 512KiB type of cart has 64 banks of 8KiB ($2000), banked in at $8000-$9FFF.

Bank switching is done by writing to $DE00. The lower six bits give the bank number (ranging from 0-63). Bit 7 in this selection word is always set.

17.14.3.7 6 - Expert Cartridge

Size	8KiB
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 06 01 00 00 00 00 00 00 00  ...@............
0020: 45 78 70 65 72 74 20 43 61 72 74 72 69 64 67 65  Expert Cartridge
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 01 00 00 80 00 20 00  CHIP..@....... .
0050: 00 00 00 0A F3 00 00 00 00 00 00 00 00 00 00 00  ................
```

17.14.3.8 7 - Fun Play, Power Play

Size	128KiB (16 banks of 8KiB modules)

EXROM active (lo) (0)
GAME inactive (hi) (1)
Load address $8000-9FFF (all modules)

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
       -----------------------------------------------  ----------------
00000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
00010: 00 00 00 40 01 00 00 07 00 01 00 00 00 00 00 00  ...@............
00020: 46 55 4E 20 50 4C 41 59 00 00 00 00 00 00 00 00  FUN PLAY........
00030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
00040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
00050: 1E 80 86 EA C3 C2 CD 38 30 1B 00 81 0D 08 80 00  .......80.......
 ...
02050: 43 48 49 50 00 00 20 10 00 00 00 08 80 00 20 00  CHIP.. ....... .
02060: 78 A2 F0 86 01 BD 1D 08 9D F8 00 CA D0 F7 4C 00  x.............L.
 ...
04060: 43 48 49 50 00 00 20 10 00 00 00 10 80 00 20 00  CHIP.. ....... .
04070: 38 E5 68 85 03 B0 11 27 03 12 C0 18 69 27 42 90  8.h....'....i'B.
 ...
06070: 43 48 49 50 00 00 20 10 00 00 00 18 80 00 20 00  CHIP.. ....... .
06080: 44 D0 5E 06 02 C0 44 11 40 04 11 44 01 5F 1C 73  D.^...D.@..D._.s
 ...
1E130: 43 48 49 50 00 00 20 10 00 00 00 39 80 00 20 00  CHIP.. ....9.. .
1E140: 85 EB 41 EA 9E 08 03 00 C0 06 18 01 00 C0 08 03  ..A.............
```

The FUN PLAY Cartridge uses $DE00 for bank selection, and uses 8KiB banks ($2000) at $8000-$9FFF. There are 16 banks of ROM memory and are referenced by the following values:

```
$00 -> Bank 0
$08 -> Bank 1
$10 -> Bank 2
$18 -> Bank 3
$20 -> Bank 4
$28 -> Bank 5
$30 -> Bank 6
$38 -> Bank 7
$01 -> Bank 8
$09 -> Bank 9
$11 -> Bank 10
$19 -> Bank 11
$21 -> Bank 12
$29 -> Bank 13
$31 -> Bank 14
$39 -> Bank 15
```

The bank field in the chip headers is set according to the value written to $DE00. The following bits are used for bank decoding in $DE00 (0 being the LSB, 3 being the MSB).

```
Bit# 76543210
     xx210xx3
```

After copying memory from the ROM banks, the selection program writes a value of $86 to $DE00. This seems either to reset or disable the cartridge ROM.

17.14.3.9 8 - Super Games

Size	64KiB (4 banks of 16KiB each)
EXROM	active (lo) (0)
GAME	active (lo) (0)
Startup mode	16KiB game
Load address	$8000-BFFF (all modules)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 08 00 00 00 00 00 00 00 00  ...@............
0020: 53 55 50 45 52 20 47 41 4D 45 53 00 00 00 00 00  SUPER GAMES.....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 0A 80 0A 80 C3 C2 CD 38 30 00 A9 80 A0 00 85 FB  .......80.......
...
4050: 43 48 49 50 00 00 40 10 00 00 00 01 80 00 40 00  CHIP..@.......@.
4060: 27 80 A8 80 C3 C2 CD 38 30 00 40 C0 40 C0 40 C0  '......80.@.@.@.
...
8060: 43 48 49 50 00 00 40 10 00 00 00 02 80 00 40 00  CHIP..@.......@.
8070: 00 00 00 49 4D C7 64 47 46 45 F3 48 DC 08 7E 0B  ...IM.dGFE.H..~.
...
C070: 43 48 49 50 00 00 40 10 00 00 00 03 80 00 40 00  CHIP..@.......@.
C080: D5 F9 F0 C1 D5 F7 F0 BD E8 B5 02 F0 FB C9 05 30  ..............0
```

The Super Games cartridge uses 4 16KiB banks ($8000-$BFFF) of ROM memory. Bank selecting is done by writing to $DF00.

$DF00 register is as follows:

```
bit   meaning
---   -------
 0    bank bit 0
 1    bank bit 1
 2    mode (0 = EXROM/GAME (bridged on the same wire - 16KiB config)
           1 = cartridge disabled)
 3    write-protect-latch  (1 = no more changes are possible until
                                the next hardware-reset )
4-7   unused
```

17.14.3.10 9 - Atomic Power

Size	32KiB (4 banks of 8KiB modules)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF (all modules)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
```

```
      -------------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 09 00 01 00 00 00 00 00 00  ...@............
0020: 41 74 6F 6D 69 63 20 50 6F 77 65 72 00 00 00 00  Atomic Power....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 0C 80 C3 C2 CD 38 30 4C 41 80 4C 1E 80 4C  .......80LA.L..L
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: 09 80 0C 80 C3 C2 CD 38 30 4C 3F 80 4C 91 80 4C  .......80L?.L..L
...
4060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
4070: EF FC 09 80 C3 C2 CD 38 30 4C 27 80 4C DB 81 4C  .......80L'.L..L
...
6070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP.. ....... .
6080: 09 80 0C 80 C3 C2 CD 38 30 4C 73 86 4C 30 80 4C  .......80Ls.L0.L
```

This cart has 32KiB of ROM and 8KiB of RAM

Writing to I/O-1 will do the following:

```
bit  meaning
---  -------
7    extra ROM bank selector (A15) (unused)
6    1 = resets FREEZE-mode (turns back to normal mode)
5    1 = enable RAM at ROML ($8000-$9FFF) &
         I/O-2 ($DF00-$DFFF = $9F00-$9FFF)
4    ROM bank selector high (A14)
3    ROM bank selector low  (A13)
2    1 = disable cartridge (turn off $DE00)
1    1 = /EXROM high
0    1 = /GAME low
```

If bit 5 (RAM enable) is 1, bit 0,1 (exrom/game) is == 2 (cart off), bit 2,6,7 (cart disable, freeze clear) are 0, then cart ROM (Bank 0..3) is mapped at 8000-9FFF, and cart RAM (Bank 0) is mapped at A000-BFFF and cart RAM (Bank 0) is is enabled in the I/O-2 area using 16KiB game config.

The cart RAM or ROM is available through a window in the I/O-2 range.

17.14.3.11 10 - Epyx Fastload

Size	8KiB
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -------------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 0A 00 01 00 00 00 00 00 00  ...@............
0020: 45 50 59 58 20 46 41 53 54 4C 4F 41 44 00 00 00  EPYX FASTLOAD...
```

```
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 30 80 5E FE C3 C2 CD 38 30 20 04 90 4C 38 DF AB  0.^....80 ..L8..
```

The Epyx FastLoad cart uses a simple capacitor to toggle the ROM on and off:

the capacitor is discharged, and 8KiB game config enabled, by either reading ROML or reading I/O-1. If none of those accesses happen the capacitor will charge, and if it is charged (after 512 cycles) then the ROM will get disabled.

17.14.3.12 11 - Westermann Learning

Size	16KiB
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 0B 00 00 00 00 00 00 00 00  ...@............
0020: 57 45 53 54 45 52 4D 41 4E 4E 00 00 00 00 00 00  WESTERMANN......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 40 00  CHIP.. .......@.
0050: 09 80 9C 80 C3 C2 CD 38 30 A2 00 8E 16 D0 20 84  .......80..... .
```

Any read from the I/O-2 range will switch the cart off.

17.14.3.13 12 - Rex Utility

Size	8KiB
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 0C 00 01 00 00 00 00 00 00  ...@............
0020: 52 45 58 00 00 00 00 00 00 00 00 00 00 00 00 00  REX.............
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 08 80 C1 FE C3 C2 CD 38 30 6C 95 E3 20 A3 FD 20  .......80l.. ..
```

Reading from $DF00-DFBF disables ROM, reading from $DFC0-DFFF enables ROM (8KiB game config).

17.14.3.14 13 - Final Cartridge I

Size	16KiB
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
```

```
      -------------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 0D 00 00 00 00 00 00 00 00  ...@............
0020: 54 68 65 20 46 69 6E 61 6C 20 43 61 72 74 72 69  The Final Cartri
0030: 64 67 65 20 49 00 00 00 00 00 00 00 00 00 00 00  dge I...........
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 80 BA 5E FE C3 C2 CD 38 30 00 A0 A0 20 2D FE 58  ..^....80... -.X
```

Any access to I/O-1 turns cartridge ROM off. Any access to I/O-2 turns cartridge ROM on.

The cart ROM is visible in I/O-1 and I/O-2.

17.14.3.15 14 - Magic Formel

Size	64KiB (8 banks of 8KiB)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load Address	$E000-FFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -------------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 0E 01 00 00 00 00 00 00 00  ...@............
0020: 4D 61 67 69 63 20 46 6F 72 6D 65 6C 00 00 00 00  Magic Formel....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 E0 00 20 00  CHIP.. ....... .
0050: 4D 46 30 8D 00 DF 60 8D 01 DF 60 8D 02 DF 60 8D  MF0...‘...‘...‘.
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 E0 00 20 00  CHIP.. ....... .
2060: 4C 5F E4 8D 00 DF 60 8D 01 DF 60 8D 02 DF 60 8D  L_....‘...‘...‘.
...
4060: 43 48 49 50 00 00 20 10 00 00 00 02 E0 00 20 00  CHIP.. ....... .
4070: 4D 46 32 8D 00 DF 60 8D 01 DF 60 8D 02 DF 60 8D  MF2...‘...‘...‘.
...
6070: 43 48 49 50 00 00 20 10 00 00 00 03 E0 00 20 00  CHIP.. ....... .
6080: 4D 46 33 8D 00 DF 60 8D 01 DF 60 8D 02 DF 60 8D  MF3...‘...‘...‘.
...
8080: 43 48 49 50 00 00 20 10 00 00 00 04 E0 00 20 00  CHIP.. ....... .
8090: 4D 46 34 8D 00 DF 60 8D 01 DF 60 8D 02 DF 60 8D  MF4...‘...‘...‘.
...
A090: 43 48 49 50 00 00 20 10 00 00 00 05 E0 00 20 00  CHIP.. ....... .
A0A0: 4D 46 35 8D 00 DF 60 8D 01 DF 60 8D 02 DF 60 8D  MF5...‘...‘...‘.
...
C0A0: 43 48 49 50 00 00 20 10 00 00 00 06 E0 00 20 00  CHIP.. ....... .
C0B0: 4D 46 36 8D 00 DF 60 8D 01 DF 60 8D 02 DF 60 8D  MF6...‘...‘...‘.
...
E0B0: 43 48 49 50 00 00 20 10 00 00 00 07 E0 00 20 00  CHIP.. ....... .
E0C0: 4D 46 37 8D 00 DF 60 8D 01 DF 60 8D 02 DF 60 8D  MF7...‘...‘...‘.
```

17.14.3.16 15 - C64 Game System, System 3

Size	512KiB (64 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF (all modules)

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
       -----------------------------------------------  ----------------
00000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
00010: 00 00 00 40 01 00 00 0F 00 01 00 00 00 00 00 00  ...@............
00020: 43 36 34 47 53 20 43 61 72 74 72 69 64 67 65 00  C64GS Cartridge.
00030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
00040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
00050: 6D 80 C5 80 C3 C2 CD 38 30 4C CB 80 4C 36 84 4C  m......80L..L6.L
...
02050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
02060: 18 D0 A9 FF 8D 15 D0 8D 1D D0 8D 17 D0 A2 07 A9  ................
...
04060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
04070: E0 08 19 21 77 84 52 98 9F 80 A5 21 31 01 31 89  ...!w.R....!1.1.
...
06070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP.. ....... .
06080: C0 08 1C 1D A0 92 03 03 D8 AA 04 C0 B8 01 40 EA  ..............@.
...
7E430: 43 48 49 50 00 00 20 10 00 00 00 3F 80 00 20 00  CHIP.. ....?.. .
7E440: 45 20 41 20 42 49 47 20 58 FE 4F 4E 20 54 48 49  E A BIG X.ON THI
```

Here is a list of the known cartridges:

```
    C64GS 4-in-1      (Commodore)  (512 KiB)
    Last Ninja Remix  (System 3)   (512 KiB)
    Myth              (System 3)   (512 KiB)
```

ROM memory is organized in 8KiB ($2000) banks located at $8000-$9FFF. Bank switching is done by accessing address $DE00+X, where X is the bank number (STA $DE00,X). For instance, to read from bank 3, address $DE03 is read from, or written to.

The CRT file contains a string of CHIP blocks, each block with a start address of $8000, length $2000 and the bank number in the bank field. In the cartridge header, EXROM ($18) is set to 0, GAME ($19) is set to 1 to enable the 8 KiB ROM configuration.

17.14.3.17 16 - Warp Speed

Size	16KiB
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 10 00 00 00 00 00 00 00 00  ...@............
```

```
0020: 57 61 72 70 73 70 65 65 64 00 00 00 00 00 00 00  Warpspeed.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 4C 22 80 4C 22 80 FF 43 42 4D 20 53 E4 20 18 E5  L".L"..CBM S. ..
```

After RESET or POWER ON, 16KiB of cartridge ROM is visible at $8000-$BFFF. Additionally, ROM normally located at $9E00-$9FFF is mirrored into I/O-1 and I/O-2 at $DE00-$DFFF. ROM at $8000-$BFFF is disabled by writing into the I/O-2 area (typically $DF00) and may be re-enabled by writing into I/O-1 ($DE00). However, the $DE00-$DFFF (I/O-1/I/O-2) area itself always remains mapped to cartridge ROM.

Note: the actual cartridge contains both a C64 and a C128 ROM, which can be selected with a switch. We implement them as if these two ROMs were different cartridges - this way they can be selected by using the other .crt file, instead of having to flip the switch in the user interface.

17.14.3.18 17 - Dinamic

Size	128KiB (16 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF (all modules)

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
       -----------------------------------------------  ----------------
00000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
00010: 00 00 00 40 01 00 00 11 00 01 00 00 00 00 00 00  ...@............
00020: 4E 61 72 63 6F 20 50 6F 6C 69 63 65 00 00 00 00  Narco Police....
00030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
00040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
00050: 0B 80 0B 80 C3 C2 CD 38 30 00 00 78 A2 FF 9A D8  .......80..x....
..
02050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
02060: 1C 8C 1B 8C 16 16 8F 16 16 88 1C 1C 86 1C 1C 89  ................
..
04060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
04070: B6 02 07 08 07 07 00 0A 0A B6 00 05 0A 00 07 07  ................
..
1E130: 43 48 49 50 00 00 20 10 00 00 00 0F 80 00 20 00  CHIP.. ....... .
1E140: 00 D5 70 03 F5 70 0F 5F 70 0F F7 70 35 FD F0 37  ..p..p._p..p5..7
```

Here is a list of the known DINAMIC cartridges:

```
Narco Police  (128 KiB)
Satan         (128 KiB)
```

ROM memory is organized in 8KiB ($2000) banks located at $8000-$9FFF. Bank switching is done by reading from address $DE00+X, where X is the bank number (LDA $DE00,X). For instance, to read from bank 3, address $DE03 is accessed.

The CRT file contains a string of CHIP blocks, each block with a start address of $8000, length $2000 and the bank number in the bank field. In the cartridge header, EXROM ($18) is set to 0, GAME ($19) is set to 1 to enable the 8 KiB ROM configuration.

17.14.3.19 18 - Zaxxon, Super Zaxxon (SEGA)

Size	20KiB (3 banks of different sizes)
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-8FFF (mirrored in $9000-9FFF, module 0, chip U1) $A000-BFFF (banked modules 1 and 2, chip U2)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 12 00 00 00 00 00 00 00 00  ...@............
0020: 5A 61 78 78 6F 6E 00 00 00 00 00 00 00 00 00 00  Zaxxon..........
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 10 10 00 00 00 00 80 00 10 00  CHIP............
0050: 0D 80 29 80 C3 C2 CD 38 30 78 4C 09 80 78 A9 00  ..)....80xL..x..
..
1050: 43 48 49 50 00 00 20 10 00 00 00 00 A0 00 20 00  CHIP.. ....... .
1060: A2 0F BD 00 20 D0 04 CA 10 F8 60 BD 70 20 F0 0D  .... .....‘.p ..
..
3060: 43 48 49 50 00 00 20 10 00 00 00 01 A0 00 20 00  CHIP.. ....... .
3070: 65 A2 36 A3 E7 A3 CB A4 94 A5 86 A6 5E A7 35 A8  e.6.........^.5.
```

The (Super) Zaxxon carts use a 4KiB ($1000) ROM at $8000-$8FFF (mirrored in $9000-$9FFF) along with two 8KiB ($2000) cartridge banks located at $A000-$BFFF. One of the two banks is selected by doing a read access to either the $8000-$8FFF area (bank 0 is selected) or to $9000-$9FFF area (bank 1 is selected). EXROM ($18 = $00) and GAME ($19 = $00) lines are always pulled to GND to select the 16 KiB ROM configuration.

The CRT file includes three CHIP blocks:

```
a) bank = 0, load address = $8000, size = $1000
b) bank = 0, load address = $A000, size = $2000
c) bank = 1, load address = $A000, size = $2000
```

17.14.3.20 19 - Magic Desk, Domark, HES Australia

Size	32KiB, 64KiB or 128KiB sizes (4 to 16 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	(banks 00-15) - $8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 13 00 01 00 00 00 00 00 00  ...@............
0020: 4D 61 67 69 63 20 44 65 73 6B 00 00 00 00 00 00  Magic Desk......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 C6 CA C3 C2 CD 38 30 8E 16 D0 20 A3 FD 20  .......80... ..
..
```

```
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: 00 3F 0A 01 00 86 4E 24 28 31 30 29 3A 4A 4F 59  .?....N$(10):JOY
..
4060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
4070: 00 8B C9 28 4E 24 2C 31 29 B3 B1 22 FF 22 A7 32  ...(N$,1)..".".2
..
6070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP.. ....... .
6080: AE 01 83 33 2C 37 2C 22 32 29 20 44 45 4C 20 4B  ...3,7,"2) DEL K
```

This cartridge type is very similar to the OCEAN cart type: ROM memory is organized in 8KiB ($2000) banks located at $8000-$9FFF. Bank switching is done by writing the bank number to $DE00. Deviant from the Ocean type, bit 7 is cleared for selecting one of the ROM banks. If bit 7 is set ($DE00 = $80), the GAME/EXROM lines are disabled, turning on RAM at $8000-$9FFF instead of ROM.

In the cartridge header, EXROM ($18) is set to 0, GAME ($19) is set to 1 to indicate the RESET/power-up configuration of 8 KiB ROM.

Here is a list of the known cartridges:

```
    Ghosbusters         (HES Australia)   (32 KiB)
    Magic Desk          (Commodore)       (32 KiB)
    Badlands            (Domark)          (64 KiB)
    Vindicators         (Domark)          (64 KiB)
    Wonderboy           (HES Australia)   (64 KiB)
    Cyberball           (Domark)         (128 KiB)
```

17.14.3.21 20 - Super Snapshot V5

Size	64KiB or 128KiB (4 or 8 banks of 16KiB each)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      ------------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 14 00 00 00 00 00 00 00 00  ...@............
0020: 53 75 70 65 72 20 53 6E 61 70 73 68 6F 74 20 35  Super Snapshot 5
0030: 20 4E 54 53 43 00 00 00 00 00 00 00 00 00 00 00   NTSC...........
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 09 80 59 80 C3 C2 CD 38 30 20 03 9F 00 FA F4 20  ..Y....80 .....
...
4050: 43 48 49 50 00 00 40 10 00 00 00 01 80 00 40 00  CHIP..@.......@.
4060: 79 DE BC FE C3 C2 CD 38 30 A9 05 8D 20 D0 8D 21  y......80... ..!
...
8060: 43 48 49 50 00 00 40 10 00 00 00 02 80 00 40 00  CHIP..@.......@.
8070: 50 DE BC FE C3 C2 CD 38 30 A9 0A 85 6A A9 0D 85  P......80...j...
...
C070: 43 48 49 50 00 00 40 10 00 00 00 03 80 00 40 00  CHIP..@.......@.
C080: 50 DE BC FE C3 C2 CD 38 30 85 07 20 1A AD A5 76  P......80.. ...v
```

The first page of the currently selected ROM bank is mirrored in the I/O-1 range when reading.

The control Register is the I/O-1 range when writing:

```
bit  meaning
---  -------
7-6  unused
5    ROM/RAM bank bit 2 (usually unused, for 128KiB ROM)
4    ROM/RAM bank bit 1
3    ROM enable
2    ROM/RAM bank bit 0
1    RAM enable, EXROM
0    release freeze, !GAME
```

17.14.3.22 21 - Comal-80

Size	64KiB (4 banks of 16KiB each)
EXROM	active (low) (0)
GAME	active (low) (0)
Load address	$8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 15 00 00 00 00 00 00 00 00  ...@............
0020: 43 6F 6D 61 6C 20 38 30 00 00 00 00 00 00 00 00  Comal 80........
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 87 87 70 CF C3 C2 CD 38 30 4C AA CF 4C 70 CF 4C  ..p....80L..Lp.L
...
4050: 43 48 49 50 00 00 40 10 00 00 00 01 80 00 40 00  CHIP..@.......@.
4060: AA CF 70 CF C3 C2 CD 38 30 01 29 01 28 01 2C 04  ..p....80.).(.,.
...
8060: 43 48 49 50 00 00 40 10 00 00 00 02 80 00 40 00  CHIP..@.......@.
8070: AA CF 70 CF C3 C2 CD 38 30 91 92 92 92 92 92 92  ..p....80.......
...
C070: 43 48 49 50 00 00 40 10 00 00 00 03 80 00 40 00  CHIP..@.......@.
C080: 7B C8 7E C8 C3 C2 CD 38 30 43 4F 4D 41 4C 80 93  {.~....80COMAL..
```

The Comal-80 Cartridge uses 16KiB banks ($4000) at $8000-$BFFF. There are 4 banks of ROM memory and the cart has 1 (write-only) bank control register which is located at $DE00 and mirrored throughout the $DE00-$DEFF range.

Two variants of this Cartridge existed:

1. Grey Cartridge (Comal Users Group, USA) Produced around 1984-1985, likely with UniComal Denmarks approval. Approx. 2,000 units distributed (according to COMAL Today).

- 4 sockets populated with 16 KB EPROMs (27128)
- NO additional socket, but it was possible to piggyback a fifth EPROM via minor hardware mod

```
bit  meaning
---  -------
7    -
6    GAME
5    EXROM
4    -
3    -
2    -
0-1  selects EPROM (16k bank)
```

2. Black Cartridge (Official Commodore Release) Used internationally and with a different hardware configuration.

- Two fixed 32 KB ROMs (Commodore) and one socket for a 32 KB (or with hardware modification 64 KB) EPROM

```
bit  meaning
---  -------
7    -
6    GAME + EXROM
5    -
4    -
3    -
2    switches to the optional third socket (additional 2x16k)
0-1  selects EPROM+bank (4x16k bank)
```

If the Cartridge Hardware Subtype/Revision (Offset 0x1A in the CRT Header) is 1, then VICE emulates the older "Grey" cartridge variant.

17.14.3.23 22 - Structured Basic

Size	16KiB (2 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 16 00 01 00 00 00 00 00 00  ...@............
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 0d 80 a2 80 c3 c2 cd 38 30 00 4c a0 84 a2 05 8e  .......80.L.....
0060: 16 d0 20 a3 fd 20 50 fd 20 5b ff 20 7e 80 58 20  .. .. P. [. ~.X
0070: 53 e4 20 bf e3 20 ed 83 8d f4 07 a5 2b a4 2c 20  S. .. ......+.,
0080: 08 a4 20 80 91 a9 00 85 14 a9 80 85 15 20 75 93  .. .......... u.
0090: a9 8f a0 8a 20 2d e4 a2 0b bd 7d 8a 9d 00 03 ca  .... -.........
...
4040: db 9f 8e 00 de ea 4c e2 9f 8e 00 de ea 4c e9 9f  ......L......L..
4050: 8d 00 de ea 4c 43 fe 8d 00 de ea 4c e2 fc 2d 2a  ....LC.....L..-*
```

Any read/write access to $DE00 or $DE01 will switch in bank 0. Any read/write access to $DE02 will switch in bank 1. Any read/write access to $DE03 will switch off EXROM.

17.14.3.24 23 - Ross

Size	16KiB or 32KiB sizes (1 or 2 banks of 16KiB each)
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 17 00 00 00 00 00 00 00 00  ...@............
0020: 52 6F 73 73 20 31 34 00 00 00 00 00 00 00 00 00  Ross 14.........
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 09 80 09 80 C3 C2 CD 38 30 A2 00 BD 20 80 4D 0E  .......80... .M.
...
4050: 43 48 49 50 00 00 40 10 00 00 00 01 80 00 40 00  CHIP..@.......@.
4060: 3F 5A 4D 4D 50 4D 8D 25 3F 1A 1F 77 3F CD E0 3F  ?ZMMPM.%?..w?..?
```

Any read access to $DE00 will switch in bank 1 (if cart is 32KiB). Any read access to $DF00 will switch off EXROM and GAME.

17.14.3.25 24 - Dela EP64

Size	8KiB to 72KiB sizes (1 to 9 banks of 8KiB each, or 1 bank of 8KiB and 1 or 2 banks of 32KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 18 00 01 00 00 00 00 00 00  ...@............
0020: 44 45 4C 41 20 45 50 36 34 00 00 00 00 00 00 00  DELA EP64.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 00 85 5E FE C3 C2 CD 38 30 FF FF FF FF FF FF FF  ..^....80.......
...
2050: 43 48 49 50 00 00 80 10 00 00 00 01 80 00 80 00  CHIP............
2060: 54 45 53 54 0D 2A 0D 54 45 20 36 34 0D 00 00 00  TEST.*.TE 64....
```

This is an eprom cartridge. It has 1 2764 (8KiB) which holds the base eprom with the base menu, and 2 27256 eproms of which 8KiB parts are banked into the $8000-9FFF area.

The bank selecting is done by writing to $DE00. The following bits are used for bank decoding in $DE00 (0 being the LSB, 3 being the MSB).

```
Bit# 76543210
     xx10xx32
```

Any bank value below 4 or above 11 switches in the base bank (bank 0).

The bit values for each eprom bank are :

```
eprom bank 1 : xx00xx01
eprom bank 2 : xx01xx01
eprom bank 3 : xx10xx01
eprom bank 4 : xx11xx01
eprom bank 5 : xx00xx10
eprom bank 6 : xx01xx10
eprom bank 7 : xx10xx10
eprom bank 8 : xx11xx10
```

Setting bit 7 high will switch off EXROM.

17.14.3.26 25 - Dela EP7x8

Size	8KiB to 64KiB sizes (1 to 8 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 19 00 01 00 00 00 00 00 00  ...@............
0020: 44 45 4C 41 20 45 50 37 78 38 00 00 00 00 00 00  DELA EP7x8......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 5E FE C3 C2 CD 38 30 78 A2 FF 9A D8 8E 16  ..^....80x......
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: 94 83 A0 83 C3 C2 CD 38 30 02 BB 5A 30 5F EE 3D  .......80..Z0_.=
```

This is an eprom cartridge. It has 8 8KiB banks of which the first holds the base menu, the other eproms can be banked into the $8000-9FFF area.

The bank selecting is done by writing to $DE00. Each low bit is used to bank in the respective eprom. If all bits are high then the EXROM is switched off.

The bit values for each eprom bank is:

```
eprom bank 1 : 11111110 ($FE) (base eprom)
eprom bank 2 : 11111101 ($FD)
eprom bank 3 : 11111011 ($FB)
eprom bank 4 : 11110111 ($F7)
eprom bank 5 : 11101111 ($EF)
eprom bank 6 : 11011111 ($DF)
eprom bank 7 : 10111111 ($BF)
eprom bank 8 : 01111111 ($7F)

EXROM off    : 11111111 ($FF)
```

17.14.3.27 26 - Dela EP256

Size	8KiB to 262KiB sizes (1 to 33 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 1A 00 01 00 00 00 00 00 00  ...@............
0020: 44 45 4C 41 20 45 50 32 35 36 00 00 00 00 00 00  DELA EP256......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 00 85 5E FE C3 C2 CD 38 30 93 0D 2B 2B 2B 20 45  ..^....80..+++ E
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: 09 80 28 80 C3 C2 CD 38 30 78 A2 05 8E 16 D0 20  ..(....80x.....
...
4060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
4070: 0B 80 BC FE C3 C2 CD 38 30 DC 10 8E 16 D0 20 87  .......80..... .
...
6070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP.. ....... .
6080: 09 80 F6 8E C3 C2 CD 38 30 A2 C8 8E 16 D0 20 ..  ?.?....80...?.?.
...
8080: 43 48 49 50 00 00 20 10 00 00 00 04 80 00 20 00  CHIP.. ....... .
8090: 94 83 A0 83 C3 C2 CD 38 30 02 BB 5A 30 5F EE 3D  .......80..Z0_.=
```

This is an eprom cartridge. It has 33 8KiB banks of which the first holds the base menu, the other eproms can be banked into the $8000-9FFF area.

The bank selecting is done by writing to $DE00.

The values for the (extra) eprom banks are:

```
eprom banks  1- 8 : $38-3F
eprom banks  9-16 : $28-2F
eprom banks 17-24 : $18-1F
eprom banks 25-32 : $08-0F
```

Setting bit 7 high will switch off EXROM.

17.14.3.28 27 - Rex EP256

Size	8KiB to 262KiB sizes (1 bank of 8KiB and 1 to 8 banks of either 8KiB, 16KiB or 32KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 1B 00 01 00 00 00 00 00 00  ...@............
```

```
0020: 52 45 58 20 45 50 32 35 36 00 00 00 00 00 00 00  REX EP256.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 C1 FE C3 C2 CD 38 30 20 A3 FD 20 50 FD 20  .......80 .. P.
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: 09 80 F2 8F C3 C2 CD 38 30 A2 C8 8E 16 D0 20 A3  .......80..... .
...
4060: 43 48 49 50 00 00 40 10 00 00 00 02 80 00 40 00  CHIP..@.......@.
4070: 09 80 09 80 C3 C2 CD 38 30 58 D8 20 84 FF 20 8A  .......80X. .. .
```

This is an eprom cartridge. It has 9 eprom sockets, of which the first holds the base eprom with the base menu which is an 8KiB eprom, the other eprom sockets can handle 8KiB, 16KiB or 32KiB eproms, of which 8KiB can be banked into the $8000-9FFF area.

The bank selecting is done by writing to $DFA0. Bits 2, 1 and 0 determine which socket is used and bits 5 and 4 are used to select an 8KiB piece of the eprom.

The possible values for bits 5 and 4 for the (extra) eprom banks are:

```
 8KiB        : 3, 2, 1, 0

16KiB bank 0 : 2, 0
16KiB bank 1 : 3, 1

32KiB bank 0 : 0
32KiB bank 1 : 1
32KiB bank 2 : 2
32KiB bank 3 : 3
```

Reading from $DFC0 switches off the EXROM. Reading from $DFE0 switches on the EXROM.

17.14.3.29 28 - Mikro Assembler

Size	8KiB (1 bank of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 1C 00 01 00 00 00 00 00 00  ...@............
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 60 80 FE 80 C3 C2 CD 38 30 4C 07 87 4C CA 82 41  `......80L..L..A
```

The $9E00-$9EFF range is mirrored at $DE00-$DEFF. The $9F00-$9FFF range is mirrored at $DF00-$DFFF.

17.14.3.30 29 - Final Cartridge Plus

Size	32KiB (1 bank of 32KiB)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$0000-$7FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 1D 01 00 00 00 00 00 00 00  ...@............
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 80 10 00 00 00 00 00 00 80 00  CHIP............
0050: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF  ................
```

This cart has 32KiB of ROM, bank 0 is in the cart image but is unused. The first 8KiB of the cart image is unused, the second 8KiB of the cart image is mapped to $E000-$FFFF, the third 8KiB of the cart image is mapped to $8000-$9FFF and the fourth 8KiB of the cart image is mapped to $A000-$BFFF. An NMI can be triggered by the cart, if address $0001 is written to and the cartridge is enabled. The cart can be disabled by software, by clearing bit 4 when writing to $DF00-$DFFF. Cart ROM at $E000-$FFFF can be disabled by setting bit 5 to 0 when writing to $DF00-$DFFF. Cart ROM at $8000-$BFFF can be disabled by setting bit 6 to 1 when writing to $DF00-$DFFF. Bit 7 of a byte written to $DF00-$DFFF can be read back from the cartridge if enabled (like a memory cell).

17.14.3.31 30 - Action Replay 4

Size	32KiB (4 banks of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 1E 00 01 00 00 00 00 00 00  ...@............
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: EA 78 48 A9 7F 8D 0D DD D0 0E 48 AD 0D DD 10 04  .xH.......H.....
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: 09 80 0C 80 C3 C2 CD 38 30 4C E9 80 4C 81 81 4C  .......80L..L..L
...
4060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
4070: 09 80 0E 80 C3 C2 CD 38 30 A2 00 4C EF FC 20 BC  .......80..L.. .
...
6070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP.. ....... .
6080: 09 80 0C 80 C3 C2 CD 38 30 4C 70 88 4C 3F 80 4C  .......80Lp.L?.L
```

The control register is the I/O-1 range:

```
bit  meaning
---  -------
0    Eprom banking bit 0 (bank address 13)
1    Controls the GAME line (0 sets GAME low, 1 sets GAME high)
2    Freeze-end bit (disables the register and hides any rom bank)
3    Controls the Exrom line (1 sets EXROM low, 0 sets EXROM high)
4    Eprom banking bit 1 (bank address 14)
5-7  Unused
```

The first page of the currently banked ROM block can be read in the I/O-2 range.

17.14.3.32 31 - Stardos

Size	16KiB (2 banks of 8KiB)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$8000-$9FFF (bank 0), $E000-$FFFF (bank 1)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 1F 01 00 00 00 00 00 00 00  ...@............
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: F9 80 B6 80 C3 C2 CD 38 30 FD 80 89 80 4C 0C 88  .......80....L..
...
2050: 43 48 49 50 00 00 20 10 00 00 00 00 E0 00 20 00  CHIP.. ....... .
2060: 85 56 20 0F BC A5 61 C9 88 90 03 20 D4 BA 20 CC  .V ...a.... .. .
```

This cart has 16KiB of ROM, of which the first 8KiB is mapped in at $8000-$9FFF and the second 8KiB is used as a kernel replacement. The kernel replacement is achieved by a clip that needs to be installed inside the C64.

Reading from I/O-1 causes a capacitor to get charged with every read, once the capacitor is charged enough it switches the cart on.

Reading from I/O-2 causes a different capacitor to get charged with every read, once the capacitor is charged enough it switched the cart off.

17.14.3.33 32 - EasyFlash

Size	1024KiB (64 banks of 2 * 8KiB)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$8000-$9FFF (ROML), $A000-$BFFF or $E000-$FFFF (ROMH)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 20 01 00 00 00 00 00 00 00  ...@... ........
0020: 45 61 73 79 46 6C 61 73 68 20 43 61 72 74 72 69  EasyFlash Cartri
```

```
0030: 64 67 65 00 00 00 00 00 00 00 00 00 00 00 00 00  dge.............
0040: 43 48 49 50 00 00 20 10 00 02 00 01 80 00 20 00  CHIP.. ....... .
0050: 00 85 5E FE C3 C2 CD 38 30 93 0D 2B 2B 2B 20 45  ..^....80..+++ E
```

EasyFlash is a 1 MiB Flash EPROM card with multiple configurations and banks possible, it also has 256 bytes of RAM which is mapped into the I/O-2 range.

There are two control registers, one at $DE00 and one at $DE02.

The register at $DE00 controls which bank is mapped into ROMH and ROML.

The register at $DE02 does the following:

```
bit  meaning
---  -------
7    LED control
6-3  Unused
2    Mode (0/1)
1    Exrom line control
0    Game line control
```

17.14.3.34 33 - EasyFlash Xbank

Size	-
EXROM	-
GAME	-
Load address	-

This CRT type is not actually related to a seperate hardware, it is used by some EasyFlash related tools as a container format. Consequently VICE does (can) not load files of this type.

17.14.3.35 34 - Capture

Size	8KiB (1 bank of 8KiB)
EXROM	inactive (hi) (1)
GAME	inactive (hi) (1)
Load address	$E000-$FFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 22 00 00 00 00 00 00 00 00  ...@..."........
0020: 4D 61 67 69 63 20 46 6F 72 6D 65 6C 00 00 00 00  Magic Formel....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 01 E0 00 20 00  CHIP.. ....... .
0050: 00 0A 0D 8A B4 A1 20 80 00 0A 82 8A 8D 20 9E 20  ...... ...... .
```

This cart has 8KiB of ROM which is mapped to $E000, and 8KiB of RAM which is mapped to $6000. The cartridge is disabled after a reset.

When the freeze button is pressed the following happens:

- an NMI is generated
- as soon as the current adress is in bank 0xfe the cart switches to ultimax mode. The cart ROM then contains one page full of "jmp $eaea", which ultimately calls the freezer code.

- the $FFF7/$FFF8 "register" logic is enabled and any access (read or write) to $FFF7 will turn the cart_enabled off (leave ultimax mode), and an access to $FFF8 will turn the cart back on (enter ultimax mode). the "register logic" that causes this can only be disabled again by a hardware reset.

17.14.3.36 35 - Action Replay 3

Size	16KiB (2 banks of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 23 00 01 00 00 00 00 00 00  ...@...#........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: EA A9 E3 48 A9 7B 48 08 4C 1A 80 EA EA EA 48 AD  ...H.{H.L.....H.
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: 09 80 5E FE C3 C2 CD 38 30 78 A2 FB D8 9A A9 27  ..^....80x.....'
```

This cart has 16KiB of ROM of which 8KiB is mapped in at both ROML and ROMH. Bank switching and control register is done through the I/O-1 range:

```
bit  meaning
---  -------
7-4  unused
3    Exrom line control
2    Disable cart
1    Unused
0    Bank
```

17.14.3.37 36 - Retro Replay

Size	32KiB, 64KiB or 128KiB (4, 8 or 16 banks of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 24 00 01 00 00 00 00 00 00  ...@...$........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 02 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 0C 80 C3 C2 CD 38 30 4C 7F 81 4C 87 81 4C  .......80L..L..L
```

Note: When the Cartridge Hardware Subtype/Revision (Offset 0x1A in the CRT Header) is 1, then the image utilizes features only present in the "Nordic Replay" (see description of Nordic/Atomic Power)

The Retro Replay has three registers: Two write-only ($DE00 & $DE01) and one read-only register ($DE00 & $DE01 giving the same results).

The register at $DE00 is reset to $00 on a hard reset if not in flash mode. If in flash mode, it is set to $02 in order to prevent the computer from starting the normal cartridge. Flash mode is selected with a jumper.

Register at $DE00:

```
bit  meaning
---  -------
0    Controls the GAME line: A 1 asserts the line, a 0  will  deassert
     it.
1    Controls the EXROM line: A 0 will assert it, a  1  will  deassert
     it.
2    Writing a 1 will disable further write accesses to all  registers
     of the Retro Replay, and  set  the  memory  map  of  the  C64  to
     standard, as if there is no cartridge installed at all.
3    Controls bank-address 13 for ROM and RAM banking.
4    Controls bank-address 14 for ROM and RAM banking.
5    Switches between ROM and RAM: 0=ROM, 1=RAM
6    Must be written once to "1" after a successful freeze in order to
     set the correct memory map and  enable  bits  0  and  1  of  this
     register. Otherwise no effect.
7    Controls bank-address 15 for ROM banking.
```

The register at $DE01 is the extended control register. If not in Flash mode, bits 1, 2 and 6 can only be written once. If in Flash mode, the REUcomp bit cannot be set, but the register will not be disabled by the first write. Bit 5 is always set to 0 if not in Flash mode.

Register at $DE01:

```
bit  meaning
---  -------
0    Enable clockport connector.
1    AllowBank (1 allows banking of RAM in $DF00/$DE02 area)
2    NoFreeze (1 disables Freeze function)
3    Bank-address 13 for RAM and ROM (mirror of $DE00)
4    Bank-address 14 for RAM and ROM (mirror of $DE00)
5    Bank-address 16 for ROM (only in flash mode)
6    REU compatibility bit. 0=standard memory map, 1 = REU  compatible
     memory map
7    Bank-address 15 for ROM (mirror of $DE00)
```

Reading from the registers at either $DE00 or $DE01 will return the content of the status register.

Status register:

```
bit  meaning
---  -------
```

```
0    1=Flashmode active (jumper set)
1    feedback of AllowBank bit
2    1=Freeze button pressed
3    feedback of banking bit 13
4    feedback of banking bit 14
5    feedback of banking bit 16
6    1=REU compatible memory map active
7    feedback of banking bit 15
```

The following memory maps are available:

- standard - $DE00 and $DE01 registers are active, $DF00-$DFFF contain the last page of the selected 8KiB bank of either ROM or RAM, whatever is selected. RAM can only be accessed in $8000-$9FFF. ROM can be mapped to $8000, $A000 or $E000 with the corresponding status on GAME and EXROM.

  Note: If the AllowBank bit is not set, the $DF00-$DFFF area will always access bank 0 of the RAM, so the older cartridge images will work. The AllowBank bit does not have any effect on the ROM mirror in that area.

- Freeze - ROM is mapped to $E000-$FFFF, bank 0 is active directly after Freeze. Writing to bits 0 and 1 of the $DE00 register will have no effect on GAME and EXROM. RAM can be selected and used in $DF00 or $DE02, respectively, but not in $8000. Banking bits work, so you have full read access to the ROM, and access to up to four RAM pages with the AllowBank bit set (minus 2 bytes if REU compatible bit is set). You should leave this memory map ASAP by setting bit 6 of $DE00, because C64 RAM in the $E000 area is not available, and you don't have control of the GAME and EXROM lines.

- REU compatible - $DE00 and $DE01 registers are active, $DE02-$DEFF contain a mirror of $9E02-$9EFF of the selected 8KiB-bank of either ROM or RAM, whatever is selected. RAM can only be accessed in $8000-$9FFF. ROM can be mapped to $8000, $A000 or $E000 with the corresponding status on GAME and EXROM. The $DF00 stays free for use with the 1764 Ram Expansion Unit (REU).

  Note: If the AllowBank bit is not set, the $DE02-$DEFF area will always access bank 0 of the RAM, so the older cartridge images will work. The AllowBank bit does not have any effect on the ROM mirror in that area.

17.14.3.38 37 - MMC64

Size	8KiB (1 bank of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 25 00 01 00 00 00 00 00 00  ...@...%........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 02 00 00 80 00 20 00  CHIP.. ....... .
```

```
0050: 09 80 64 97 C3 C2 CD 38 30 78 D8 A2 FF 9A 20 D4  ..d....80x.... .
```

The clockport registers of this cart can be switched to either $DE01-$DE0F or $DF21-$DF2F. The control registers are available at $DF10-$DF13.

The register at $DE01 / $DF21 is write only:

```
bit  meaning
---  -------
7-1  Unused
0    0 = disable clock port, 1 = enable clockport
```

The registers at $DE02-$DE0F / $DF22-$DF2F are for the clock port and are read/write.

The register at $DF10 is the MMC64 SPI transfer register, a byte written to this registers is sent to the card & response from the card is read here.

The register at $DF11 is the MMC64 control register:

```
bit  meaning
---  -------
0    0 = MMC64 BIOS active, 1 = external ROM active
1    0 = card selected, 1 = card not selected
2    0 = 250khz transfer, 1 = 8mhz transfer
3    0 = clock port @ $DE00, 1 = clock port @ $DF20
4    0 = normal Operation, 1 = flash mode (*)
5    0 = allow external rom when BIOS is disabled,
     1 = disable external ROM
6    0 = SPI write trigger mode, 1 = SPI read trigger mode
7    0 = MMC64 is active, 1 = MMC64 is completely disabled (**)
```

(*) bit can only be programmed when flash jumper is set (**) bit can only be modified after unlocking

The register at $DF12 is the MMC64 status register, which is read-only:

```
bit  meaning
---  -------
0    0 = SPI ready, 1 = SPI busy
1    external GAME line
2    external EXROM line
3    0 = card inserted, 1 = no card inserted
4    0 = card write enabled, 1 = card write disabled
5    0 = flash jumper not set, 1 = flash jumper set
6-7  unused
```

The register at $DF13 is the MMC64 identification register, which when reading from it can have the following values:

$64 when bit 1 of $DF11 is 0. $01 when bit 1 of $DF11 is 1 and REV A hardware is used. $02 when bit 1 of $DF11 is 1 and REV B hardware is used.

when writing to it it can be used to unlock bit 7 of $DF11 or to re-enable the cart:

Write $55 & $AA into this register to unlock bit 7 of $DF11. Write $0A & $1C into this register to re-enable MMC64 hardware.

17.14.3.39 38 - MMC Replay

Size	64KiB or 512KiB (8 or 64 banks of 8KiB)
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 26 00 00 00 00 00 00 00 00  ...@...&........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 02 00 00 80 00 20 00  CHIP.. ....... .
0050: 1A 80 6E 9E C3 C2 CD 38 30 4D 4D 43 52 45 50 4C  ..n....80MMCREPL
```

The cart uses the following registers:

$DE00 - RR control register write

```
bit  meaning
---  -------
0    GAME line
1    EXROM line
2    1 = disable RR, bit can be reset by setting bit 6 of $DF12
3    bank address 13
4    bank address 14
5    0 = rom enable, 1 = ram enable
6    1 = exit freeze mode
7    bank address 15
```

$DE01 - extended RR control register write

```
bit  meaning
---  -------
0    0 = disable clockport, 1 = enable clockport
1    0 = disable I/O RAM banking, 1 = enable I/O RAM banking
2    0 = enable freeze, 1 = disable freeze
3    bank address 13 (mirror of $DE00)
4    bank address 14 (mirror of $DE00)
5    0 = enable MMC registers, 1 = disable MMC registers. Can only  be
     written when bit 6 of $DF12 is 1. Register becomes effective when
     bit 0 of $DF11 is 1.
6    0 = RAM/ROM at $DFxx, 1 = RAM/ROM at $DExx
7    bank address 15 (mirror of $DE00)
```

$DE02-$DE0F - Clockport memory area (when enabled)

$DF10 - MMC SPI transfer register, a byte written is sent to the card & response from the card is read here.

$DF11 - MMC control register

```
bit  meaning
---  -------
```

```
0    0 = MMC BIOS enabled, 1 = MMC BIOS disabled.  Enabling  MMC  BIOS
     sets ROM banking to the last 64KiB bank.
1    0 = card selected, 1 = card not selected. This bit also  controls
     the green activity LED.
2    0 = 250khz transfer, 1 = 8mhz transfer
3    ALWAYS 0
4    ALWAYS 0
5    (in RR-Mode:)
      0 = allow RR rom when MMC BIOS disabled , 1 = disable RR ROM
      (in mmcreplay bios mode:)
      RAM banking (0 = $E000 - $FFFF, 1 = $8000 - $9FFF)
      (in 16KiB mode:)
      enable RAM at $A000 - $BFFF
6    0 = SPI write trigger mode, 1 = SPI read trigger mode
7    ALWAYS 0
```

$DF12 - MMC status register

```
bit  meaning
---  -------
0    0 = SPI ready, 1 = SPI busy (read)
                  1 = forbid ROM  write  accesses  (write).  Setting
                  this bit will disable writes  to  ROM  until  next
                  reset
1  feedback of $DE00 bit 0 (GAME)
2  feedback of $DE00 bit 1 (EXROM)
3  0 = card inserted, 1 = no card inserted
4  0 = card write enabled, 1 = card write disabled
5  EEPROM DATA line /  DDR  register.  Setting  DATA  to  "1"  enables
   reading data bit from EEPROM at this position.
6  0 = RR compatibility mode, 1 = Extended mode
   Selecting RR compatibility mode limits RAM  to  32KiB  and  disables
   writes  to  extended  banking  register.  Selecting  Extended  mode
   enables full RAM banking and enables Nordic Power mode in RR mode.
7  EEPROM CLK line
```

$DF13 - Extended banking register Can only be read/written to when bit 6 of $DF12 is 1

```
bit  meaning
---  -------
0    bank address 16
1    bank address 17
2    bank address 18
3    ALWAYS 0
4    ALWAYS 0
5    16KiB rom mapping
6    1 = enable  RR  register.  Disabling  RR  register  disables  ALL
     ROM/RAM banking too.
7    ALWAYS 0
```

NOTE THAT THE MMC REPLAY EMULATION IS CURRENTLY INCOMPLETE AND CONSIDERED BROKEN.

17.14.3.40 39 - IDE64

Size	64KiB or 128KiB (4 or 8 banks of 16KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 27 00 01 00 00 00 00 00 00  ...@...'........
0020: 49 44 45 36 34 20 43 41 52 54 52 49 44 47 45 20  IDE64 CARTRIDGE
0030: 49 44 45 44 4f 53 20 32 30 31 33 31 32 31 32 00  IDEDOS 20131212?
0040: 43 48 49 50 00 00 40 10 00 02 00 00 80 00 40 00  CHIP..@.......@.
0050: 63 80 5E FE C3 C2 CD 38 30 20 49 44 45 36 34 20  c.^....80 IDE64
```

The IDE64 cart uses the following registers:

```
$DE20 - $DE2F IDE BUS Registers
$DE30 - Low Data HDD register
$DE31 - High Data HDD register
```

$DE32 register:

```
bit  meaning
---  -------
7    unused (0)
6    unused (0)
5    unused (0)
4    version number (1)
3    romaddr15
2    romaddr14
1    game
0    exrom
```

```
$DE32 - $DE35 = IDE64 ROM bank select registers
$DE5F         = RTC access (bit 0 only to serial accessed RTC)
$DE60 - $DEFF = ROM used by software
$DEFB         = IDE64 clock reset, kill the cartridge
$DEFC - $DEFF = IDE64 memory configuration registers
```

17.14.3.41 40 - Super Snapshot V4

Size	32KiB (2 banks of 16KiB)
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-$BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
```

```
0010: 00 00 00 40 01 00 00 28 00 00 00 00 00 00 00 00  ...@...(........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP.. ....... .
0050: 80 AD B5 80 C3 C2 CD 38 30 08 48 A9 06 8D 00 DF  .......80.H.....
...
2050: 43 48 49 50 00 00 40 10 00 00 00 01 80 00 40 00  CHIP.. ....... .
2060: 13 80 BC FE C3 C2 CD 38 30 08 48 A9 02 8D 00 DF  .......80.H.....
```

This cart has 32KiB of ROM and 8KiB of RAM, it uses I/O-1 as a mirror of the last page of cart RAM. It has the following registers in the I/O-2 range:

ROM config register at $DF00 (can only be written to):

```
bit  meaning
---  -------
0    ?
1    ? (write 1 to release freeze mode)
2    ROM bank select
3    write 1 to disable cartridge
4-6  unused
7    ?
```

Note: if bit0, bit1, bit7 are all 0, then ultimax mapping is selected and RAM is enabled at ROML, otherwise if bit 0 is 0, then 16KiB mapping is enabled, or if bit 0 is 1, then 8KiB mapping is enabled.

RAM config register at $DF01 (read/write):

If written value == last value - 1, then ultimax mapping is selected and RAM is enabled at ROML, if written value == last value + 1, then ROM is enabled at ROML and exrom is deasserted (switch to either 8KiB or 16KiB mapping)

$DF02-$DFFF holds the last page of the first 8KiB of the current bank.

17.14.3.42 41 - IEEE-488

Size	4KiB (1 bank of 4KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$8FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 29 00 01 00 00 00 00 00 00  ...@...)........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 10 10 00 00 00 00 80 00 10 00  CHIP............
0050: 09 80 7A 80 C3 C2 CD 38 30 8E 16 D0 20 84 FF 20  ..z....80... ..
```

The cart uses a TPI for the IEEE488 interface/communication in the I/O-2 range:

```
$DF00 - Port A Data
$DF01 - Port B Data
```

```
$DF02 - Port C Data
$DF03 - Port A Direction
$DF04 - Port B Direction
$DF05 - Port C Direction
$DF06 - Control register
$DF07 - Active Interrupt register
```

17.14.3.43 42 - Game Killer

Size	8KiB (1 bank of 8KiB)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$E000-$FFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 2A 01 00 00 00 00 00 00 00  ...@...*........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 E0 00 20 00  CHIP.. ....... .
0050: 00 25 08 CF 07 9E 32 30 38 38 20 4D 43 2E 43 52  .%....2088 MC.CR
```

When the cartridge is active, ultimax is enabled when the address being accessed is is the $E000-$FFFF range, so the ROM is visible at $E000, below is normal C64 RAM. The cart can be disabled by writing to either I/O-1 or I/O-2 range. When the freezer button is pressed, the cartridge will be enabled and an NMI will be triggered.

17.14.3.44 43 - Prophet64

Size	256KiB (32 banks of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 2B 00 01 00 00 00 00 00 00  ...@...+........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 09 80 C3 C2 CD 38 30 78 A0 00 84 F8 84 FA  .......80x......
```

The control register is the I/O-2 range:

```
bit  meaning
---  -------
7-6  unused
5    disable cart
4-0  bank select
```

17.14.3.45 44 - EXOS

Size	8KiB (1 bank of 8KiB)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$E000-$FFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 2C 01 00 00 00 00 00 00 00  ...@...,........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 E0 00 20 00  CHIP.. ....... .
0050: 85 56 20 0F BC A5 61 C9 88 90 03 20 D4 BA 20 CC  .V ...a.... .. .
```

This cart has 8KiB of ROM, mapped in at $E000-$FFFF only when hirom is selected. The cart uses a clip that needs to be installed inside the C64.

17.14.3.46 45 - Freeze Frame

Size	8KiB (1 bank of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 2D 00 01 00 00 00 00 00 00  ...@...-........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 10 80 10 80 C3 C2 CD 38 30 20 00 00 00 00 00 00  .......80 ......
```

When reading from the I/O-1 range the cart is enabled, when reading from the I/O-2 range the cart is disabled. When the freeze button is pressed the ROM is mapped to both $8000-$9FFF and $E000-$FFFF.

17.14.3.47 46 - Freeze Machine

Size	16KiB or 32KiB (1 or 2 banks of 16KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 2E 00 01 00 00 00 00 00 00  ...@............
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP.. ....... .
```

```
0050: 3A 83 60 80 C3 C2 CD 38 30 20 00 00 40 00 00 00  :.‘....80 ..@...
...
2050: 43 48 49 50 00 00 40 10 00 00 00 01 80 00 40 00  CHIP.. ....... .
2060: 3A 83 60 80 C3 C2 CD 38 30 20 00 00 40 00 00 00  :.‘....80 ..@...
```

Warning, the following information is based on guess-work and might be incorrect, any further information and/or corrections are appreciated.

When the cart starts, the first half of the active ROM is banked to ROML ($8000-$9fff). When reading from the I/O-1 range the second half of the active ROM bank is enabled in RMH ($A000-$BFFF). When reading from the I/O-2 range the cart is disabled.

The 32k variant swaps the 16k bank with a second one when a reset happens.

When a freeze happens the first half of the ROM is mapped to ROML ($8000-$9FFF) and the second half to ROMH ($E000-$FFFF).

17.14.3.48 47 - Snapshot 64

Size	4KiB (1 bank of 4KiB)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$E000-$EFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 2F 01 00 00 00 00 00 00 00  ...@.../........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 10 10 00 00 00 00 E0 00 10 00  CHIP............
0050: 78 D8 48 8A 48 98 48 AC 0D DD 10 03 4C EE F2 AD  x.H.H.H.....L...
```

Warning, the following information is based on guess-work and might be incorrect, any further information and/or corrections are appreciated.

The cart has a control bit (bit 0) in the I/O-2 range which is used to disable or enable the cart.

17.14.3.49 48 - Super Explode V5.0

Size	16KiB (2 banks of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 30 00 01 00 00 00 00 00 00  ...@...0........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: D7 86 5E FE C3 C2 CD 38 30 A9 00 2C A9 FF 85 FE  ..^....80..,....
...
```

```
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: E8 96 5E FE C3 C2 CD 38 30 20 6C 81 A9 09 8D 99  ..^....80 l.....
```

Warning, the following information is based on guess-work and might be incorrect, any further information and/or corrections are appreciated.

The cart has 16KiB of ROM which are used as two banks of 8KiB, they are mapped into $8000-$9FFF and the last page of the current ROM bank is mirrored in $DF00-$DFFF. The cart has a control bit (bit 7) at $DF00, which is used to select what ROM bank is used.

17.14.3.50 49 - Magic Voice

Size	16KiB
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$8000-$BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 31 01 00 00 00 00 00 00 00  ...@...1........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: EA 2C 80 DF 50 FB A0 00 8C 80 DF B9 E3 A3 29 0F  .,..P.........).
...
2050: 43 48 49 50 00 00 40 10 00 00 00 00 A0 00 40 00  CHIP.. ....... .
2060: 4A EB C0 49 6A EA BB FB 4E CA 43 1E 75 63 15 97  J..Ij...N.C.uc..
```

This cart has 16KiB of ROM, mapped in at reset at $8000-$BFFF. The cart is controled through a TPI at $DF80-$DF87:

```
$DF80 - Port A Data
$DF81 - Port B Data
$DF82 - Port C Data
$DF83 - Port A Direction
$DF84 - Port B Direction
$DF85 - Port C Direction
$DF86 - Control register
$DF87 - Active Interrupt register
```

The cart has a pass-through port and does the following at start-up:

- Program starts after reset at $FFD3, and copies code from $FF36-$FFD2 to $0200-$029C (157 bytes)
- Program continues at $021A, copies $A000-$BFFF from EPROM to RAM at $A000-$BFFF (8KiB), copies $E000-$FFFF from EPROM to RAM at $E000-$FFFF (8KiB), copies $AE62-$B461 from RAM to RAM at $C000-$C5FF (Magic Voice Code)
- Jump to beginning of Magic Voice code at $C000

17.14.3.51 50 - Action Replay 2

Size	16KiB (2 banks of 8KiB)
EXROM	active (lo) (0)

GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 32 00 01 00 00 00 00 00 00  ...@...2........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: EA EA 68 AA 68 85 94 68 85 95 68 85 96 68 85 97  ..h.h..h..h..h..
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: 30 80 5E FE C3 C2 CD 38 30 20 04 90 4C 38 DF 1A  0.^....80 ..L8..
```

Warning, the following information is based on guess-work and might be incorrect, any further information and/or corrections are appreciated.

I/O-1 is somehow used to enable the cart, the exact way in which this is done is unknown. Reading from the I/O-2 range will give you the last page of the current ROM bank, and writing to it will disable the cart.

17.14.3.52 51 - MACH 5

Size	4KiB or 8KiB (1 bank of 4KiB or 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	$8000-$8FFF (4KiB), $8000-$9FFF (8KiB)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 33 00 01 00 00 00 00 00 00  ...@...3........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: AF 83 5E FE C3 C2 CD 38 30 4D 41 43 48 35 A5 93  ..^....80MACH5..
```

This cart has 8KiB ROM mapped at $8000-$9FFF, the $9E00-$9EFF range is mirrored at $DE00-$DEFF and the $9F00-$9FFF range is mirrored at $DF00-$DFFF.

17.14.3.53 52 - Diashow maker

Size	8KiB (1 bank of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
```

```
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 34 00 01 00 00 00 00 00 00  ...@...4........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 09 80 C3 C2 CD 38 30 AD 11 D0 29 10 D0 62  .......80...)..b
```

Accessing I/O-1 (the software uses $DE00 only it seems) disables cartridge ROM. A reset enables 8KiB game mode and the ROM bank is mapped to $8000. A freeze causes ROM to be mapped to $8000.

17.14.3.54 53 - Pagefox

Size	64KiB (4 banks of 16KiB)
EXROM	active (lo) (0)
GAME	active (lo) (0)
Startup mode	16KiB Game
Load address	$8000-$BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 35 00 00 00 00 00 00 00 00  ...@...5........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 31 80 BB 0E C3 C2 CD 38 30 50 46 20 56 31 2E 30  1......80PF V1.0
...
4050: 43 48 49 50 00 00 40 10 00 00 00 01 80 00 40 00  CHIP..@.......@.
4060: A2 FE 9A 20 EC AE 20 82 80 20 74 86 20 A5 8B 4C  ... .. .. t. ..L
...
8060: 43 48 49 50 00 00 40 10 00 00 00 02 80 00 40 00  CHIP..@.......@.
8070: 5A 01 02 03 04 06 0A 0B 10 14 1E 28 3C 00 00 00  Z.........(<...
...
C070: 43 48 49 50 00 00 40 10 00 00 00 03 80 00 40 00  CHIP..@.......@.
C080: 1E 03 14 82 09 05 09 0F 0C 0D 0F 05 09 09 0B 0A  ................
```

This cart has 64KiB ROM (2 32KiB Eproms, mapped to $8000 and $A000 in 16KiB Game Mode), and 32KiB RAM (mapped to $8000 and $A000 in 16KiB Game Mode). The cart has 1 (write-only) bank control register which is located at $DE80 and mirrored throughout the $DE80-$DEFF range:

```
Bit 0: unused/don't care
Bit 1: Bank select: 0=upper, 1=lower (not correct ?!)
Bit 2: chip select 0
Bit 3: chip select 1
Bit 4: cartridge enable/disable: 0=enable, 1=disable
Bits 5-7: unused/don't care
```

Chip select combinations of 0/1 are:

```
00: Eprom "79"
```

```
01: Eprom "ZS3"
10: Ram
11: empty space (reading returns VIC-II data)
```

note: on the original hardware "disabling" the cartridge by setting bit 4 of the control register does NOT prevent write accesses to the cartridge RAM!. So to actually disable the RAM, it is suggested to write $FF to the register.

17.14.3.55 54 - Kingsoft

Size	24KiB (3 banks of 8KiB)
EXROM	active (lo) (0)
GAME	active (lo) (0)
Startup mode	16KiB Game
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 36 00 00 00 00 00 00 00 00  ...@...6........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 A2 89 C3 C2 CD 38 30 20 D3 83 78 8D 00 DE  .......80 ..x...
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: 72 A2 05 A5 45 10 01 CA A5 46 10 02 CA CA 86 28  r...E....F.....(
...
4060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
4070: 78 A2 FF 9A D8 A9 08 8D 16 D0 A0 00 98 99 02 00  x...............
```

This cart has 24KiB ROM (3 8KiB Eproms)

```
reading io1:
- switches to 16KiB game mode
- first eprom mapped to 8000 (ROML)
- second eprom mapped to A000 (ROMH)

writing io1:
- switches to ultimax mode _only_:
  - if 0xc000 > address >= 0x8000
  - if address >= 0xe000
  (meaning 0x0000-0x7fff and 0xc000-0xdfff gives normal c64 ram/io)
- first eprom mapped to 8000 (ROML)
- third eprom mapped to e000 (ROMH)
```

17.14.3.56 55 - Silverrock 128

Size	128KiB (16 banks of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game

Load address $8000-$9FFF

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
       -----------------------------------------------  ----------------
00000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
00010: 00 00 00 40 01 00 00 37 00 01 00 00 00 00 00 00  ...@...7........
00020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
00030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
00040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
00050: 09 80 09 80 C3 C2 CD 38 30 78 A2 FF 9A D8 A9 00  .......80x......
...
02050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
02060: 00 21 1D A9 90 1B 67 70 FD B0 04 C3 19 B9 11 2D  .!....gp.......-
...
04060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
04070: 00 41 62 92 AD 71 32 87 08 20 BE 90 4C 36 8F 20  .Ab..q2.. ..L6.
...
06070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP.. ....... .
06080: 00 61 00 02 02 03 0D 40 82 15 D0 A1 08 40 84 00  .a.....@.....@..
...
08080: 43 48 49 50 00 00 20 10 00 00 00 04 80 00 20 00  CHIP.. ....... .
08090: 00 81 8A 85 0D F0 8A 28 83 F8 8A 00 8B 58 83 60  .......(.....X.‘
...
0A090: 43 48 49 50 00 00 20 10 00 00 00 05 80 00 20 00  CHIP.. ....... .
0A0A0: 00 A1 C2 E3 C4 86 32 14 00 C5 40 EA 13 CA CB CC  ......2...@.....
...
0C0A0: 43 48 49 50 00 00 20 10 00 00 00 06 80 00 20 00  CHIP.. ....... .
0C0B0: 00 C1 81 59 60 00 81 5D D9 58 5E EE 58 6E 3C 28  ...Y‘..].X^.Xn<(
...
0E0B0: 43 48 49 50 00 00 20 10 00 00 00 07 80 00 20 00  CHIP.. ....... .
0E0C0: 00 E1 0F BF 3D 56 00 7E 52 FD 50 03 AA 00 0D 40  ....=V.~R.P....@
...
100C0: 43 48 49 50 00 00 20 10 00 00 00 08 80 00 20 00  CHIP.. ....... .
100D0: 01 01 B9 D1 0D 15 89 55 65 45 41 C5 01 45 45 A8  .......UeEA..EE.
...
120D0: 43 48 49 50 00 00 20 10 00 00 00 09 80 00 20 00  CHIP.. ....... .
120E0: 01 21 0C C7 29 54 41 29 4D C5 06 24 C7 24 8F 81  .!..)TA)M..$.$..
...
140E0: 43 48 49 50 00 00 20 10 00 00 00 0A 80 00 20 00  CHIP.. ....... .
140F0: 01 41 EA 87 7A AF 95 67 BD F7 00 7D 6E C4 5D A6  .A..z..g...}n.].
...
160F0: 43 48 49 50 00 00 20 10 00 00 00 0B 80 00 20 00  CHIP.. ....... .
16100: 01 61 6A 92 6B 93 6B 94 6C 95 6D 96 6E 97 6E 98  .aj.k.k.l.m.n.n.
...
18100: 43 48 49 50 00 00 20 10 00 00 00 0C 80 00 20 00  CHIP.. ....... .
18110: 01 81 0E 83 0D 0B 0F 81 FF 28 28 1E E3 0A 14 62  .........((....b
...
```

```
1A110: 43 48 49 50 00 00 20 10 00 00 00 0D 80 00 20 00  CHIP.. ....... .
1A120: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
...
1C120: 43 48 49 50 00 00 20 10 00 00 00 0E 80 00 20 00  CHIP.. ....... .
1C130: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
...
1E130: 43 48 49 50 00 00 20 10 00 00 00 0F 80 00 20 00  CHIP.. ....... .
1E140: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
```

Cartridge hardware is designed around a 128KiB ROM (PROM or Mask ROM) which is divided into 16 banks each of 8KiB. When adressing the onboard bank-switching logic the requested ROM bank is mapped at address range $8000-$9fff (8KiB)

The cartridge PCB layout was cost-optimized for mass-production purposes.

The address-/data-lines and bank-switching logic uses the closest address-/data-lines with shortest and most direct path/distance in order to avoid too much PCB re-routing and keep production costs as low as possible.

This means that the respective address-/data-lines from the cartridge port may not necessarily connect with the corresponding address-/data-line of the ROM according to the official specs of the ROM.

This has over the years given some headaches and invalid dumps when cartridge dumpers that wanted to dump the cartridge contents by de-soldering the ROM and dumping it using an EPROM programmer/reader.

The image extracted from such an operation would need transformation according to the actual cartridge PCB re-routing of address-/data-lines. Alternatively one can make an adapter that implements this applied re-routing of the address-/data-lines.

This mass-production cost optimization also results in an obscured bank-switching address pattern/values more info on that later.

[HWrev1]: The original "Hugo" PCB [HWrev1] is labeled "HUGO Copyright 1990" on both sides of the PCB. PCB production date is stamped on the back og PCB in the format: "YYMM" 1Mib 27C010 EPROM or PROM (typical Atmel) "Hugo" PCB [HWrev1] contains the following discrete logic IC components: 74LS00N (DIP) and 74LS237N (DIP) DIP pitch (pin spacing) 2.54mm (0.1 inch) Assembly/production facilities: Philips, Strandlodsvej (Amager), Copenhagen, Denmark

[HWrev1] Bank-switching pattern: In order for the rom contents to appear as a continuous memory layout the following ROM bank-switching pattern must be applied by writing to adress 0xDE00: Cartridge bank-switching values: 00 80 10 90 20 a0 30 b0 40 c0 50 d0 60 e0 70 f0 Writing the bank-switch value to any address in address-range 0xDE00-0xDEFF will work.

[HWrev2]: Revised PCB [HWrev2] for SMD mount is labeled "SO-A4-1" on both sides of PCB. 1Mib Mask ROM (SOIC-32) PCB contains the following discrete logic SMD IC components: 74HCT02T (SOIC-14) and 74HCT174T (SOIC-16) SOIC (pitch) pin spacing 1.27mm (SMD mounted) Assembly/production facilities: Sono Press, Germany.

[HWrev2] Uses alternative bank-switching pattern: Writing *ANY* value to address 0xDE0y (offset y) will select ROM bank y Cartridge still has 16 banks and the lower four bits in the address selects the right bank. Hence the valid address range is 0xDE00-0xDE0F

17.14.3.57 56 - Formel 64

Size	32KiB (4 banks of 8KiB)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Startup mode	Ultimax
Load address	$E000-$FFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 38 00 00 00 00 00 00 00 00  ...@...8........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 E0 00 20 00  CHIP.. ....... .
0050: 48 A9 FC 0C A9 FE 8D C2 DF 68 48 C9 8C F0 1C C9  H........hH.....
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 E0 00 20 00  CHIP.. ....... .
2060: EA EA 58 48 A9 FC 8D C2 DF 4C 6E E2 EA EA A9 FA  ..XH.....Ln.....
...
4060: 43 48 49 50 00 00 20 10 00 00 00 02 E0 00 20 00  CHIP.. ....... .
4070: 4C 86 E4 A9 FE 78 8D C2 DF 60 78 8C C3 DF A9 FA  L....x...‘x.....
...
6070: 43 48 49 50 00 00 20 10 00 00 00 03 E0 00 20 00  CHIP.. ....... .
6080: EA EA 48 58 A9 FC 8D C2 DF 68 48 C9 46 D0 7E 68  ..HX.....hH.F.˜h
```

the following is a quick overview of how the cartridge works, as its a bit unusual and different from most other cartridges:

```
- 27256 EPROM (32KiB)
- 7430 TTL
- MC6821
- 1 button (reset)

rom bank 0x00 - 0x03 (0x04) 8192* 4 32KiB mapped to e000
MC6821 registers mapped to io2 at dfc0..dfc4

- press reset and hold delete to get the main menu
- press reset and hold control to skip cbm80 check
  (dont start additional cartridge)

- press RESTORE, then...
  - left arrow, return  show disk directory
  - delete, 1           load"*" from disk, run
  - f1/f2               ? (disk stuff?)
  - f3/f4               ? (disk stuff?)
  - f5/f6, q            enter monitor
  - f7/f8, 2            show drive error channel
  - control, cursor     back to basic
```

```
  - type "help" in basic to get a list of available commands

*** MC6821 Port usage

Port A (parallel cable to floppy drive):
dfc0 port a ddr
dfc1 port a (in/out)

Port B (controls banking)
dfc2 port b ddr ($7f)
dfc3 port b (out)

bit3    1 = rom at $e000 enabled, 0 = cartridge disabled
bit1-2  rom bank number
bit0    ?
```

17.14.3.58 57 - RGCD

Size	64KiB (8 banks of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 39 00 01 00 00 00 00 00 00  ...@...9........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 0C 80 0C 80 C3 C2 CD 38 30 31 30 34 8E 16 D0 20  .......80104...
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: B9 0A 72 AB 0B F0 08 C9 2F 0C 10 27 E8 EF 5A C5  ..r...../..'..Z.
...
4060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
4070: C0 B4 6C A6 6A 0A 14 5E 65 AD 94 02 86 C8 30 1F  ..l.j..^e.....0.
...
6070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP.. ....... .
6080: A4 89 C6 3A C4 60 F0 10 4D F5 89 F0 13 6C E0 78  ...:.`..M....l.x
...
8080: 43 48 49 50 00 00 20 10 00 00 00 04 80 00 20 00  CHIP.. ....... .
8090: 00 0B 08 14 00 9E 32 30 36 31 00 00 00 A0 35 BA  ......2061....5.
...
A090: 43 48 49 50 00 00 20 10 00 00 00 05 80 00 20 00  CHIP.. ....... .
A0A0: 1B 4A 00 53 90 77 8E 1A 1D 94 00 A6 E2 50 29 11  .J.S.w.......P).
...
```

```
C0A0: 43 48 49 50 00 00 20 10 00 00 00 06 80 00 20 00  CHIP.. ....... .
C0B0: 14 F7 2E 1A 34 B4 60 53 07 88 C4 F0 21 F6 88 20  ....4.‘S....!..
...
E0B0: 43 48 49 50 00 00 20 10 00 00 00 07 80 00 20 00  CHIP.. ....... .
E0C0: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
```

This cartridge type is very similar to the Magic Desk cart type: ROM memory is organized in 8KiB ($2000) banks located at $8000-$9FFF. Bank switching is done by writing the bank number to $DE00.

```
bit 0-2   bank number
bit 3     exrom latch (1 = cart disabled until reset or powercycle)
```

If the Cartridge Hardware Subtype/Revision (Offset 0x1A in the CRT Header) is 1, then the banks are addressed in reverse order and the emulated cartridge works like the eprom cartridge by "Hucky". This cartridge can also be equipped with 8Kib, 16Kib, 32Kib EPROMs.

17.14.3.59 58 - RR-Net MK3

Size	8KiB
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 3A 00 01 00 00 00 00 00 00  ...@...:........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 02 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 09 80 C3 C2 CD 38 30 78 A2 FF 9A D8 8E 16  .......80x......
```

This cartridge has a CS8900a based RR-Net compatible network interface, and on 8KiB ($2000) bank ROM located at $8000-$9FFF. The ROM can be switched on/off by writing to the IO1 space:

```
a write to $de80 enables the ROM
a write to $de88 disables the ROM
```

17.14.3.60 59 - EasyCalc

Size	24KiB (3 banks of 8KiB)
EXROM	active (lo) (0)
GAME	active (lo) (0)
Startup mode	16KiB Game
Load address	$8000-$9FFF (bank 1), $A000-$BFFF (banks 2/3)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 3B 00 00 00 00 00 00 00 00  ...@...;........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
```

```
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 59 89 BC FE C3 C2 CD 38 30 0B 80 B1 84 48 E6 84  Y......80....H..
...
2050: 43 48 49 50 00 00 20 10 00 00 00 00 A0 00 20 00  CHIP.. ....... .
2060: 04 0B 20 52 45 50 4C 49 43 41 54 45 20 FB 89 0F  .. REPLICATE ...
...
4060: 43 48 49 50 00 00 20 10 00 00 00 01 A0 00 20 00  CHIP.. ....... .
4070: 02 10 B5 00 C9 E3 D0 01 C8 C9 E2 D0 01 C8 C9 E0  ................
```

This cart uses 8KiB mapped in at $8000-$9FFF and 2 banks of 8KiB mapped in at $A000-$BFFF.

The bank at $A000-$BFFF is selected by bit 0 of the address of a write access to I/O-1.

```
A0 | bank
---------
 0 |  0
 1 |  1
```

17.14.3.61 60 - GMod2

Size	512KiB (64 banks of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	$8000-$9FFF

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
       -----------------------------------------------  ----------------
00000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
00010: 00 00 00 40 01 00 00 3C 00 01 00 00 00 00 00 00  ...@...<........
00020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
00030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
00040: 43 48 49 50 00 00 20 10 00 02 00 00 80 00 20 00  CHIP.. ....... .
00050: 09 80 09 80 C3 C2 CD 38 30 78 8E 16 D0 20 A3 FD  .......80x... ..
...
02050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
02060: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
...
7E430: 43 48 49 50 00 00 20 10 00 00 00 3F 80 00 20 00  CHIP.. ....?.. .
7E440: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
```

GMod2 (Individual Computers)

This cart uses 512KiB Flash ROM (29F040) in 64 banks, mapped in at $8000-$9fff and has a 2KiB serial EEPROM (m93C86).

```
io1
    - register at de00

    bit7   (rw)  write enable (write 1), EEPROM data output (read)
    bit6   (ro)  EXROM (0=active) and EEPROM chip select (1=selected)
```

```
      bit5-0 (ro)  rom bank  bit5 EEPROM clock bit4 EEPROM data input

   see http://wiki.icomp.de/wiki/GMod2
```

17.14.3.62 61 - MAX Basic

Size	16KiB (ULTIMAX mode)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$8000-$9FFF (bank 1), $E000-$FFFF (bank 2)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 3D 01 00 00 00 00 00 00 00  ...@...=........
0020: 4D 41 58 20 42 41 53 49 43 00 00 00 00 00 00 00  MAX BASIC.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 94 E3 7B E3 43 42 4D 42 41 53 49 43 30 88 41 87  ....CBMBASIC0.A.
```

This cartridge uses two 8KiB ROMs that contain BASIC and KERNAL for the MAX Machine. It also provides additional 2KiB RAM which will be mapped to $0800.

17.14.3.63 62 - GMod3

Size	2MiB/4MiB/8MiB/16MiB (256/512/1024/2048 banks of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	$8000-$9FFF

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
       -----------------------------------------------  ----------------
00000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
00010: 00 00 00 40 01 00 00 3E 00 01 00 00 00 00 00 00  ...@...<........
00020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
00030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
00040: 43 48 49 50 00 00 20 10 00 02 00 00 80 00 20 00  CHIP.. ....... .
00050: 09 80 09 80 C3 C2 CD 38 30 78 8E 16 D0 20 A3 FD  .......80x... ..
...
02050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
02060: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
...
001FF030: 43 48 49 50 00 00 20 10 00 00 00 FF 80 00 20 00 CHIP.. ....?.. .
001FF040: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 ................
```

GMod3 (Individual Computers)

This cart uses 2MiB/4MiB/8MiB/16MiB Flash ROM (EN25QH128A(2T)) in 256/512/1024/2048 banks, mapped in at $8000-$9fff.

```
io1
    - register at de00-de07 (write)

    To select the ROM bank, write to de00-7. The lower 3 bits of the address
    are the upper three bits of the bank, as in:

    ldx #bankhi  ; upper 3 bits of the bank number
    lda #banklo  ; lower 8 bits of the bank number
    sta $de00,x

    - register at de00/de08 (read)

    The current ROM bank can be read from de00/de08, as in:

    ldx $de08    ; upper 3 bits of the bank number
    lda $de00    ; lower 8 bits of the bank number

    - register at de08 (write)

    bit7   (w)  bitbang mode enabled
    bit6   (w)  EXROM (0=active)
    bit5   (w)  enable cartridge irq vectors

    - register at de00 (bitbang mode)

    bit7   (r)  EEPROM data output
    bit6   (w)  EEPROM chip select (0=selected)
    bit5   (w)  EEPROM clock
    bit4   (w)  EEPROM data input

see http://wiki.icomp.de/wiki/GMod3
```

17.14.3.64 63 - ZIPP-CODE 48

Size	8KiB
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 3f 00 01 00 00 00 00 00 00  ...@...?........
0020: 5a 49 50 50 2d 43 4f 44 45 20 34 38 00 00 00 00  ZIPP-CODE 48....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 11 de 7b e3 c3 c2 cd 38 30 41 80 00 00 03 81 31  ..?....80A.....1
```

```
reading IO1 enables the cartridge ROM
reading IO2 disables the cartridge ROM
the second last page of the ROM is mirrored in IO1
```

17.14.3.65 64 - Blackbox V8

Size	32KiB or 64KiB
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 40 00 00 00 00 00 00 00 00  ...@...@........
0020: 42 4c 41 43 4b 42 4f 58 20 56 38 00 00 00 00 00  BLACKBOX V8.....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 09 80 22 80 c3 c2 cd 38 30 8e 16 d0 20 a3 fd 20  ..?....80... ..
```

```
writing to IO2 sets the cartridge config:
A0 - EXROM
A1 - GAME
A2 - bank lsb
A3 - bank msb
```

17.14.3.66 65 - Blackbox V3

Size	8KiB
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 41 00 01 00 00 00 00 00 00  ...@...A........
0020: 42 4c 41 43 4b 42 4f 58 20 56 33 00 00 00 00 00  BLACKBOX V3.....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: a7 85 2b 80 c3 c2 cd 38 30 8e 16 d0 20 a3 fd 20  ..+....80... ..
```

```
writing IO1 disables the cartridge ROM
writing IO2 enables the cartridge ROM
```

17.14.3.67 66 - Blackbox V4

Size	16KiB
EXROM	active (lo) (0)
GAME	active (lo) (0)

Load address $8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 42 00 00 00 00 00 00 00 00  ...@...B........
0020: 42 4c 41 43 4b 42 4f 58 20 56 34 00 00 00 00 00  BLACKBOX V4.....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: a7 85 2b 80 c3 c2 cd 38 30 8e 16 d0 20 a3 fd 20  ..+....80... ..
```

```
    reading IO1 enables the cartridge ROM
    reading IO2 disables the cartridge ROM
```

17.14.3.68 67 - REX RAM-Floppy

Size 8KiB
EXROM active (lo) (0)
GAME inactive (hi) (1)
Load address $8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 43 00 01 00 00 00 00 00 00  ...@...C........
0020: 52 45 58 20 52 41 4d 2d 46 4c 4f 50 50 59 00 00  REX RAM-FLOPPY..
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 09 fe c3 c2 cd 38 30 20 a3 fd 20 50 fd 20  .......80 .. P.
```

This cartridge contains an 8KiB ROM which contains the software, plus up to 256KiB RAM which can then be used to store up to 100 programs. The RAM is battery buffered, and the cartridge can be disabled with a switch on the board.

```
    dfa0    (write) selects RAM bank
    df50    (read) toggles RAM writeable
    dfc0    (read) toggles cartridge enable
    dfe0    (read) toggles RAM enable
```

17.14.3.69 68 - BIS-Plus Cartridge

Size 2, 4 or 8KiB
EXROM active (lo) (0)
GAME inactive (hi) (1)
Load address $8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 44 00 01 00 00 00 00 00 00  ...@...D........
0020: 42 49 53 2d 50 4c 55 53 00 00 00 00 00 00 00 00  BIS-PLUS........
```

```
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 5e fe c3 c2 cd 38 30 8e 16 d0 20 a3 fd a0  ..^....80... ...
```

This cartridge contains a 2, 4 or 8KiB ROM. The software is copied to RAM and then the cartridge disables itself by writing to IO1 (de00).

17.14.3.70 69 - SD-BOX

Size	128KiB
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 45 00 00 00 00 00 00 00 00  ...@...E........
0020: 53 44 2d 42 4f 58 00 00 00 00 00 00 00 00 00 00  SD-BOX..........
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 09 80 81 ea c3 c2 cd 38 30 a9 00 8d 00 de a9 40  .......80......@
```

This cartridge contains a 128KiB banked ROM with a filebrowser and some extra software on it. It also contains a sd2iec clone, which seems to be seperate and runs the original sd2iec firmware.

```
there is one register at de00:

bit 0-3     ROM bank
bit 4       reset SD
bit 5       CE ROM
bit 6       EXROM
bit 7       register enable

additionally there are 3 "ram cells" at de01-de03
```

17.14.3.71 70 - MultiMAX

Size	1MiB, 64*16KiB (ULTIMAX mode)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$8000-$9FFF (odd banks), $E000-$FFFF (even banks)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 46 01 00 00 00 00 00 00 00  ...@...F........
0020: 4d 55 4c 54 49 4d 41 58 00 00 00 00 00 00 00 00  MULTIMAX........
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
```

```
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 48 80 48 83 48 83 48 83 64 85 64 85 f5 87 aa 88  H.H.H.H.d.d.....
```

This cartridge contains 1MiB ROM organised in 64 16KiB banks. It operates in ultimax mode and contains all known ultimax releases. It also provides additional 2KiB RAM for MAX-Basic, which will be mapped to $0800.

```
there is one register mirrored in the entire IO1 space:

bit 7       when set, the register is disabled and can only be reenabled by reset
bit 0-5     select ROM bank 0-63
```

17.14.3.72 71 - Blackbox V9

Size	32KiB, 2*16KiB
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$8000-$9FFF $E000-$FFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 47 01 00 00 00 00 00 00 00  ...@...G........
0020: 42 4c 41 43 4b 42 4f 58 20 56 39 00 00 00 00 00  BLACKBOX V9.....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..@.......@.
0050: 00 9a b6 fe c3 c2 cd 38 30 c3 ff 00 ff 00 ff 00  .......80.......
```

The cartridge starts in ultimax mode in the last bank. The IO1 area also contains a mirror of the second last ROM page.

```
a register is mapped to IO1, which is changed by accessing an address in
the IO space, the address bits are mapped like this:

bit 7   -   bank    (inverted on write)
bit 6   -   exrom
bit 0   -   game
```

17.14.3.73 72 - Lt. Kernal Host Adaptor

Size	8KiB ROM, 16KiB RAM
EXROM	active (lo) (0) Initially
GAME	inactive (hi) (1) Initially
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 48 00 01 00 00 00 00 00 00  ...@...H........
0020: 4c 54 2e 20 4b 45 52 4e 41 4c 20 48 4f 53 54 20  LT. KERNAL HOST
0030: 41 44 41 50 54 4f 52 20 36 2e 32 20 52 4f 4d 00  ADAPTOR 6.2 ROM.
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP............
0050: 14 80 14 80 c3 c2 cd 38 30 00 38 37 30 30 30 30  .......80.......
```

The Lt. Kernal Host Adaptor (SCSI ID 7) boot rom looks on sector 0 of drive 0 (ID 0) to find the partition (LU or logical unit) information and DOS. If the drive is not found, it will start up in stock mode. See src/c64/cart/ltkernal.c for more details.

The Lt. Kernal Host Adaptor uses the following registers:

```
$DF00    = MC6821 Port A: Data and DDR
$DF01    = MC6821 Port A: Control
$DF02    = MC6821 Port B: Data and DDR
$DF03    = MC6821 Port B: Control
$DF04..7 = LTK Port and Freeze state
```

MC6821 Port A connects to the SCSI data bus (inverted). Data is saved to hard disk inverted to save data processing time.

```
signal  meaning
------  -------
PA 7-0  /Data bus on SCSI (input/output)
CA2     Pulses SCSI ACK (low to high)
```

MC6821 Port B connects to mostly the SCSI control bus. PB2.6 controls write access to 16KiB SRAM. When low (0), no writes are permitted, when high (1) writes are permitted to $8000-$9FFF AND $E000-$FFFF. Initially, this signal is low, which keeps the boot ROM mapped to $8000-$9FFF (lower 4KiB for the C64, upper 4KiB for the C128). Once a high (1) is written, the boot ROM is mapped out permanently until a reset. CB2 controls which KERNAL is in place. A high (1) uses the stock KERNAL while a low (0) maps in one of the 8K RAMs as the KERNAL. The other RAM at $8000-$9FFF memory is ONLY mapped in when PB2.6 is high (1). The HIRAM line from the PLA goes to the adaptor so it can determine the proper write action to the main RAM or adaptor RAM. Writing a high (1) to CB2 while PB2.6 is low (0) queues up a system reset. If CB2 is not set back to a low (0) or PB2.6 to a high (1), a reset will be asserted in 57 cycles. On startup, the existing kernal is copied and patched by the LTK DOS. On the C64, all known kernal versions work, however in C128 mode, only the original kernal is compatible with the latest LTK DOS (7.2). Make sure to use the 318020-03 kernal (also known as the first version). The LTK DOS overwrites the new patches to the second and third kernal versions.

```
signal  meaning
------  -------
PB 7    /REQ on SCSI (input)
PB 6    SRAM control
PB 5    RST on SCSI (output)
PB 4    SEL on SCSI (output)
PB 3    /BUSY on SCSI (input)
PB 2    /CD on SCSI (input)
PB 1    /MSG on SCSI (input)
PB 0    /IO on SCSI (input)
CB2     KERNAL control
```

$DF04 - $DF07:

The LTK Port can be between 0 and 15. Only adaptors set to port 0 are allowed to change the host configuration.

```
bit  meaning
---  -------
3-0  LTK port number (input)
4    Freeze state (0: active, 1: inactive)
```

17.14.3.74 73 - RAMLink

Size	64KiB ROM, 8KiB RAM
EXROM	inactive (hi) (1) Initially
GAME	active (lo) (0) Initially
Load address	$8000-$BFFF and $E000-$FFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 49 01 00 00 00 00 00 00 00  ................
0020: 43 4d 44 20 52 41 4d 4c 49 4e 4b 20 32 2e 30 31  CMD RAMLink 2.01
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 20 00  CHIP............
0050: 00 00 00 00 00 00 00 00 ee 42 df 20 1c b9 ee 42  ................
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP............
2060: 03 4c 75 9c 88 f0 01 88 8c 80 de a9 8d 20 38 8f  ................
...
4060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP............
4070: 0a 17 d8 8d 64 8c be 00 ce 42 df 20 48 88 ce 42  ................
...
6070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP............
6080: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
...
8080: 43 48 49 50 00 00 20 10 00 00 00 04 80 00 20 00  CHIP............
8090: 40 48 20 c9 ff aa 68 90 01 8a 60 20 bd ff 20 64  ................
...
a090: 43 48 49 50 00 00 20 10 00 00 00 05 80 00 20 00  CHIP............
a0a0: 85 56 20 0f bc a5 61 c9 88 90 03 20 d4 ba 20 cc  ................
...
c0a0: 43 48 49 50 00 00 20 10 00 00 00 06 80 00 20 00  CHIP............
c0b0: 80 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
...
e0b0: 43 48 49 50 00 00 20 10 00 00 00 07 80 00 20 00  CHIP............
e0c0: a2 ff 78 9a d8 a9 00 8d 00 ff a2 0a bd 4b e0 9d  ................
```

or

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 49 00 01 00 00 00 00 00 00  ................
```

```
0020: 43 4d 44 20 52 41 4d 4c 49 4e 4b 20 31 2e 34 30  CMD RAMLink 1.40
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 20 00  CHIP..........
0050: 52 41 4d 4c 49 4e 4b 20 44 4f 53 20 28 43 29 31  ................
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP..........
2060: 4e 54 41 58 20 45 52 52 4f 52 00 57 52 49 54 45  ................
...
4060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP..........
4070: aa 09 f9 10 ec b9 9e 1b ec ce 3f 70 8d 84 df a9  ................
...
6070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP..........
6080: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
...
8080: 43 48 49 50 00 00 20 10 00 00 00 04 80 00 20 00  CHIP..........
8090: 40 48 20 c9 ff aa 68 90 01 8a 60 20 bd ff 20 64  ................
...
a090: 43 48 49 50 00 00 20 10 00 00 00 05 80 00 20 00  CHIP..........
a0a0: 85 56 20 0f bc a5 61 c9 88 90 03 20 d4 ba 20 cc  ................
...
c0a0: 43 48 49 50 00 00 20 10 00 00 00 06 80 00 20 00  CHIP..........
c0b0: 80 8d 3f de a9 1b 8d 32 de 8d 34 de a9 ee 8d 33  ................
...
e0b0: 43 48 49 50 00 00 20 10 00 00 00 07 80 00 20 00  CHIP..........
e0c0: a2 ff 78 9a d8 a9 00 8d 00 ff a2 0a bd 4b e0 9d  ................
```

The RAMLink CRT file is 64 KiB and consists of eight 8 KiB banks. The first two are for the RAMLink DOS which is banked in from $8000-$BFFF (16 KiB). The third and forth are the configuration portion of the DOS which also banks in at $8000-$BFFF, however only the first 8 KiB is officially used. The banks are the 64 shadow kernal, 64 trap kernal, 128 shadow kernal, and 128 trap kernal respectively. The first bytes of the configuration DOS bank contain 16-bit checksums of various parts of the ROM. The precise mapping is specific to the particular version of the ROM as the code resides there. The ROMs verify these checksums on boot up. Should this fail, the firmware turns on the error LED and goes into an infinite loop. Since vice doesn't provide an error or activity LED for RAMLink, the system would just appear to hang.

The actual RAMLink appears to have the ability to upgrade to a 128 KiB ROM, but no firmware which utilizes it is known to exist.

The RAMLink uses many locations in IO2 primarily so that it can co-exist with other cartridges. Most of these registers are write only, that is to say, all that is required to activate them is a write; the contents of the write are not important. This technique minimizes the coding required to interact with the device. Although RAMLink has 2 pass-thru ports, they are not complete shorts to the cartridge bus, some of the control lines are altered based on the state of the device. Initially, RAMLink is disabled, the trap kernal is active and only one of its registers is visible:

```
$DF7E    = Enable RAMLink registers and switch to shadow kernal
```

Once a write is done to $DF7E, all the other registers become active. The shadow kernal also switches in. This happens regardless of the state of the microprocessor IO port ($1). Even if the kernal ROM is mapped out, the shadow kernal will appear.

```
    $DF20     = NMI Trap Enable? (not certain as to its function)
    $DF21     = Mirror to $DF01 (for REU) when NMI trap is on
    $DF22     = NMI Trap Disable? (not certain as to its function)
    $DF40     = I8255A Port A (Main board and parallel interface)
    $DF41     = I8255A Port B
    $DF42     = I8255A Port C
    $DF43     = I8255A Control
    $DF60     = Enable DOS mapping (maps in $8000-$BFFF)
    $DF70     = Disable DOS mapping
    $DF7F     = Disable RAMLink registers and switch to trap kernal
                (also disables DOS mapping)
    $DF80..9F = Switch SRAM page ($DF80 selects page 0,
                $DF81 selects page 1, $DF82 selects page 2, etc.)
    $DFA0     = I8255A Port A (RAMCard 1; RAMCard 2 uses custom circuit
                but same interface)
    $DFA1     = I8255A Port B
    $DFA2     = I8255A Port C
    $DFA3     = I8255A Control
    $DFB0..BF = 72421 RTC interface (RAMCard 2 only)
    $DFC0     = Maps in SRAM to $DE00-$DEFF
    $DFC1     = Maps in RAMCard to $DE00-$DEFF
    $DFC2     = Maps in RAM Port $DE00-$DEFF (GEORAM, RAMDrive)
    $DFC3     = Maps in Pass-Thru to $DE00-$DEFF
```

Note that only address $DF40..42, $DFB0..BF present valid information when read. Addresses $DFA0..$DFA3 also require valid data when being written to. See src/c64/cart/ramlink.c for more details.

When kernal swiching, the ROMs change inbetween instructions. This is possible as the CPU does not cache any incoming instructions. The following is an example of a JSR to the trap kernal at $E000:

```
PC     TRAP         SHADOW      DETAILS
          KERNAL       KERNAL
-----  ---------    ---------   ----------------------------------------
$C000  JSR $E000    JSR $E000   Same code
$E000  STA $DF7E    STA $DF7F   In trap kernel, next instruction in shadow
$E003  INC $D021    INC $D020   Increment $D020
$E006  STA $DF7E    STA $DF7F   Switch back to trap on next instruction
$E009  RTS          NOP         Return to $C003
```

If the shadow kernal was active, a JSR to $E000 would result in $D021 being incremented and continuing on back to the NOP in the shadow kernal. The use of write only registers allows the RAMLink Kernal and DOS to perform ROM switching with minimal instructions and time.

The RAMLink includes two switches: "Enable/Disable" and "Normal/Direct". "Enable/Disable" controls whether or not RAMLink is active. In the "Disabled" state,

control will be given back to the stock ROMs. "Normal/Direct" controls how the IO cartidge lines to the RAM Port (REU, GEORAM, RAMDrive) are passed. In "Normal" mode, these lines are connected only when the RAMLink registers are active. This prevents any unauthorized access to these devices while RAMLink code isn't running. "Direct" connects the IO lines all the time.

17.14.3.75 74 - Drean

Size	32KiB (4 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 4a 00 01 00 00 00 00 00 00  .......J........
0020: 44 52 45 41 4e 20 48 45 52 4f 00 00 00 00 00 00  DREAN HERO......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 0b 80 bc fe c3 c2 cd 38 30 03 49 8e 16 d0 bd 00  .......80.I.....
..
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP.. ....... .
2060: 32 aa 5a aa 00 00 ff 00 90 d9 68 d9 00 00 90 05  2.Z.......h.....
..
4060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
4070: ff 7f 7f ff ff 3f 00 00 00 00 00 c0 00 c0 ff 3f  .....?.........?
..
6070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP.. ....... .
6080: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
```

This cartridge type was found in an argentinian H.E.R.O. clone cartridge, ROM memory is organized in 8KiB ($2000) banks located at $8000-$9FFF. There appears to be one register mirrored over IO2 (the software uses $DFFF). Banks are selected by writing to the lower 2 bits of the register, bit 5 disables the cartridge.

17.14.3.76 75 - IEEE Flash! 64

Size	8KiB (1 bank of 8KiB)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$E000-$FFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 2D 01 00 00 00 00 00 00 00  ...@...,........
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 E0 00 20 00  CHIP.. ....... .
```

```
0050: 85 56 20 0F BC A5 61 C9 88 90 03 20 D4 BA 20 CC  .V ...a.... .. .
```

This cart has 8KiB of ROM, mapped in at $E000-$FFFF only when hirom is selected. The cart uses a clip that needs to be installed inside the C64.

17.14.3.77 76 - Turtle Graphics II

Size	16KiB (2 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 4c 00 01 00 00 00 00 00 00  ......L........
0020: 48 45 53 20 54 75 72 74 6c 65 20 47 72 61 70 68  HES Turtle Graph
0030: 69 63 73 20 49 49 00 00 00 00 00 00 00 00 00 00  ics II..........
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 09 80 bc fe c3 c2 cd 38 30 78 a9 80 85 11 a9 30  .......80x.....0
0060: 85 10 a9 0e 85 13 a9 00 85 12 aa a8 b1 10 91 12  ................
0070: c8 d0 f9 e6 11 e6 13 e8 e0 09 d0 f0 4c 90 15 ea  ............L...
0080: 01 c0 00 01 a0 00 01 90 00 03 88 00 03 84 00 05  ................
0090: 82 00 05 81 00 09 80 80 09 80 40 11 80 20 11 80  ............ ..
...
4040: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
4050: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
```

Any read/write access to IO1 area will switch to bank 1. Reset will switch to bank 0.

17.14.3.78 77 - Freeze Frame MK2

Size	16KiB
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Load address	$8000-$BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CART RIDGE
0010: 00 00 00 40 01 00 00 4d 00 01 00 00 00 00 00 00  ......M ........
0020: 46 72 65 65 7a 65 20 46 72 61 6d 65 20 4d 4b 32  Freeze F rame MK2
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ........ ........
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP... .......
0050: 10 80 0c 80 c3 c2 cd 38 30 20 00 00 40 00 00 00  .......8 0 .....
...
4030: 02 b0 02 c6 03 60 00 00 a9 ff 85 00 a4 01 68 aa  .....‘.. ......h.
4040: 68 98 09 20 48 8a 48 4c 1b ff e0 fe 10 80 10 80  h.. H.HL ........
```

Warning, the following information is based on guess-work and might be incorrect, any further information and/or corrections are appreciated.

When the cart starts, the first half of the active ROM is banked to ROML ($8000-$9fff). When reading from the I/O-2 range the second half of the active ROM bank is enabled in RMH ($A000-$BFFF). When reading from the I/O-1 range the cart is disabled.

When a freeze happens the first half of the ROM is mapped to ROML ($8000-$9FFF) and the second half to ROMH ($E000-$FFFF).

17.14.3.79 78 - Partner 64

Size	16KiB
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$8000-$9FFF (ROML), $E000-$FFFF (ROMH)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 4e 01 00 00 00 00 00 00 00  .....N........
0020: 50 61 72 74 6e 65 72 20 36 34 00 00 00 00 00 00  Partner 64......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050: 78 48 a5 00 48 a5 01 48 a9 2f 85 00 a9 37 85 01  xH..H..H./...7..
...
4040: 00 00 00 00 00 00 00 00 00 00 01 00 eb 06 c7 06  ................
4050: ff ff ff ff ff ff ff 4c 5d ea 00 80 12 84 e0 84  .......L].......
```

Warning, the following information is based on guess-work and might be incorrect, any further information and/or corrections are appreciated.

This cartridge has 16KiB of ROM and 8KiB of RAM. It starts in ultimax mode with ROML at $8000, ROMH at $e000 and the extra RAM at $a000.

17.14.3.80 79 - Hyper-BASIC

Size	64KiB (8 banks of 8KiB)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	$8000-$9FFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 4F 00 01 00 00 00 00 00 00  .....O........
0020: 48 79 70 65 72 2D 42 41 53 49 43 00 00 00 00 00  Hyper-BASIC.....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP............
0050: 44 CF 4C 4F 4F D0 45 58 49 D4 43 41 53 C5 47 4B  D.LOO.EXI.CAS.GK
...
2050: 43 48 49 50 00 00 20 10 00 00 00 01 80 00 20 00  CHIP............
2060: A9 FF 2C A9 01 85 FF A5 D6 48 A5 D3 48 20 9E B7  ..,.............
```

```
...
4060: 43 48 49 50 00 00 20 10 00 00 00 02 80 00 20 00  CHIP.. ....... .
4070: A9 01 A0 A1 A2 A1 20 BD FF A0 0F A2 04 20 BA FF  .. ... .. ... ..
...
6070: 43 48 49 50 00 00 20 10 00 00 00 03 80 00 20 00  CHIP.. ....... .
6080: 09 80 BC FE C3 C2 CD 38 30 A2 FF 8E 02 DC E8 8E  .......80.......
...
8080: 43 48 49 50 00 00 20 10 00 00 00 04 80 00 20 00  CHIP.. ....... .
8090: A0 0F B9 40 80 99 00 01 88 10 F7 AE 01 08 E0 09   ..............
...
A090: 43 48 49 50 00 00 20 10 00 00 00 05 80 00 20 00  CHIP.. ....... .
A0A0: A0 00 B9 00 9E 99 00 9E B9 00 9F 99 00 9F C8 D0   ..............
...
C0A0: 43 48 49 50 00 00 20 10 00 00 00 06 80 00 20 00  CHIP.. ....... .
C0B0: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF  ................
...
E0B0: 43 48 49 50 00 00 20 10 00 00 00 07 80 00 20 00  CHIP.. ....... .
E0C0: 2C CA 91 AD 0A 03 AE 0B 03 8D B8 02 8E B9 02 A9  ,...............
```

This cartridge is organized in 8KiB ($2000) banks located at $8000-$9FFF. Bank switching is done by writing to the register at $de00, which is mirrored across the entire IO1 range. Somewhat uncommon is that the register directly exposes the chip-enable for the two ROMs.

```
bit 0 - _CE for ROM2, 0 = enable (don't select both)
bit 1 - _CE for ROM1, 0 = enable (don't select both)
(bit 2 - probably unused)
(bit 3 - probably unused)
bit 4 - A13
bit 5 - A14
bit 6 and/or 7 - _EXROM,  0 = make cart visible
```

17.14.3.81 80 - Universal Cartridge 1

Size	32KiB, 64KiB or 128KiB sizes (2 to 8 banks of 16KiB each)
EXROM	active (lo) (0)
GAME	inactive (lo) (0)
Startup mode	16KiB Game
Load address	(banks 00-07) - $8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 50 00 00 01 00 00 00 00 00  ...@.....
0020: 75 63 2d 6c 4f 41 44 45 52 00 00 00 00 00 00 00  uc-lOADER.....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 02 00 00 80 00 20 00  CHIP.. ....... .
0050: 19 80 9e 82 c3 c2 cd 38 30 4c 55 84 4c 64 84 4c  .......80... ..
..
2050: 43 48 49 50 00 00 20 10 00 02 00 00 a0 00 20 00  CHIP.. ....... .
2060: 00 3F 0A 01 00 86 4E 24 28 31 30 29 3A 4A 4F 59  .?....N$(10):JOY
```

```
..
4060: 43 48 49 50 00 00 20 10 00 02 00 01 80 00 20 00  CHIP.. ....... .
4070: 00 8B C9 28 4E 24 2C 31 29 B3 B1 22 FF 22 A7 32  ...(N$,1)..".".2
..
6070: 43 48 49 50 00 00 20 10 00 02 00 01 a0 00 20 00  CHIP.. ....... .
6080: AE 01 83 33 2C 37 2C 22 32 29 20 44 45 4C 20 4B  ...3,7,"2) DEL K
```

This cartridge type has up to 128K ROM memory and 32K SRAM memory. Each Bank is organized in 16KiB ($4000), banks are located at $8000-$BFFF. Bank switching is done by writing the bank number to $DE00 (Bit 0 to 2).

```
Register at IO1 ($DE00):
Bit 0-2   bank number
Bit 3 --- IO Register disable (1 - Register is invisible)
Bit 4 --- SRAM write enable   (1 - SRAM is writable)
Bit 5 --- SRAM select         (1 - RAM, 0 - ROM)
Bit 6 --- Signal /GAME        (C64 Cartridge Mode)
Bit 7 --- Signal /EXROM       (C64 Cartridge Mode)

UC1 Documentation:
https://oe7twj.at/index.php?title=Universal_Cartridge_1
```

In the cartridge header, EXROM ($18) is set to 0, GAME ($19) is set to 0 to indicate the RESET/power-up configuration of 16 KiB ROM.

There is a Windows Tool which can create RAW Image file and UC1 CRT file. The content of the image file is configurable using a CSV file.

Since UC1 cartridge can set /EXROM and /GAME signal, all known C64 basic configurations for cartridges (Off, 8K, 16K, UltiMax) are configurable.

Cartridge memory can be either ROM or SRAM. SRAM can be protected, so it appears as ROM memory. IO Register can be disabled, so configuration cannot be overwritten. Basic CRT files (8K, 16K, UltiMax) can be loaded into UC1 SRAM from ROM or Disk and ececuted by a Reset.

17.14.3.82 81 - Universal Cartridge 1.5

Size	128KiB, 256KiB or 512KiB sizes (8 to 32 banks of 16KiB each)
EXROM	active (lo) (0)
GAME	inactive (lo) (0)
Startup mode	16KiB Game
Load address	(banks 00-07) - $8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 51 00 00 01 00 00 00 00 00  ...@.....
0020: 75 63 2d 6c 4f 41 44 45 52 00 00 00 00 00 00 00  uc-lOADER.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 02 00 00 80 00 20 00  CHIP.. ....... .
0050: 19 80 9e 82 c3 c2 cd 38 30 4c 55 84 4c 64 84 4c  .......80... ..
```

```
..
2050: 43 48 49 50 00 00 20 10 00 02 00 00 a0 00 20 00  CHIP.. ....... .
2060: 00 3F 0A 01 00 86 4E 24 28 31 30 29 3A 4A 4F 59  .?....N$(10):JOY
..
4060: 43 48 49 50 00 00 20 10 00 02 00 01 80 00 20 00  CHIP.. ....... .
4070: 00 8B C9 28 4E 24 2C 31 29 B3 B1 22 FF 22 A7 32  ...(N$,1).."."."2
..
6070: 43 48 49 50 00 00 20 10 00 02 00 01 a0 00 20 00  CHIP.. ....... .
6080: AE 01 83 33 2C 37 2C 22 32 29 20 44 45 4C 20 4B  ...3,7,"2) DEL K
```

This cartridge has nearly the same hardware spec as an UC2 cartridge. The difference is in the hardware components. UC2 is controlled by an CPLD and UC15 uses GAL and TTL.

From the software side the difference is: UC2 can read back the register values. UC15 cannot readback cause register are simple TTL FF.

17.14.3.83 82 - Universal Cartridge 2

Size	128KiB, 256KiB or 512KiB sizes (8 to 32 banks of 16KiB each)
EXROM	active (lo) (0)
GAME	inactive (lo) (0)
Startup mode	16KiB Game
Load address	(banks 00-07) - $8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 52 00 00 01 00 00 00 00 00  ...@.....
0020: 75 63 2d 6c 4f 41 44 45 52 00 00 00 00 00 00 00  uc-lOADER.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 02 00 00 80 00 20 00  CHIP.. ....... .
0050: 19 80 9e 82 c3 c2 cd 38 30 4c 55 84 4c 64 84 4c  .......80... ..
..
2050: 43 48 49 50 00 00 20 10 00 02 00 00 a0 00 20 00  CHIP.. ....... .
2060: 00 3F 0A 01 00 86 4E 24 28 31 30 29 3A 4A 4F 59  .?....N$(10):JOY
..
4060: 43 48 49 50 00 00 20 10 00 02 00 01 80 00 20 00  CHIP.. ....... .
4070: 00 8B C9 28 4E 24 2C 31 29 B3 B1 22 FF 22 A7 32  ...(N$,1).."."."2
..
6070: 43 48 49 50 00 00 20 10 00 02 00 01 a0 00 20 00  CHIP.. ....... .
6080: AE 01 83 33 2C 37 2C 22 32 29 20 44 45 4C 20 4B  ...3,7,"2) DEL K
```

This cartridge type has up to 512K ROM memory and 512K SRAM memory. Each Bank is organized in 16KiB ($4000), banks are located at $8000-$BFFF. Bank switching is done by writing the bank number to $DE02 (Bit 0 to 4).

```
Register A at IO1 ($DE02):
Bit 0-4   bank number

Register B at IO1 ($DE03):
```

```
Bit 3 --- IO Register disable (1 - Register is invisible)
Bit 4 --- SRAM write enable   (1 - SRAM is writable)
Bit 5 --- SRAM select         (1 - RAM, 0 - ROM)
Bit 6 --- Signal /GAME        (C64 Cartridge Mode)
Bit 7 --- Signal /EXROM       (C64 Cartridge Mode)

UC2 Documentation:
https://oe7twj.at/index.php?title=Universal_Cartridge_2
```

In the cartridge header, EXROM ($18) is set to 0, GAME ($19) is set to 0 to indicate the RESET/power-up configuration of 16 KiB ROM.

There is a Windows Tool which can create RAW Image file and UC2 CRT file. The content of the image file is configurable using a CSV file.

Since UC2 cartridge can set /EXROM and /GAME signal, all known C64 basic configurations for cartridges (Off, 8K, 16K, UltiMax) are configurable.

Cartridge memory can be either ROM or SRAM. SRAM can be protected, so it appears as ROM memory. IO Register can be disabled, so configuration cannot be overwritten. Basic CRT files (8K, 16K, UltiMax) can be loaded into UC2 SRAM from ROM or Disk and ececuted by a Reset.

17.14.3.84 83 - BMP Data Turbo 2000

Size	16KiB
EXROM	active (lo) (0)
GAME	active (lo) (0)
Load address	$8000-BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
0010: 00 00 00 40 01 00 00 53 00 00 00 00 00 00 00 00  ......S........
0020: 42 4d 50 20 44 61 74 61 20 54 75 72 62 6f 20 32  BMP Data Turbo 2
0030: 30 30 30 00 00 00 00 00 00 00 00 00 00 00 00 00  000.............
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
0050: 09 80 00 08 c3 c2 cd 38 30 a2 0b 8e 11 d0 20 a3  .......80.......
```

After RESET or POWER ON, 16KiB of cartridge ROM is visible at $8000-$BFFF. ROM at $8000-$BFFF is disabled by writing into the I/O-2 area (typically $DF00) and may be re-enabled by writing into I/O-1 ($DE00).

17.14.3.85 84 - Profi-DOS

Size	16KiB (2 banks of 8KiB)
EXROM	inactive (hi) (1)
GAME	active (lo) (0)
Load address	$E000-$FFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
```

```
0010: 00 00 00 40 01 00 00 54 01 00 00 00 00 00 00 00  .....T........
0020: 50 72 6f 66 69 2d 44 4f 53 00 00 00 00 00 00 00  Profi-DOS.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 e0 00 20 00  CHIP.. ....... .
0050: 8d 9d 02 a9 eb 48 a9 8e 48 ad 9c 02 48 ad 9d 02  .....H..H...H...
..
2050: 43 48 49 50 00 00 20 10 00 00 00 01 e0 00 20 00  CHIP.. ....... .
2060: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
```

The ROM is connected like this:

```
    Port    EPROM

      D7 => D6
      D6 => D4
      D5 => D7
      D4 => D5
      D3 => D0
      D2 => D3
      D1 => D1
      D0 => D2

      A0 => A0
      A1 => A1
      A2 => A2
      A3 => A3
      A4 => A4
      A5 => A5
      A6 => A6
      A7 => A7
      A8 => A12
      A9 => A10
     A10 => A11
     A11 => A9
     A12 => A8
     A13 => A13
```

After reorganizing the ROM data as shown above, the first 8k bank of the ROM contains the code. The second bank is used as a lookup table, and wired so that when D0 becomes active (=0), then a counter is started which counts 4 cycles. If during those 4 cycles D1 becomes active (=0), then the EPROM is enabled and the cartridge enables ultimax mode for ROMH. This way the cart can patch the kernal, without having an explicit enable mechanism - which makes it hard to dump (you'll have to know the addresses where this happens)

Any access (read or write) to IO2 ($dfxx) will disable the cartridge again.

17.14.3.86 85 - Magic Desk 16

Size	32KiB, 64KiB ... up to 2048KiB sizes (4 to 128 banks of 16KiB each)

EXROM	active (lo) (0)
GAME	active (lo) (0)
Startup mode	16KiB Game
Load address	(banks 0-127) - $8000-BFFF

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -----------------------------------------------  ----------------
000000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
000010: 00 00 00 40 01 00 00 55 00 00 00 00 00 00 00 00  ......U........
000020: 4d 61 67 69 63 20 44 65 73 6b 20 31 36 4b 00 00  Magic Desk 16K..
000030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
000040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
000050: 09 80 09 80 c3 c2 cd 38 30 78 a2 ff 9a d8 a9 08  .......80x......
..
200820: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
200830: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
```

Very similar to the classic "Magic Desk" cartridge, except that it uses 16KiB banks instead and operates in "16k Game" mode.

Bank switching is done by writing the bank number to $DE00. Bit 7 is cleared for selecting one of the ROM banks. If bit 7 is set ($DE00 = $80), the GAME/EXROM lines are disabled, turning on RAM at $8000-B9FFF instead of ROM.

used by "SNK vs CAPCOM - Stronger Edition"

17.14.3.87 86 - Protovision "Megabyter"

Size	1024KiB (128 banks of 8KiB each)
EXROM	active (lo) (0)
GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	(banks 0-127) - $8000-9FFF

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -----------------------------------------------  ----------------
000000: 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
000010: 00 00 00 40 01 00 00 56 00 01 00 00 00 00 00 00  ......V........
000020: 50 54 56 20 4d 65 67 61 62 79 74 65 72 00 00 00  PTV Megabyter...
000030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
000040: 43 48 49 50 00 00 20 10 00 02 00 00 80 00 20 00  CHIP.. ....... .
000050: 09 80 00 00 c3 c2 cd 38 30 78 20 d3 82 a9 00 8d  .......80x .....
...
100820: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
100830: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
```

17.14.3.88 87 - Magic Desk Plus

Size	256KiB, 512KiB, or 1024KiB ROM (32, 64, or 128 banks of 8KiB)
EXROM	active (lo) (0)

GAME	inactive (hi) (1)
Startup mode	8KiB Game
Load address	(banks 0-127) - $8000-$9FFF
Additional memory	128KiB SRAM and/or 8KiB/32KiB parallel EEPROM

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
       -----------------------------------------------  ----------------
000000 43 36 34 20 43 41 52 54 52 49 44 47 45 20 20 20  C64 CARTRIDGE
000010 00 00 00 40 01 00 00 57 00 01 00 00 00 00 00 00  ......W........
000020 4d 61 67 69 63 20 44 65 73 6b 20 50 6c 75 73 00  Magic Desk Plus.
000030 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
000040 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
000050 09 80 39 80 c3 c2 cd 38 30 78 8e 16 d0 20 a3 fd  ..9....80x... ..
...
040230 ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
```

Magic Desk Plus is very similar to the original Magic Desk, except it has additional SRAM and/or EEPROM:

- Revision 0: SRAM and 32KiB EEPROM
- Revision 1: SRAM and 8KiB EEPROM
- Revision 2: 32KiB EEPROM
- Revision 3: 8KiB EEPROM
- Revision 4: SRAM only

The SRAM and EEPROM are hardware features of the selected revision and are emulated even if no backing image is configured. In that case their contents are temporary. SRAM and EEPROM data are stored in separate optional image files, not as CHIP packets in the CRT image.

The ROM bank register is at $DE00 and uses bits 0-6 for the bank number; bit 7 disables the cartridge. $DE01 selects the SRAM or EEPROM page and $DE03 is the memory control register. The selected SRAM or EEPROM page is accessible through the 256-byte window at $DF00-$DFFF.

17.14.4 C128 Cartridge Specifics

Note: Cartridges that work on both C64 and C128 get "C64 Cartridge" type and get a CRT-ID in the C64 list! **ONLY** Cartridges that exclusively work in C128 mode get a CRT-ID in the C128 list and "C128 Cartridge" header type.

CRT ID	Cartridge Type
0	generic cartridge
1	Warpspeed128
2	Partner 128
3	Comal 80
4	Magic Desk 128
5	Gmod2-C128

17.14.4.1 0 - generic (external Function ROM)

Generic cartridges for the C128 can use two 16KiB ROM blocks named ROML and ROMH, which are mapped to $8000-$BFFF and $C000-$FFFF respectively.

Commonly such cartridges contain one or two 8KiB or 16KiB CHIP packets, one for ROML and one for ROMH. When a block uses a ROM smaller than the block (like 8KiB ROM), then mirrors should be created within that ROM block.

Typical cartridges are 32KiB (ROML+ROMH) or 16KiB (ROML only), although smaller cartridges exist.

8KiB lo function ROM

Size 8KiB
Load address $8000-$BFFF

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F      ASCII
       -----------------------------------------------  ----------------
0000:  43 31 32 38 20 43 41 52 54 52 49 44 47 45 20 20  C128 CARTRIDGE
0010:  00 00 00 40 02 00 00 00 00 00 00 00 00 00 00 00  ...............
0020:  47 65 6e 65 72 69 63 20 43 31 32 38 20 43 61 72  Generic C128 Car
0030:  74 72 69 64 67 65 00 00 00 00 00 00 00 00 00 00  tridge..........
0040:  43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
0050:  4c 0a 80 4c 0a 80 02 43 42 4d 78 a2 ff 9a d8 a9  L..L...CBMx.....
...
2030:  aa aa aa aa aa aa aa aa aa aa aa aa aa aa aa aa  ................
2040:  aa aa aa aa aa aa aa aa aa aa aa aa aa aa aa aa  ................
```

16KiB lo function ROM

Size 16KiB
Load address $8000-$BFFF

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F      ASCII
       -----------------------------------------------  ----------------
0000:  43 31 32 38 20 43 41 52 54 52 49 44 47 45 20 20  C128 CARTRIDGE
0010:  00 00 00 40 02 00 00 00 00 00 00 00 00 00 00 00  ...............
0020:  47 65 6e 65 72 69 63 20 43 31 32 38 20 43 61 72  Generic C128 Car
0030:  74 72 69 64 67 65 00 00 00 00 00 00 00 00 00 00  tridge..........
0040:  43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
0050:  4c 0a 80 60 ea ea ff 43 42 4d a5 d4 c9 48 d0 01  L..‘...CBM...H..
...
4030:  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
4040:  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
```

32KiB lo+hi function ROM

Size 32KiB
Load address $8000-$BFFF

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F      ASCII
       -----------------------------------------------  ----------------
0000:  43 31 32 38 20 43 41 52 54 52 49 44 47 45 20 20  C128 CARTRIDGE
0010:  00 00 00 40 02 00 00 00 00 00 00 00 00 00 00 00  ...............
0020:  47 65 6e 65 72 69 63 20 43 31 32 38 20 43 61 72  Generic C128 Car
0030:  74 72 69 64 67 65 00 00 00 00 00 00 00 00 00 00  tridge..........
0040:  43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
0050:  4c 0a 80 d2 d7 d7 30 43 42 4d ad 00 ff 85 fe ad  L.....0CBM......
...
```

```
8040:  32 39 3a 89 31 00 ee 26 50 00 d6 31 32 00 00 00  29:.1..&P..12...
8050:  a9 01 a0 00 91 2d 20 4f 4f 4c 82 4f 00 00 00 00  .....- OOL.O....
```

17.14.4.2 1 - Warpspeed128

Size 16KiB
Load address $8000-$BFFF

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
       -----------------------------------------------  ----------------
0000:  43 31 32 38 20 43 41 52 54 52 49 44 47 45 20 20  C128 CARTRIDGE
0010:  00 00 00 40 02 00 00 01 00 00 00 00 00 00 00 00  ................
0020:  57 61 72 70 20 53 70 65 65 64 20 31 32 38 00 00  Warp Speed 128..
0030:  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040:  43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
0050:  4c 22 80 4c 22 80 ff 43 42 4d 20 53 e4 20 18 e5  L".L"..CBM S. ..
...
4030:  19 ad 00 1c 29 fd 05 19 8d 00 1c a2 02 bd 03 01  ....).....
4040:  95 15 95 11 ca d0 f6 8e 00 07 60 41 54 21 21 21  ..........`AT!!!
```

This is a 16KiB ROM starting at $8000. The part at $9e00-$9fff is mirrored to $de00-$dfff in I/O space.

Note: the actual cartridge contains both a C64 and a C128 ROM, which can be selected with a switch. We implement them as if these two ROMs were different cartridges - this way they can be selected by using the other .crt file, instead of having to flip the switch in the user interface.

17.14.4.3 2 - Partner 128

Size 16KiB ROM, 8KiB RAM
Load address $8000-$BFFF

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 43 31 32 38 20 43 41 52 54 52 49 44 47 45 20 20  C128 CARTRIDGE
0010: 00 00 00 40 02 00 00 02 00 00 00 00 00 00 00 00  ................
0020: 50 61 72 74 6e 65 72 20 31 32 38 00 00 00 00 00  Partner 128.....
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
0050: 4c 5a 87 4c 5a 87 02 43 42 4d 85 1a 84 1b a0 00  LZ.LZ..CBM......
...
4030: 22 20 4b f2 8a 0a 0a 0a 0a 0a 85 c2 ad 00 1c 29  " K............)
4040: 9f 05 c2 8d 00 1c 60 8d 03 ff 91 ae 8d 04 ff 60  ......`........`
```

$8000-$9fff contains the ROM $de00-$de7f contains 128 bytes ram $de80-$deff contains a mirror of $9e80-$9eff Writing to $de80 selects which one of 64 blocks of 128 byte ram is visible in $de00-$de7f.

17.14.4.4 3 - Comal 80

Size 96KiB (6 banks of 16KiB)
Load address $C000-$FFFF

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
```

```
        -------------------------------------------------  ----------------
000000: 43 31 32 38 20 43 41 52 54 52 49 44 47 45 20 20  C128 CARTRIDGE
000010: 00 00 00 40 02 00 00 03 00 00 00 00 00 00 00 00  ................
000020: 43 6f 6d 61 6c 20 38 30 20 28 43 31 32 38 29 00  Comal 80 (C128).
000030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
000040: 43 48 49 50 00 00 40 10 00 00 00 00 c0 00 40 00  CHIP..........
000050: 4c 8c c9 4c 85 1f 01 43 42 4d 4c 5d d2 4c b9 cd  L..L...CBML].L..
...
018080: 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20
018090: 20 20 20 20 20 20 20 20 20 20 05 ff 3d ff 17 ff              ..=...
```

This cartridge contains 3 32KiB ROMs, each containing two 16KiB banks, which are mapped to $C000 (ROMH). Bankswitching is done via one register at $DE00.

17.14.4.5 4 - Magic Desk 128

Size 16KiB ROM, up to 64 banks (=1MiB)
Load address $8000-$BFFF

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -------------------------------------------------  ----------------
000000: 43 31 32 38 20 43 41 52 54 52 49 44 47 45 20 20  C128 CARTRIDGE
000010: 00 00 00 40 02 00 00 04 00 00 00 00 00 00 00 00  ................
000020: 4d 61 67 69 63 20 44 65 73 6b 20 31 32 38 00 00  Magic Desk 128..
000030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
000040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
000050: 4c 0a 80 4c 12 81 01 43 42 4d d8 a9 3a 8d 00 ff  L..L...CBM..:...
...
040120: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
040130: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
```

This cartridge contains two ROM sockets with 19 address lines (A0..A18) each. A19 selects which of the two ROM sockets is selected.

D0..D5 of the register at $de00 go to A14..A19. D6/D7 of the register are not used

That means: D0..D4 select the ROM bank D5 selects the ROM socket

The common use-case would be to fill the first ROM first (with 512KiB), and only then add a second ROM. This way you can use linear values in the register to select the respective banks.

17.14.4.6 5 - Gmod2-C128

Size 16KiB ROM, up to 32 banks (=512KiB)
Load address $8000-$BFFF

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -------------------------------------------------  ----------------
000000: 43 31 32 38 20 43 41 52 54 52 49 44 47 45 20 20  C128 CARTRIDGE
000010: 00 00 00 40 02 00 00 05 00 00 00 00 00 00 00 00  ................
000020: 47 6d 6f 64 32 2d 43 31 32 38 00 00 00 00 00 00  Gmod2-C128......
000030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
000040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
```

```
000050: 4c 0a 80 4c 0a 80 02 43 42 4d 78 a9 0a 8d 00 ff  L..L...CBMx.....
...
080220: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
080230: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
```

This is similar to the C64 Gmod2 Cartridge. The differences are: - Banks are 16KiB instead of 8. Because of that one bit less in the register is used for banking - The ROM is always mapped to ROML area

17.14.5 VIC20 Cartridge Specifics

CRT ID	Cartridge Type
0	generic cartridge
1	Mega-Cart
2	Behr Bonz
3	Vic Flash Plugin
4	UltiMem
5	Final Expansion
6	Rabbit tape turbo
7	Super Expander
8	Mikro Assembler
9	Write Now

17.14.5.1 0 - generic cartridge

Generic VIC20 cartridges are a bit more complex than the ones from the other machines. Since the VIC20 has so little internal memory, most of it is available to be used for ROM and/or RAM at the expansion port:

Block	Start	End	Size
0	$0400	$0FFF	3k
1	$2000	$3FFF	8k
2	$4000	$5FFF	8k
3	$6000	$7FFF	8k
5	$A000	$BFFF	8k

Common start addresses for cartridge ROMs are $2000, $4000, $6000, $7000, $A000, $B000. Also common, for 16KiB cartridges, is a combination of block 1 or 3 with block 5.

Generic VIC20 .crt files may contain one or more CHIP packets fitting into the above blocks. If only the beginning of a block is used for ROM, then the reminder should be filled with mirrors (as it would happen on the real hardware when using a smaller rom and not decoding the unused lines).

17.14.5.2 1 - Mega-Cart

Size 2MiB

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -----------------------------------------------  ----------------
  0000: 56 49 43 32 30 20 43 41 52 54 52 49 44 47 45 20  VIC20 CARTRIDGE
  0010: 00 00 00 40 02 00 00 01 00 00 00 00 00 00 00 00  ................
  0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
```

```
  0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
  0040: 43 48 49 50 00 00 20 10 00 00 00 00 a0 00 20 00  CHIP.. ....... .
  0050: fd be 0e bf 41 30 c3 c2 cd 81 00 81 55 c1 5a 81  ....A0......U.Z.
...
201020: a7 ac b0 b5 b9 bd c1 c4 c7 ca cd d0 d3 d5 d8 da  ................
201030: dc de e0 e2 e3 e5 fe 24 17 00 00 00 00 00 00 00  .......$........
```

17.14.5.3 2 - Behr Bonz

Size 2MiB (128 banks)

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -----------------------------------------------  ----------------
  0000: 56 49 43 32 30 20 43 41 52 54 52 49 44 47 45 20  VIC20 CARTRIDGE
  0010: 00 00 00 40 02 00 00 02 00 00 00 00 00 00 00 00  ................
  0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
  0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
  0040: 43 48 49 50 00 00 20 10 00 00 00 00 a0 00 20 00  CHIP.. ....... .
  0050: 10 b0 10 b0 41 30 c3 c2 cd ff ff ff ff ff ff ff  ....A0..........
...
201020: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
201030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
```

This cartridge has one bank register, mirrored over the whole IO3 ($9c00-$9fff)

17.14.5.4 3 - Vic Flash Plugin

Size 4MiB

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -----------------------------------------------  ----------------
  0000: 56 49 43 32 30 20 43 41 52 54 52 49 44 47 45 20  VIC20 CARTRIDGE
  0010: 00 00 00 40 02 00 00 03 00 00 00 00 00 00 00 00  ................
  0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
  0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
  0040: 43 48 49 50 00 00 20 10 00 00 00 00 a0 00 20 00  CHIP.. ....... .
  0050: 66 a1 31 a1 41 30 c3 c2 cd 00 00 01 03 00 01 8d  f.1.A0..........
...
402020: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
402030: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
```

This cartridge has two registers, which are mirrored over the whole IO2 ($9800-$9bff) area:

```
$9800   current bank (A20..A13)
$9801   configuration register
 b7     1: I/O2 disabled until RESET
 b6     1: ROM write protect (set by default)
 b5     1: RAM at BLK5 (instead of ROM)
 b4     0: 3k (RAM123), 1: 8k+ (BLK1)
 b3     1: BLK1/RAM123 enable
 b2     unused (always 0)
 b1     unused (always 0)
 b0     A21
```

17.14.5.5 4 - UltiMem

Size 8MiB or 16MiB

```
         00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
         -----------------------------------------------  ----------------
   0000: 56 49 43 32 30 20 43 41 52 54 52 49 44 47 45 20  VIC20 CARTRIDGE
   0010: 00 00 00 40 02 00 00 04 00 00 00 00 00 00 00 00  ................
   0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
   0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
   0040: 43 48 49 50 00 00 20 10 00 00 00 00 a0 00 20 00  CHIP.. ....... .
   0050: 09 a0 09 a0 41 30 c3 c2 cd 78 a2 ff 9a d8 a9 7f  ....A0...x......
...
1008020: a7 00 a2 07 a3 74 a3 57 a1 55 a1 be a9 ea a0 da  .....t.W.U......
1008030: ff ea a0 d0 ff b6 a3 aa aa aa aa aa aa aa aa aa  ................
```

This cartridge has 512KiB or 1MiB of RAM and 8MiB or 16MiB of flash rom.

17.14.5.6 5 - Final Expansion

Size 512KiB (ROM) and 512KiB (RAM)

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
       -----------------------------------------------  ----------------
 0000: 56 49 43 32 30 20 43 41 52 54 52 49 44 47 45 20  VIC20 CARTRIDGE
 0010: 00 00 00 40 02 00 00 05 00 00 00 00 00 00 00 00  ................
 0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
 0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
 0040: 43 48 49 50 00 00 20 10 00 00 00 00 a0 00 20 00  CHIP.. ....... .
 0050: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
...
80420: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
80430: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
```

This cartridge has 512KiB Flash ROM and 512KiB RAM.

It has two registers mirrored every 4 Bytes in the while IO3 ($9c00-$9fff) range.

```
$9c02
    bit 7..5    mode
                000: Start Mode
                001: Flash-Write-Mode
                010: Super ROM Mode
                011: RAM/ROM Mode
                100: RAM 1 Mode
                101: Super RAM Mode
                110: RAM 2 Mode
    bit 4       block5 select
    bit 3       block3 select
    bit 2       block2 select
    bit 1       block1 select
    bit 0       block0 select
```

```
$9c03
    bit 7  1: register off
    bit 6  1: invert A14
    bit 5  1: invert A13
    bit 4  1: block5 off
    bit 3  1: block3 off
    bit 2  1: block2 off
    bit 1  1: block1 off
    bit 0  1: block0 off
```

17.14.5.7 6 - Rabbit tape turbo

Size 2KiB (ROM)

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F      ASCII
       -----------------------------------------------  ----------------
 0000: 56 49 43 32 30 20 43 41 52 54 52 49 44 47 45 20  VIC20 CARTRIDGE
 0010: 00 00 00 40 02 00 00 06 00 00 00 00 00 00 00 00  ................
 0020: 52 61 62 62 69 74 20 54 61 70 65 00 00 00 00 00  Rabbit Tape.....
 0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
 0040: 43 48 49 50 00 00 08 10 00 00 00 00 98 00 08 00  CHIP............
 0050: a9 4c 85 73 a9 12 85 74 a9 98 85 75 a2 00 20 f9  .L.s...t...u.. .
 0060: 9b 60 ea e6 7a d0 02 e6 7b 86 fb ba bd 01 01 c9  .‘..z...........
 0070: 8c d0 21 bd 02 01 c9 c4 d0 1a a5 7a d0 18 a5 7b  ..!........z....
 0080: c9 02 d0 12 a0 00 b1 7a c9 2a d0 0a 20 6c 98 a0  .......z.*.. l..
 0090: 00 98 91 7a a6 fb ad 8f 99 c9 4c d0 0d c6 7a a5  ...z......L...z.
...
 0830: aa aa aa aa aa aa aa aa aa aa aa aa aa aa aa aa  ................
 0840: aa aa aa aa aa aa aa aa aa aa aa aa aa aa aa fe  ................
```

This cartridge maps 2KiB ROM into the IO2 and IO3 area ($9800 - $9fff). It can be started by SYS38912.

17.14.5.8 7 - Super Expander

Size 4KiB (ROM) 3KiB (RAM)

```
       00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F      ASCII
       -----------------------------------------------  ----------------
 0000: 56 49 43 32 30 20 43 41 52 54 52 49 44 47 45 20  VIC20 CARTRIDGE
 0010: 00 00 00 40 02 00 00 07 00 00 00 00 00 00 00 00  ................
 0020: 53 75 70 65 72 20 45 78 70 61 6e 64 65 72 00 00  Super Expander..
 0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
 0040: 43 48 49 50 00 00 10 10 00 00 00 00 a0 00 10 00  CHIP............
 0050: 09 a0 a8 a0 41 30 c3 c2 cd 20 8d fd 20 52 fd 20  ....A0... .. R.
 0060: f9 fd 20 18 e5 ad 84 02 c9 21 b0 0a a0 10 a2 00  .. ......!......
 0070: 20 7b fe 4c 35 a0 a0 20 a2 00 20 8a fe a9 1e 8d   ..L5.. .. .....
 0080: 88 02 20 18 e5 a9 35 8d d8 02 20 f7 ad a9 04 8d  .. ...5... .....
 0090: d5 02 a9 01 8d b0 02 a9 03 8d b1 02 a9 00 8d b5  ................
...
 1030: 34 a7 4c 67 a8 4c 57 a8 4c fc a7 4c cc a8 4c 71  4.Lg.LW.L..L..Lq
```

```
1040: a9 4c d3 a9 4c 31 ab 4c a6 a8 4c 89 a8 4c 8b af  .L..L1.L..L..L..
```

This cartridge maps 4KiB ROM into block5 ($a000-$afff), and 3Kib RAM to RAM123 ($0400 - $0fff).

17.14.5.9 8 - Mikro Assembler

Size 8KiB (ROM) 3KiB (RAM)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F      ASCII
      -----------------------------------------------  ----------------
0000: 56 49 43 32 30 20 43 41 52 54 52 49 44 47 45 20  VIC20 CARTRIDGE
0010: 00 00 00 40 02 00 00 08 00 00 00 00 00 00 00 00  ...............
0020: 4d 69 6b 72 6f 20 41 73 73 65 6d 62 6c 65 72 00  Mikro Assembler.
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 10 10 00 00 00 00 60 00 10 00  CHIP........`...
0050: 01 0d 39 55 61 00 00 00 69 75 00 7d 00 8d 91 95  ..9Ua...iu......
0060: 00 a5 b5 d1 00 00 ed 00 00 00 f1 44 43 47 61 4e  ...........DCGaN
0070: 44 52 61 53 4c 5d 61 43 43 90 00 43 53 b0 00 45  DRaSL]aCC..CS..E
0080: 51 f0 00 49 54 68 61 4d 49 30 00 4e 45 d0 00 50  Q..IThaMI0.NE..P
0090: 4c 10 00 52 4b 00 00 56 43 50 00 56 53 70 00 59  L..RK..VCP.VSp.Y
...
2040: a7 69 aa 69 ad 69 b0 69 de 6a f3 6a 53 4d 20 20  .i.i.i.i.j.jSM
2050: 07 01 04 f9 7f 20 20 20 20 00 05 33 02 88 00 00  .....    ..3....
```

This cartridge maps 4KiB ROM into block3 and 5 ($6000-$6fff, $a000-$afff), and 3Kib RAM to RAM123 ($0400 - $0fff).

17.14.5.10 9 - Write Now

Size 8KiB (ROM)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F      ASCII
      -----------------------------------------------  ----------------
0000: 56 49 43 32 30 20 43 41 52 54 52 49 44 47 45 20  VIC20 CARTRIDGE
0010: 00 00 00 40 02 00 00 09 00 00 00 00 00 00 00 00  ...............
0020: 57 72 69 74 65 20 4e 6f 77 00 00 00 00 00 00 00  Write Now.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 20 10 00 00 00 00 a0 00 20 00  CHIP.. ....... .
0050: 00 a5 2c c5 37 a5 2d e5 38 90 03 4c 22 a9 20 af  ..,.7.-.8..L". .
0060: a3 a5 3b 85 26 a5 3c 85 27 a9 01 85 48 20 80 a4  ..;.&.<.'...H ..
0070: c8 a9 20 91 26 4c 3b a4 20 20 a7 20 3f a0 a9 f2  .. .&L;.....?...
0080: 85 52 a9 10 85 53 20 ac a0 20 63 a1 4c 2b a0 a9  .R...S .. c.L+..
0090: 10 85 71 a5 2a 85 26 a5 2b 85 27 38 a5 2c e9 01  ..q.*.&.+.'8.,..

...
2030: bd ed bf a2 00 81 28 e6 28 d0 02 e6 29 60 a0 20  ......(.(...)`..
2040: df 69 e9 5f 27 0b 43 48 45 4b 2e 49 54 82 43 4d  .i._'.CHEK.IT.CM
```

This cartridge maps 8KiB ROM into block5 ($a000-$afff), plus the last 2KiB of the ROM are mapped into IO ($9800-$9Bff)

17.14.5.11 10 - Minimon

Size 2KiB (ROM)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F      ASCII
      -----------------------------------------------  ----------------
0000: 56 49 43 32 30 20 43 41 52 54 52 49 44 47 45 20  VIC20 CARTRIDGE
0010: 00 00 00 40 02 00 00 0a 00 00 00 00 00 00 00 00  ................
0020: 4d 69 6e 69 6d 6f 6e 00 00 00 00 00 00 00 00 00  Minimon.........
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 08 10 00 00 00 00 98 00 08 00  CHIP............
0050: 20 a2 9f a2 00 8e 12 01 20 e2 9f 0d 4d 4f 4e 49   ....... ...MONI
0060: 54 4f 52 0d 00 90 14 d8 a2 05 68 9d 10 01 ca 10  TOR.......h.....
0070: f9 20 e2 9f 0d 42 52 45 41 4b 00 a9 c0 85 9d ba  . ...BREAK......
0080: 8e 16 01 a9 00 85 c1 85 c2 58 20 e2 9f 0d 20 20  .........X ...
0090: 50 43 20 20 53 52 20 41 43 20 58 52 20 59 52 20  PC  SR AC XR YR

...
0830: ff ff 68 85 22 68 85 23 e6 22 d0 02 e6 23 a0 00  ..h."h.#."...#..
0840: b1 22 f0 05 20 d2 ff 90 ef a5 23 48 a5 22 48 60  ."..  .....#H."H‘
```

This cartridge maps 2KiB ROM into the IO2 and IO3 area ($9800 - $9fff). It (the monitor) can be started by SYS38912.

The cartridge has two switches:

- PGM: when enabled, the memory in the socket can be written to (when it is SRAM or EEPROM)
- CART/MON: when in MON position, the ROM is mapped into IO. in CART position the IO is mapped transparent to the pass through port.

"Freeze" (a long press on the reset button on the original cartridge) will trigger a reset, and inhibit reads to BLK5 for ~300ms, to prevent a CBMA0 cart from starting.

17.14.6 PLUS4 Cartridge Specifics

CRT ID	Cartridge Type
0	generic cartridge
1	c264 magic cart
2	Plus4 multi cart
3	1MB Cartridge
4	"Speedy" freezer cartridge

For all Plus4 binary formats (non .crt) the convention is to store all banks of c1lo first, then all banks of c1hi, c2lo, c2hi.

17.14.6.1 0 - generic cartridge

Generic cartridges can use 4 different ROM spaces, which are c1lo, c1hi, c2lo, c2hi. most cartridges will use c1lo only, c2 is really uncommon. to indicate which chip chunk goes where, the bank number is used - bank 1 means c2.

32KiB C1 cartridge

Size 32KiB

Load address $8000-$FFFF (C1)

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
      -----------------------------------------------  ----------------
0000: 50 4c 55 53 34 20 43 41 52 54 52 49 44 47 45 20  PLUS4 CARTRIDGE
0010: 00 00 00 40 02 00 00 00 00 00 00 00 00 00 00 00  ................
0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
0050: 4c 47 80 4c f4 80 06 43 42 4d 53 59 53 31 35 32  LG.L...CBMSYS152
...
8040: ff 20 40 ff 20 40 ff 20 40 ff 20 40 ff 20 40 ff  . . . . . .
8050: 20 40 ff 20 40 ff 8d 3e ff 4c a4 f2 f6 ff b3 fc   . ..>.L......
```

17.14.6.2 1 - c264 magic cart

Size 128KiB/256KiB/512KiB/1MiB/2MiB (32/64/128 banks of 16KiB)

Load address $8000-$BFFF (C1LO)

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -----------------------------------------------  ----------------
  0000: 50 4c 55 53 34 20 43 41 52 54 52 49 44 47 45 20  PLUS4 CARTRIDGE
  0010: 00 00 00 40 02 00 00 01 00 00 00 00 00 00 00 00  ................
  0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
  0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
  0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
  0050: 4c 13 80 4c 13 80 01 43 42 4d 38 42 49 54 43 48  L..L...CBM8BITCH
...
200820: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
200830: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
```

This cartridge has one banking register at $fdfe.

17.14.6.3 2 - Plus4 multi cart

Size 2MiB/4MiB (64/128 banks of 32KiB)

Load address $8000-$FFFF (C1)

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -----------------------------------------------  ----------------
  0000: 50 4c 55 53 34 20 43 41 52 54 52 49 44 47 45 20  PLUS4 CARTRIDGE
  0010: 00 00 00 40 02 00 00 02 00 00 00 00 00 00 00 00  ................
  0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
  0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
  0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
  0050: 4c 6b 80 4c 6b 80 01 43 42 4d 0a 0a ee f1 00 20  Lk.Lk..CBM.....
...
401020: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
401030: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff  ................
```

This cartridge has one banking register at $fda0.

17.14.6.4 3 - 1MB Cartridge

Size	1MiB (64 banks of 16KiB)
Load address	$8000-$BFFF (C1LO)

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -----------------------------------------------  ----------------
  0000: 50 4c 55 53 34 20 43 41 52 54 52 49 44 47 45 20  PLUS4 CARTRIDGE
  0010: 00 00 00 40 02 00 00 03 00 00 00 00 00 00 00 00  ................
  0020: 56 49 43 45 20 43 41 52 54 00 00 00 00 00 00 00  VICE CART.......
  0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
  0040: 43 48 49 50 00 00 40 10 00 00 00 00 80 00 40 00  CHIP..........
  0050: 4c 4f 80 00 00 00 0c 43 42 4d 53 59 53 31 35 32  LO.....CBMSYS152
...
100420: 2f ef ee da db 20 20 20 20 20 20 da db 2f d8 2f  /............././
100430: 2f ef 2f da db 20 20 20 20 20 20 da db 2f 2f 2f  /./.........///
```

This cartridge has one banking register at $fe00.

17.14.6.5 4 - Speedy

Size	8KiB
Load address	$8000-$BFFF (mirrored twice) (C1LO)

```
        00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F       ASCII
        -----------------------------------------------  ----------------
  0000: 50 4c 55 53 34 20 43 41 52 54 52 49 44 47 45 20  PLUS4 CARTRIDGE
  0010: 00 00 00 40 02 00 00 04 00 00 00 00 00 00 00 00  ................
  0020: 53 70 65 65 64 79 00 00 00 00 00 00 00 00 00 00  Speedy..........
  0030: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00  ................
  0040: 43 48 49 50 00 00 20 10 00 00 00 00 80 00 20 00  CHIP.. ....... .
  0050: 4c 0a 80 42 4f 50 02 43 42 4d a2 00 bd 26 80 9d  L..BOP.CBM...&..
...
  2030: 20 20 20 20 20 20 20 20 00 84 00 a4 00 06 00 ef  ................
  2040: 12 ff fd ff fd ff ff ff ff ff df ff ff ff ff ff  ................
```

17.14.7 CBM2 Cartridge Specifics

CRT ID	Cartridge Type
0	generic cartridge

17.14.7.1 0 - generic cartridge

Generic cartridges can use 4 different ROM spaces, one 4k block at $1000, and 3 8k blocks at $2000, $4000 and $6000 respectively.

17.15 The PSID image format for ripped SID tunes

This section describes the SID file format used for SID tunes in the HVSC (High Voltage SID Collection - http://hvsc.de). It is based mostly on Michael Schwendt's document that describes the file format and the PSID v2NG extensions described by Simon White and Dag Lem and was further extended by Wilfred Bos (PSID v3/v4, RSID v3/v4) and LaLa

(assembled most of the following text) and last not least tweaked a bit by Groepaz to fit into the VICE documentation.

The original documentation maintained by the HVSC team can be found here: `http://www.hvsc.de/download/C64Music/DOCUMENTS/SID_file_format.txt`

SID files use the .sid file extension.

Since PSID v2 is simply an extension of PSID v1, PSID v2NG is an extension of PSID v2, RSID is a restricted version of PSID v2NG, PSID v3/v4 and RSID v3/v4 are extentions of PSID v2NG and RSID v2, all of the formats are discussed together below. RSID in specific is discussed in detail under the 'magicID' field description.

The information presented here targets programmers or other people with reasonable background. It is not suitable for newbies who have never before used a machine code monitor, a disassembler, or a hexadecimal editor.

17.15.1 The SID file header v1

The detailed structure of the SID header looks like the following. Header offsets are in hexadecimal notation. Other integer values are decimal unless explicitly marked otherwise. Any stored integer values are in big-endian format:

`+00 magicID: ‘‘PSID’’ or ‘‘RSID’’`

This is a four byte long ASCII character string containing the value 0x50534944 or 0x52534944. 'RSID' (Real SID) denotes that the file strictly requires a true Commodore-64 environment to run properly. 'PSID' files will generally run trouble-free on older PlaySID and libsidplay1 based emulators, too.

Some words about the Real C64 SID file format (RSID):

The RSID format was designed to contain tunes that are not PlaySID compatible, but strictly require a real C64 environment to run. Tunes that are multi-speed and/or contain samples and/or use additional interrupt sources or do busy looping will cause older SID emulators to lock up or play very wrongly (if at all).

By using the name RSID for such rips all existing SID emulators will reject these tunes safely until they can be upgraded to cope with the additional requirements.

Due to the nature of these tunes, every effort must be made to make sure they are directly runnable on an actual C64 computer. As such the tunes will only be presented with the default C64 power-on environment and expected to configure and use all hardware appropriately.

RSID is based on PSIDv2NG with the following modifications:

- magicID = RSID
- version = 2, 3 and 4 only
- loadAddress = 0 (reserved)
- playAddress = 0 (reserved)
- speed = 0 (reserved)
- psidSpecific flag is called C64BASIC flag

The above fields MUST be checked and if any differ from the above then the tune MUST be rejected. The definitions above will force tunes to contain proper hardware configuration code and install valid interrupt handlers.

See section "The SID file environment" below for the default C64 power-on environment for each SID file format.

`+04 WORD version`

Available version number can be 0x0001, 0x0002, 0x0003 or 0x0004. Headers of version 2, 3 and 4 provide additional fields. RSID and PSID v2NG files must have 0x0002, 0x0003 or 0x0004 here.

0x004E denotes an unofficial extension defined by Juergen Wothke for use in "Deepsid".

`+06 WORD dataOffset`

This is the offset from the start of the file to the C64 binary data area. Because of the fixed size of the header, this is either 0x0076 for version 1 and 0x007C for version 2, 3 and 4.

The 0x4e extension actually uses a variable header length, and requires this offset to be correct (and used). Eg. for a 1-SID file the offset would be 0x7C, for a 2-SID file the offset would be 0x7E, etc.

`+08 WORD loadAddress`

The C64 memory location where to put the C64 data. 0 means the data are in original C64 binary file format, i.e. the first two bytes of the data contain the little-endian load address (low byte, high byte). This must always be true for RSID files. Furthermore, the actual load address must NOT be less than $07E8 in RSID files.

You must be absolutely sure what to enter here. There is no way to detect automatically whether the first two bytes in a C64 data file are meant to be a load address or some arbitrary bytes of code or data. Unless your C64 file is not a normal binary file and thus has no load address in front, you need not enter anything else than 0 here. The SID tune will not play if you specify a load address which is present in the C64 file already.

Normal C64 binary data files have a load address in their first two bytes, so they can be loaded to a pre-defined destination address by executing LOAD"FILE",8,1, for instance. If a load address is explicitly specified in the sidtune info file, some sidtune converters and utilities conjecture that the C64 data don't have a load address in their first two bytes. Hence, the explicit load address from the info file is moved in front of the C64 data to create a valid C64 binary file which can be easily loaded on a C64, too. If that C64 file were to be saved, it would contain two superfluous data bytes at offset 2 if an original load address had been in the first two bytes of the old file. This process of adding a duplicate load address can be repeated. The file loader strips off the first two bytes (the used load address) and puts the rest of the file contents (including the now obsolete load address at file offset 2) into memory. If the new load address is the same than the old one the two added bytes cause the whole data to be displaced by two bytes, which most likely results in malfunctioning code. Also, superfluous bytes in memory then can confuse disassemblers which start at the beginning of the file or memory buffer.

`+0A WORD initAddress`

The start address of the machine code subroutine that initializes a song, accepting the contents of the 8-bit 6510 Accumulator as the song number parameter. 0 means the address is equal to the effective load address.

In RSID files initAddress must never point to a ROM area ($A000-$BFFF or $D000-$FFFF) or be lower than $07E8. Also, if the C64 BASIC flag is set, initAddress must be 0.

`+0C WORD playAddress`

The start address of the machine code subroutine that can be called frequently to produce a continuous sound. 0 means the initialization subroutine is expected to install an interrupt handler, which then calls the music player at some place. This must always be true for RSID files.

```
+0E    WORD songs
```

The number of songs (or sound effects) that can be initialized by calling the init address. The minimum is 1. The maximum is 256.

```
+10    WORD startSong
```

The song number to be played by default. This value is optional. It often specifies the first song you would hear upon starting the program is has been taken from. It has a default of 1.

```
+12    LONGWORD speed
```

This is a 32 bit big endian number.

For version 1 and 2 and for version 2NG, 3 and 4 with PlaySID specific flag (+76) set, the 'speed' should be handled as follows:

Each bit in 'speed' specifies the speed for the corresponding tune number, i.e. bit 0 specifies the speed for tune 1. If there are more than 32 tunes, the speed specified for tune 32 is the same as tune 1, for tune 33 it is the same as tune 2, etc.

For version 2NG, 3 and 4 with PlaySID specific flag (+76) cleared, the 'speed' should be handled as follows:

Each bit in 'speed' specifies the speed for the corresponding tune number, i.e. bit 0 specifies the speed for tune 1. If there are more than 32 tunes, the speed specified for tune 32 is also used for all higher numbered tunes.

For all version counts:

- A 0 bit specifies vertical blank interrupt (50Hz PAL, 60Hz NTSC), and a 1 bit specifies CIA 1 timer interrupt (default 60Hz).
- Surplus bits in 'speed' should be set to 0.
- For RSID files 'speed' must always be set to 0.

Note that if 'play' = 0, the bits in 'speed' should still be set for backwards compatibility with older SID players. New SID players running in a C64 environment will ignore the speed bits in this case.

WARNING: This field does not work in PlaySID for Amiga like it was intended, therefore the above is a redefinition of the original 'speed' field in SID v2NG! See also the 'clock' (video standard) field described below for 'flags'.

```
+16    ''<name>''
+36    ''<author>''
+56    ''<released>'' (once known as ''<copyright>'')
```

These are 32 byte long ASCII character strings. Upon evaluating the header, these fields may hold a character string of 32 bytes which is not zero terminated. For less than 32 characters the string should be zero terminated. The maximum number of available free characters is 32.

```
+76    <data>
```

Version 1 of the SID header is complete at this point. The binary C64 data starts here.

17.15.2 The SID file header v2, v3 and v4

Version 2, 3 and 4 of the header incorporates the v1 header fields and provides additional fields. Some of these are actually v2NG, v3 or v4 specific - those are noted below.

```
+76    WORD flags
```

This is a 16 bit big endian number containing the following bitfields:

- Bit 0 specifies format of the binary data (musPlayer): 0 = built-in music player, 1 = Compute!'s Sidplayer MUS data, music player must be merged.

If this bit is set, the appended binary data are in Compute!'s Sidplayer MUS format, and does not contain a built-in music player. An external player machine code must be merged to replay such a sidtune.

- Bit 1 specifies whether the tune is PlaySID specific, e.g. uses PlaySID samples (psidSpecific): 0 = C64 compatible, 1 = PlaySID specific (PSID v2NG, v3, v4) 1 = C64 BASIC flag (RSID)

This is a v2NG and RSID specific field.

PlaySID samples were invented to facilitate playback of C64 volume register samples with the original Amiga PlaySID software. PlaySID samples made samples a reality on slow Amiga hardware with a player that was updated only once a frame.

Unfortunately, converting C64 volume samples to PlaySID samples means that they can no longer be played on a C64, and furthermore the conversion might potentially break the non-sample part of a tune if the timing between writes to the SID registers is at all altered. This follows from the ADSR bugs in the SID chip.

Today, the speed of common hardware and the sophistication of the SID players is such that there is little need for PlaySID samples. However, with all the PlaySID sample PSIDs in existence there's a need to differentiate between SID files containing only original C64 code and PSID files containing PlaySID samples or having other PlaySID specific issues. As stated above, bit 1 in 'flags' is reserved for this purpose.

Since RSID files do not have the need for PlaySID samples, this flag is used for a different purpose: tunes that include a BASIC executable portion will be played (with the BASIC portion executed) if the C64 BASIC flag is set. At the same time, initAddress must be 0.

- Bits 2-3 specify the video standard (clock): 00 = Unknown, 01 = PAL, 10 = NTSC, 11 = PAL and NTSC.

This is a v2NG specific field.

As can be seen from the 'speed' field, it is not possible to specify NTSC C64 playback. This is unfortunate, since the different clock speeds means that a tune written for the NTSC C64 will be slightly detuned if played back on a PAL C64. Furthermore, NTSC C64 tunes driven by a vertical blank interrupt have to be converted to use the CIA 1 timer to fit into this scheme. This can cause severe problems, as the NTSC refresh rate is once every 17045 cycles, while the CIA 1 timer A is latched with 17095 cycles. Apart from the difference in timing itself, the SID ADSR bugs can actually break the tune.

The 'clock' (video standard) field was introduced to circumvent this problem.

- Bits 4-5 specify the SID version (sidModel): 00 = Unknown, 01 = MOS6581, 10 = MOS8580, 11 = MOS6581 and MOS8580.

This is a v2NG specific field.

- Bits 6-7 specify the SID version (sidModel) of the second SID: 00 = Unknown, 01 = MOS6581, 10 = MOS8580, 11 = MOS6581 and MOS8580.

If bits 6-7 are set to Unknown then the second SID will be set to the same SID model as the first SID.

This is a v3 specific field.

- Bits 8-9 specify the SID version (sidModel) of the third SID: 00 = Unknown, 01 = MOS6581, 10 = MOS8580, 11 = MOS6581 and MOS8580.

If bits 8-9 are set to Unknown then the third SID will be set to the same SID model as the first SID.

This is a v4 specific field.

The MOS6581 and the MOS8580 have three notable differences. First, combined waveforms are generally louder on a MOS8580, to the extent that some combinations that are clearly audible on a MOS8580 are completely silent on a MOS6581. Second, the internal DC levels in the MOS8580 are so small that software or hardware tricks must be used to play volume samples. Third, the MOS8580 analog filter has totally different characteristics from the MOS6581 analog filter.

To ensure that music specifically written for one of the two SID versions can be played back correctly, bits 4-9 in 'flags' are used as stated above.

- Bits 10-15 are reserved and should be set to 0.

The 0x4e extension slightly changes the valid bits:

- Bit 0 specifies format of the binary data (musPlayer): 0 = built-in music player, 1 = Compute!'s Sidplayer MUS data, music player must be merged.
- Bit 1 specifies whether the tune is PlaySID specific, e.g. uses PlaySID samples (psidSpecific): 0 = C64 compatible, 1 = PlaySID specific (PSID v2NG, v3, v4) 1 = C64 BASIC flag (RSID)
- Bits 2-3 specify the video standard (clock): 00 = Unknown, 01 = PAL, 10 = NTSC, 11 = PAL and NTSC.
- Bits 4-5 specify the SID version (sidModel): 00 = Unknown, 01 = MOS6581, 10 = MOS8580, 11 = MOS6581 and MOS8580.
- 6, output channel for main $d400 SID - aka SID #0 (default: 0)
- Bits 7-15 are reserved and should be set to 0.

```
+78    BYTE startPage (relocStartPage)
```

This is a v2NG specific field.

This is an 8 bit number. If 'startPage' is 0, the SID file is clean, i.e. it does not write outside its data range within the driver ranges. In this case the largest free memory range can be determined from the start address and the data length of the SID binary data. If 'startPage' is 0xFF, there is not even a single free page, and driver relocation is impossible. Otherwise, 'startPage' specifies the start page of the single largest free memory range within the driver ranges. For example, if 'startPage' is 0x1E, this free memory range starts at $1E00.

```
+79    BYTE pageLength (relocPages)
```

This is a v2NG specific field.

This is an 8 bit number indicating the number of free pages after 'startPage'. If 'startPage' is not 0 or 0xFF, 'pageLength' is set to the number of free pages starting at 'startPage'. If 'startPage' is 0 or 0xFF, 'pageLength' must be set to 0.

The relocation range indicated by 'startPage' and 'pageLength' should never overlap or encompass the load range of the C64 data. For RSID files, the relocation range should also not overlap or encompass any of the ROM areas ($A000-$BFFF and $D000-$FFFF) or the reserved memory area ($0000-$03FF).

`+7A BYTE secondSIDAddress`

This is a v3 specific field. For v2NG, it should be set to 0.

This is an 8 bit number indicating the address of the second SID. It specifies the middle part of the address, $Dxx0, starting from value $42 for $D420 to $FE for $DFE0). Only even values are valid. Ranges $00-$41 ($D000-$D410) and $80-$DF ($D800-$DDF0) are invalid. Any invalid value means that no second SID is used, like $00.

`+7B BYTE thirdSIDAddress`

This is a v4 specific field. For v2NG and v3, it should be set to 0.

This is an 8 bit number indicating the address of the third SID. It specifies the middle part of the address, $Dxx0, starting from value $42 for $D420 to $FE for $DFE0). Only even values are valid. Ranges $00-$41 ($D000-$D410) and $80-$DF ($D800-$DDF0) are invalid. Any invalid value means that no third SID is used, like $00. The address of the third SID cannot be the same as the second SID.

The 0x4e extension adds a variable amount of flag words (one per extra SID) instead of just using the one reserved word left:

`+7A WORD nSidFlags`

For each additional SID there is one additional nSidFlags WORD. A 0-WORD must be used as an "end" marker, i.e. with only 1 SID the nSidFlags at +7A would be 0. (This corresponds to the reserved 0 WORD that old versions require at 0x7A.)

- Bits 0-3, unused
- Bits 4-5, SID version (same as respective flag for main SID - see +76): 00 = Unknown: defaults to respective main SID setting
- Bit 6, output channel (same position as respective flag for main SID - see +76)
- Bit 7, unused
- Bits 8-15, address of SID chip (same "2 center nibbles" format as used in original HVSC formats, e.g. "D420" would be represented as 0x42)

`+7C <data>`

Version 2, 3 and 4 of the SID header ends here. This offset is the start of the binary C64 data. See also 'loadAddress' for what the first 2 bytes of 'data' might indicate.

17.15.3 The SID file environment

Before the data of a SID file is loaded in memory of a C64, certain addresses and chips must be initialized in order to play the SID tune correctly.

For RSID and PSID files the following address must be set:

$02A6	depending on the PAL/NTSC flag in the SID file header, it is set to 0x01 for PAL and set to 0x00 for NTSC.

The default C64 environment for PSID files is as follows:

VIC	IRQ set to any raster value less than 0x100. Enabled when speed flag is 0, otherwise disabled.
CIA 1 timer A	set to 60Hz (0x4025 for PAL and 0x4295 for NTSC) with the counter running. IRQs active when speed flag is 1, otherwise IRQs are disabled.
Other timers	disabled and loaded with 0xFFFF.

When the init and play addresses are called the bank register value must be written for every call and the value is calculated as follows:

```
if   address < $A000  -> 0x37 // I/O, Kernal-ROM, Basic-ROM
else address < $D000  -> 0x36 // I/O, Kernal-ROM
else address >= $E000 -> 0x35 // I/O only
else                  -> 0x34 // RAM only
```

The default C64 environment for RSID files is as follows:

VIC	IRQ set to raster 0x137, but not enabled.
CIA 1 timer A	set to 60Hz (0x4025 for PAL and 0x4295 for NTSC) with the counter running and IRQs active.
Other timers	disabled and loaded with 0xFFFF.
Bank register	0x37

- A side effect of the bank register is that init MUST NOT be located under a ROM/IO memory area (addresses $A000-$BFFF and $D000-$FFFF) or outside the load image.
- Since every effort needs to be made to run the tune on a real C64 the load address of the image MUST NOT be set lower than $07E8.
- If the C64 BASIC flag is set, the value at $030C must be set with the song number to be played (0x00 for song 1).

17.16 The Swaplist (.vfl, .lst) file format

"Swaplist" or "Fliplist" files contain filenames of (mostly) disk images that belong to a program (game/demo) so the can be easily swapped/changed by a button press. They were first introduced by VICE, and have since then been adopted and expanded by other devices. This is an attempt to unify the efforts and create a standard for a format that will work with all OSs and across all devices that can use these files, so eg a SD-Card containing a software collection will work with all of them.

17.16.1 general

The format itself is pretty straightforward and almost trivial: it is simply a text file containing one filename per line.

- Each line contains a filename. Depending on the device or emulator, this does not necessarily have to be a disk image (see below). Filenames can contain a full (absolute) or relative (to the swaplist file) path, or no path at all.

  Note that path notation is device- and/or OS specific and not portable - for that reason it is probably a good idea to not put pathes into these files unless absolutely necessary.

  What happens when a (non comment) line contains an invalid/non existing file name is undefined and implementation specific (see below)

- Blank lines are ignored, and parsing is tolerant of varying line?endings (any occurrences and/or combination of 0x0a and/or 0x0d constitutes a line-ending)
- Any line starting with a semicolon (;) is a comment and will be skipped. Any device specific extensions should be implemented in comment lines, so other devices will always skip them.

17.16.2 extensions

Some devices introduced custom extensions on the format, which are considered completely optional. Most notable is the #PETSCII tag introduced by sd2iec, which parsers that don't support it should simply ignore.

Details on the various devices/emulators that support swaplists follow:

17.16.2.1 VICE

- Until 3.5, VICE used to write "# Vice fliplist file" to the first line of the file. To make the created files more universal, this was changed to use the new comment line delimiter in 3.5 r40450, ie "; Vice fliplist file"
- A line containing ";UNIT X" indicates that the following files belong to the drive X (This used to be "UNIT X" before 3.5 r40481). When the swaplist contains only files for one drive, this marker will not be created.
- File names that are not readable (or do not exist) will be ignored/skipped
- the sd2iec #PETSCII marker is recognized since VICE 3.5 r40450

After 3.5 r40450 VICE will still accept (read) files in the old format, but only create files in the new, more compatible format.

17.16.2.2 sd2iec

- The file names are parsed in the same way as the CD command, so you can include a path to the image if desired. Due to the way this feature is implemented you are not limited to a swap list containing just D64 images, M2I and even FAT directories will work too.
- a file name including path can be max. 255 characters long
- there can be max. 255 files in a swaplist
- By default sd2iec assumes the swap file is encoded in ASCII. If the the first line of the file matches "#PETSCII" (in hex: $23 $50 $45 $54 $53 $43 $49 $49), filenames are assumed to be encoded in PETSCII instead.
- When sd2iec finds an invalid/non existing file name, it will stop and indicate an error by blinking the LED

The comment delimiter (;) is recognized in firmware since April 22nd, 2021

17.16.2.3 Chameleon

- as of now, filenames must _not_ contain a path component
- a file name can be max. 255 characters long
- since the files in the list are not "swapped" but immediately mounted, there can be max. 4 files in the list

- d64 and g64 files are recognized, files with other extensions are skipped
- the sd2iec #PETSCII marker is recognized

17.16.2.4 pi1541

(some of the following needs more explanation and/or will perhaps change in the future):

- Path can be relative or not and is in the format; [drive#:][/]directory/file
- Comments are in the C programming style of // and /* */
- All files will be ignored if they don't have an extension of the currently supported disk image (d64, g64, nib, nbz or t64). If the disk image fails to load then it will be ignored.
- When a file is found where the file name does not end with a supported disk extension the file name will be compared to the list of ROM file names that have been specified inside of the Pi1541's options.txt file and if a match is found then that ROM will be used for that LST selection. If more than one has been specified inside the LST file then the last one encountered will be used.

17.16.3 Examples

example: barebones minimal swaplist

```
FOO.D64
BAR.D64
BAZ.D64
```

example: simple VICE fliplist

```
; Vice fliplist file
FOO.D64
BAR.D64
BAZ.D64
```

example: VICE fliplist for multiple drives + comments

```
; Vice fliplist file
;UNIT 8
FOO.D64
BAR.D64
BAZ.D64
;UNIT 9
FUU.D64
BOR.D64
BOZ.D64
; don't forget this one
;UNIT 10
BUZ.D64
```

example: sd2iec style pathes + comment

```
//NEATGAME/:DISK1A.D64
//NEATGAME/:DISK1B.D64
//NEATGAME/:DISK2A.D64
//NEATGAME/:DISK2B.D64
; save disk in separate directory for easier backup
//SAVEDISKS/:NEATGAME.D64
```

18 Acknowledgments

VICE derives from X64, the first Commodore 64 emulator for the X Window System. Here is an informal list of the people who were mostly involved in the development of X64 and VICE:

The VICE core team:

- **Pottendo** Fixed WiC64 support. Most of the former contributions (pre V3.x) have been taken over and/or replaced by other's contributions. Added FunMP3 userport cartdridge support.
- **Marco van den Heuvel** Translated the UI to Dutch. Made the internationalization support for the Win32 and Amiga ports. Wrote the GEO-RAM and RamCart cartridge code. Wrote the c64 +60K, +256K and 256K memory expansions code. Wrote the pet REU code. Wrote the plus4 memory expansions code. Made the ethernet support for the DOS port. Maintains the QNX 4.x, QNX 6.x, Solaris, Openserver, Unixware, Minix 3.x, Amiga, Syllable and OS/2 binary ports. Maintains the Win64 and Open Watcom project files. Maintains the SDL port(s). Added new .crt support. Added new screenshot formats. Added new sound recording support. Added SID cartridge support for xpet, xplus4 and xvic. Improved the MMC64 emulation. Added 2 MHz mode and banks 2/3 support for x128. Added the various userport joystick emulations. Added text copy and paste support to the Amiga and BeOS ports. Added DQBB and ISEPIC cartridge support. Added SFX Sound Sampler and SFX Sound Expander support. Added PCI support to the Amiga and DOS ports. Rewrote the sound system into a modular one, added always mono and always stereo support for the sound output. Added the RTC system. Added digiblaster support. Added 3rd SID support. Added the 6309 CPU emulation. Added the 65(S)C02 emulation. Added the 65816 emulation. Added native screenshot (koala/doodle) support. Added 6502/6510/8500/8502 cpu port unused bit fading. Added vice.chm (windows), vice.guide (amiga), vice.hlp (windows), vice.inf (os/2), vice.pdf (generic) and vice.txt documentation generation. Added Android port based on 'AnVICE 1.0.5'. Improved/fixed the 'in-source' FFMPEG support and fixed it for msvc7.0 and up. Generalized the 'RS232 net' support to be able to be used on any network supporting arch. Started the SDL2 port. Added generic sampler input support. Added new joyport system and converted the joystick, mouse and lightpen code to use the new system. Added joyport attached rtc (bbrtc). Added joyport attached cardkey keypad. Added joyport attached coplin keypad. Added joyport attached atari cx21 keypad. Added joyport attached atari cx85 keypad. Added joyport attached paperclip 64 dongle. Added joyport attached rushware keypad. Added joyport attached 2/4 bit samplers. Added vic20 I/O-2 and I/O-3 RAM support. Added the c64 cp/m (z80) cartridge. Added IDE64 digimax short bus device emulation. Added tapeport system and the tapelog, cp-clockf83, tape-sense dongle, and (not yet working) dtl basic dongle devices. Added easy calc result cartridge emulation. Added hardware SID I/O access system which allows CW3, HardSID, ParSID and SSI2001 to work on AmigaOS, BeOS, DOS, SDL, *nix and windows with and without device drivers. Added clockport system for mmc64, mmcreplay, retroreplay and ide64. Added rrnet clockport device. Added mp3@64 clockport device. Added userport snes pad (petscii) support. Extended the joyport system to be able to handle all snes pad buttons. Extended the joyport system to be able to handle 8-player joystick

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- **Daniel Kahlin** Worked on DTV VIC emulation, palette, DTV SID support in resid, better DMA/Blitter support and did lots of refactoring. Added new monitor commands and features. Improved the VIC emulation for xvic. Made MIDI driver code for Win32. Rewrote the xvic cartridge system. Added Mega-Cart and Final Expansion V3.2 support to xvic. Wrote large parts of the new VIC-II emulation used in x64sc, especially the dot clock domain emulation. Wrote many test programs for hardware analysis.
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- **Spiro Trikaliotis** Copyright © 2000-2011 Wrote the Win32 console implementation for the built-in monitor, corrected some REU related bugs, improved the CIA emulation, added com-port CIA support to the Win32 port, added text copy and paste support

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Internationalization Team:

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- **Daniel Kahlin**

  Groepaz

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  Marco van den Heuvel

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- **LouD** Added USBSID-Pico hardware SID support.

(We hope we have not forgotten anybody; if you think we have, please tell us.)

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20 Contact information

20.1 VICE home page

You can find the latest news about VICE at the official VICE home page:

`http://vice-emu.sourceforge.net`

VICE has moved its source repository to public services provided by SourceForge. You can find it at

`http://sf.net/projects/vice-emu.`

We would like to thank the SourceForge staff for that help.

If you are going to report a bug, please check those pages *first*; it is possible that the problem you encountered has already been fixed with a newer version.

20.2 How to send feedback

Before contacting us, have a look at this manual and see if your question is answered there. Keep in mind that we work on VICE in our spare-time, so the more time we don't need to answer the same questions over and over again, the more time we have to improve the emulation itself. On the other hand, that does not mean that you should not contact us, especially if you find bugs or have suggestions which might improve the emulation.

Bug reports, suggestions, support requests should be directed to the SourceForge trackers at

- `https://sourceforge.net/p/vice-emu/bugs/`.

This way, you, the users, and we, the developers, can track what has been reported and what has been already fixed. Ideally, also sent the report to the mailing address of the Vice team at

- VICE Mailing List (`vice-emu-mail@lists.sourceforge.net`) for all general questions, bug reports, suggestions.

You can also contact (some of) us on IRC, at #vice-dev on libera.chat.

It's always nice to receive feedback and/or bugreports about VICE, but please read these few notes before sending mail to anybody in the team.

- Please put the word '`VICE`' *in all capitals* in your subject line (e.g., '`VICE fails to run game XXX`'). This helps mail splitting and reduces chances that your message is unintentionally deleted, forgotten or lost.
- Please don't send any HTML mail (we really hate that!). Be patient when your mail does not show up on the mailing list - it is moderated manually. Once your mail shows up you should be approved and be able to send mail without further delay.
- Please don't send *any* binaries without asking first.
- Please read the following documents carefully before reporting a bug or a problem you cannot solve:
  - the VICE documentation (you are reading it!);
  - the VICE FAQ (it is available on the Internet, and reachable from the VICE home page: `http://vice-emu.sourceforge.net`);

- When you report a bug, please try to be as accurate as possible and describe how it can be reproduced to the very detail. You should also tell us what machine you are running on, what operating system you are using as well as the version of it.
- Please don't ask us how to transfer original C64 disk or tapes to your PC; this has been asked a gazillion times through email. To transfer disks, you can use the Star Commander (`http://sta.c64.org/sc.html`) on DOS, and OpenCBM (`http://www.trikaliotis.net/opencbm`) on Windows and Linux. And no, you cannot read C64 disks with your old 5"1/4 PC drive (unless investing in a disk drive controller that can do this).
- Please don't ask us where to find games for the emulator on the Internet. It should be very easy to find them using your favourite search engine.
- Please don't ask us when the next version will be out, because we really don't know.
- Please write in English.

In any case, we would be *really* glad to receive your comments about VICE. We cannot always answer all the email, but we surely read all of it.

Thanks!

20.3 How to contribute

If you want to make a major contribution, please *ask* first. It has already happened a couple of times that somebody started working at something that had already been done but not released to the public yet, and we really do *not* want anybody to waste time.

If you are going to make a patch, please make sure the patch is relative to the very latest version, and provide us with the following:

- Make sure you are giving us a diff against the latest Subversion trunk version of VICE. Get it with the command:

  `'svn checkout svn://svn.code.sf.net/p/vice-emu/code/trunk vice-src'`
- send a unified diff file against the trunk version of VICE (see above bullet point) by using the command: '`svn diff`' inside of the SVN workspace you checked out before.
- If you cannot use SVN for one or the other reason, send a unified diff file containing all the changes you have made '`diff -u`'; please don't use plain '`diff`'), as it adds much work for us to get it working;

Preferably post your patch on the tracker: `https://sourceforge.net/p/vice-emu/patches/`.

This is very important, and makes adding patches much smoother and safer.

People willing to port VICE to other platforms are always welcome. But notice from experience it will take at least a full year of continuous work to write a well working and stable port.

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Version 2, June 1991

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