



Parallella-16 Prototype System

Memory Map

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1. Parallella-16 SDK Memory Map

Table 1 shows the default memory map of the Parallella-16 Epiphany subsystem. The SDK Debug port number is used to connect to the correct TCP/IP port on the e-server from the e-gdb debug client using the procedure specified in the Epiphany SDK Reference document's debugger chapter.

Table 1: Memory Map for Parallella-16

Core Number	Start Address	End Address	Size	Epiphany SDK Debug Port Number
(32, 8)	80800000	80807FFF	32KB	51000
(32, 9)	80900000	80907FFF	32KB	51001
(32,10)	80A00000	80A07FFF	32KB	51002
(32,11)	80B00000	80B07FFF	32KB	51003
(33, 8)	84800000	84807FFF	32KB	51004
(33, 9)	84900000	84907FFF	32KB	51005
(33,10)	84A00000	84A07FFF	32KB	51006
(33,11)	84B00000	84B07FFF	32KB	51007
(34, 8)	88800000	88807FFF	32KB	51008
(34, 9)	88900000	88907FFF	32KB	51009
(34,10)	88A00000	88A07FFF	32KB	51010
(34,11)	88B00000	88B07FFF	32KB	51011
(35, 8)	8C800000	8C807FFF	32KB	51012
(35, 9)	8C900000	8C907FFF	32KB	51013
(35,10)	8CA00000	8CA07FFF	32KB	51014
(35,11)	8CB00000	8CB07FFF	32KB	51015
DRAM (host)	1E000000	1FFFFFFFFF	32MB	n/a
DRAM (device)	8E000000	8FFFFFFFFF	32MB	n/a

(*) The physical DRAM range is 0x1e000000÷0x1fffffffff. This range is aliased to 0x8e000000÷0x8fffffffff as seen from the Epiphany subsystem side.